
ECE323: Electronics II T/TR 12:00 - 1:20pm, Room: KEC 1003

Instructor: Pavan Kumar Hanumolu, hanumolu@eecs.oregonstate.edu
Office: KEC 4097, Ph: 737-2178
Office hours: Wednesday 5pm-6pm (or by appointment)
Textbook: Sedra and Smith, *Microelectronic Circuits*, Oxford Press, 5th Edition
Prerequisites: ECE322
Course website: <http://web.engr.oregonstate.edu/~hanumolu/ece323.htm>
TAs: Bangda Yang, yangba@onid.orst.edu
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TA Office hours: TBA

Grading:

Lab	25%
Homework	10%
Midterm I (Oct 23, in class)	20%
Midterm II (Nov 13, in class)	20%
Final (Dec 12, 9:30am)	25%

Tentative Course Outline

Date	Topic	Reading
Sep 29	Course overview, ECE322 Review	4.2,4.3,5.4,5.5
Oct 6	Transistor large/small-signal models	4.6, 5.6
Oct 13	Single stage amplifiers	4.7, 5.7
Oct 20	Bode plots	Class notes
Oct 27	Frequency response	4.8, 5.8
Nov 3	Frequency response	4.9, 5.9, 6.5, 6.6
Nov 10	Feedback	8.1, 8.2, 8.7
Nov 17	Feedback stability	8.8, 8.9, 8.10, 8.11
Nov 24	Oscillators	13.1, 13.4, 13.5
Dec 1	Digital logic	1.7, 4.10, 5.10, 10.1

Homework:

- (a) Most of the homework assignments will be custom-designed problems relating to the topics covered in the class.
- (b) Each assignment is due at the beginning of the Thursday class. No late homework will be accepted.
- (c) Homework will not be graded in detail, but rather on a scale of 10 with 10 points awarded for mostly correct, 5 points for partially correct solutions, and 0 points for little or no effort.

Tests/Final exam:

- (a) The tests will be closed book. One page of notes will be allowed.
- (b) Final comprehensive exam will include everything covered during the term.
- (c) If you need to reschedule final exam for a very good reason, you should let me know at least 15 days in advance.

Academic Honesty: Cheating is unacceptable. You may discuss homework solutions with friends but not copy others work.