

Dick Casali

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SUMMARY

Resourceful engineer with 20 years experience in customer oriented manufacturing, research or failure analysis environment. Adept analytical problem solver with human relations skills, especially in high-pressure environments. Proven leadership and management skills in lab setting.

EDUCATION

PhD Materials Science, estimated 2010, Oregon State University
M.S. Materials Science, 2000, Oregon State University
B.S. Chemistry, 1988, Washington State University

EXPERIENCE

(Nov 06 - now)

•Sr. Failure Analysis Engineer Intel/ATD/BTAC & GPAC, Hawthorn Farms, OR
Material and failure analysis for reliability testing of solder joints on a printed circuit board. Responsible for Ecology Compliancy testing for halogen free, lead free and other 'green' testing as applied to the motherboard level. Chairperson of Intel corporate wide FTIR User Group.

(Mar 05 – Nov 06)

•Analysis Lab Team Lead Intel/ATD/SM/GPAC, Hawthorn Farms, OR
Managed 8 direct reports and lab environment to reliably provide requested data to customers in a timely manner. Managed lab improvement projects including installing new rework equipment, technician training for all certified process steps including ringing in vendors, assisting customers to submit jobs

(Jul 00 – Mar 05)

•Materials Analyst Engineer Intel/SM/SMTD/SMAC, Hawthorn Farms, OR
Provided actionable data to business units requiring failure analysis of desktop and server motherboard assemblies utilizing analytical data and material science principles. Provided analytical material analysis on successful product development and technical certification for Tiger and Westville server products, Itanium LIF socket (Recognized at SM level), among others. Major contributor to SMTD's BGA Open Joint TF-guidelines produced enabled the factory to significantly reduce BGA OJ DPM. Recognized at SMTD level. Direct technicians as required for maximizing analysis through put.

(Oct 95 - May 97)

•Sr. Advisory Engineer-Scientist Seagate Technology, San Jose, CA
Using Design of Experiments principles, developed methods to determine tin, ion content and %cure for adhesives. Performed correlation studies. Evaluated emerging technologies for hard disc drive technology. Worked in clean rooms as required.

(May 93 - Oct 95)

•Lab Manager & Sr. Engineer Eeonyx Corp., Pinole, CA
Supervised 3 scientists in conductive polymer synthesis and process lab. Interacted daily with customers and vendors. Provided on-site training and presentations to customers. Evaluated and summarized data for senior management. Refined melt processing technology to be transferred to customers. Initiated and documented In-process and Final QA criteria and procedures. Safety administrator for this start-up company.

(May 92 - Apr 93)

•Sr. Advisory Engineer Seagate Technology, Scotts Valley, CA
Provided materials and process solutions for manufacturing problems of HDD. Analytically determined micro-quantities of organic/inorganic substances utilizing GC-

MS, IC, FTIR, TMA, DSC, TGA and SEM to the sub-component level. Maintained and trained personnel for TMA, TGA and FTIR.

PROFICIENCY SEM-EDX, FTIR, DSC, TGA, TMA, GC-MS, IC, viscometer, particle analyzer, BET surface area analyzer.