Text: Microelectronic Circuits by Sedra & Smith.

Labs: Entirely by lab TAs + guidance from Don Heer (KEC-1117, 7-2978, heer@oregonstate.edu). Please consult the TA of your lab directly for all labs related questions. Don Heer will help resolve tough issues (if any), and I’ll try to be helpful.

HW: Homework will not be graded based on right or wrong answers, but on the level of effort shown in what you submit each week. Each assignment is to be submitted on the due date at the beginning of the class. No late homework will be accepted. Homework grading will be done using one of three scores: 10, 5, or 0. A “good” amount of effort and understanding demonstrated will receive 10; no or little work will receive 0; and 5 for demonstrating your efforts somewhere in between.

Exams: Exams are closed book, closed notes, and no calculator – just your pencil/pen. I will provide a “reference sheet” for the exam. This sheet will be made available well before the exam so that you’d know what to expect.

OH: Ask specific questions at the office hours, referring to lecture notes, homework solutions, etc. It would be appropriate to come with printed notes/solutions (or show it on your laptop).

Grading: Laboratory (from TA + Don Heer) 25%
Homework 10%
Midterm-1 20% (Thursday Apr-21 in class)
Midterm-2 20% (Tuesday May-24 in class)
Final 25% (Wednesday Jun-8 at 12:15 pm)