

<http://eecs.oregonstate.edu/~moon/ece323>

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	Mon	Tue	Wed	Thu	Fri
1:30	Evan		Evan		Evan
2:00	KEC atrium	ECE 323	KEC atrium	ECE 323	KEC atrium
2:30					
3:30		Moon		Moon	
4:00	Runpeng	KEC 4093	Runpeng	KEC 4093	Runpeng
4:30	KEC atrium		KEC atrium		KEC atrium
5:00					

**Text** Microelectronic Circuits by Sedra & Smith (& Carusone & Gaudet)

**HW** Homework will not be graded based on right or wrong answers, but on the level of effort shown in what you submit each week. Each assignment is to be submitted via Canvas (scanned PDF only) by/before the due date at 2pm. No late homework will be accepted. Homework grading will be done using one of three scores: **10**, **5**, or **0**. A complete/good effort and understanding demonstrated will receive a **10**; obviously insufficient work (or copying of solutions) will receive a **0**; and a **5** for something in between.

**Exams** The in-person exams will be **closed** book/notes. I provide a reference sheet along with the exam. This reference sheet is already made available on the class web page, so you should get used to the content well ahead of time.

**OH** Office hours are not intended for mini lectures. **Please ask specific questions, referring to a copy of my own writing, e.g. posted lecture notes, homework solutions...**

**Grade**

Homework (six of them)	15%
Midterm-1	25% ( <b>Tuesday Apr-30</b> 2:05-3:15pm)
Midterm-2	25% ( <b>Thursday May-23</b> 2:05-3:15pm)
Final	35% ( <b>Thursday Jun-13</b> 12:05-1:15pm)

**Canvas** I very minimally use Canvas, to let you upload HW and to keep you informed of your scores. Please don't expect me to receive/see Canvas messages. Send me an email instead.

**Academic Dishonesty** (cheating) is defined as an act of deception in which a student seeks to claim credit for the work or effort of another person, or uses unauthorized materials or fabricated information in any academic work or research, either through the student's own efforts or the efforts of another. [See Code of Student Conduct document at <https://studentlife.oregonstate.edu/pre-student-conduct-community-standards>]. **Exams:** Discussing/communicating with others or copying from others/friends... **Homework:** Copying (whole or partial) solutions, copying (whole or partial) another student's work... What will be the penalty? You will receive 0% for that and potentially for the entire course. You will also be reported to the university.

**Your reading guide for the course...**

Section numbers are from the eighth edition, but you can find the same topic in older editions.

Course overview; ECE 322 review

- 5.1 Device structure and physical operation (MOSFET)
- 5.2 Current-voltage characteristics (MOSFET)
- 5.3 MOSFET circuits at DC
- 6.1 Device structure and physical operation (BJT)
- 6.2 Current-voltage characteristics (BJT)
- 6.3 BJT circuits at DC
- 7.1 Basic principles (transistor amplifiers)
- 7.2 Small-signal operation and models (transistor amplifiers)

Single stage amplifiers

- 7.3 Basic configuration (transistor amplifiers)
- 7.4 Biasing (transistor amplifiers)
- 7.5 Discrete-circuit amplifiers (transistor amplifiers)

Multi stages & building blocks

- 8.2 IC biasing: current sources and current mirrors
- 8.3 The basic gain cell
- 8.4 The common-gate and common-base amplifiers as current buffers
- Parts of 9.\* (differential and multistage amplifiers)

Frequency response

- 10.1 High-frequency transistor models
- 10.2 High-frequency response of CS and CE amplifiers
- 10.3 The method of open-circuit time constants
- 10.8 Low-frequency response of discrete-circuits CS and CE amplifiers

Feedback

- 11.\*

Oscillators/feedback & stability

- 15.1 Basic principles of sinusoidal oscillators
- 15.2 Opamp-RC oscillator circuits
- 15.4 Nonlinear oscillators or function generators