

Message From the Incoming Editor-in-Chief

I HAVE much appreciation and respect for what Prof. Bram Nauta has done for the IEEE JOURNAL OF SOLID-STATE CIRCUITS as the Editor-in-Chief. Prof. Nauta has attracted and maintained a strong and seasoned editorial board, systematically refined a review process that cultivates excellence without bias, and kept a large and complete database of manuscripts in pristine order. As the incoming Editor-in-Chief, I could not have hoped for a situation better than what I am to inherit. As I intend to see the same spirit of excellence carry the Journal into the future, I express my sincere gratitude towards all editors of the past who have built the Journal from infancy, and my special gratitude and admiration towards Prof. Nauta, who has done such an amazing job.

The IEEE JOURNAL OF SOLID-STATE CIRCUITS is a publication with simply the best reputation in our field. Such standing is not a result of the efforts of a few persons or a group of orga-

nizers, but of all authors, reviewers, associate editors, and others involved. In other words, the Journal's reputation is the direct reflection of the international Solid-State Circuits community. This is your journal, and you make it what it is. I am grateful to you, and I count on your contributions into the future.

I look forward to receiving and managing high-quality manuscripts from all over the world and working with my outstanding associate editors to keep the level of the Journal as high as it is now. Serving the Journal is a great privilege and honor, and I thank the Solid-State Circuits Society and community for giving me this opportunity.

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Un-Ku Moon (S'92–M'94–SM'99–F'09) received the B.S. degree from the University of Washington, Seattle, in 1987, the M.Eng. degree from Cornell University, Ithaca, NY, in 1989, and the Ph.D. degree from the University of Illinois at Urbana-Champaign in 1994.

He has been with the School of Electrical Engineering and Computer Science, Oregon State University, Corvallis, since 1998. Before joining Oregon State University, he was with Bell Laboratories from 1988 to 1989, and from 1994 to 1998. His technical contributions have been in the area of analog and mixed-signal circuits including high-linearity filters, timing recovery, PLLs, data converters, and low-voltage circuits for CMOS.

Prof. Moon has served as an Associate Editor of the IEEE JOURNAL OF SOLID-STATE CIRCUITS and the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: ANALOG AND DIGITAL SIGNAL PROCESSING, as the Editor-in-Chief and Deputy Editor-in-Chief of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, and on the Technical Program Committee of the IEEE Custom Integrated Circuits Conference. He also served on the IEEE Solid-State Circuits

Society (SSCS) Administrative Committee (AdCom) and the IEEE Circuits and Systems Society (CASS) Board of Governors as the SSCS representative to CASS. He currently serves on the Technical Program Committee of the IEEE International Solid-State Circuits Conference and the IEEE VLSI Circuits Symposium.