

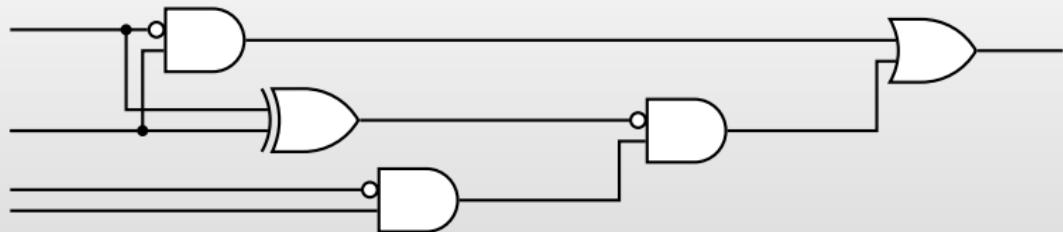
Practical Garbled Circuit Optimizations

Mike Rosulek
Oregon State **OSU**
UNIVERSITY

Collaborators: David Evans / Vlad Kolesnikov / Payman Mohassel / Samee Zahur

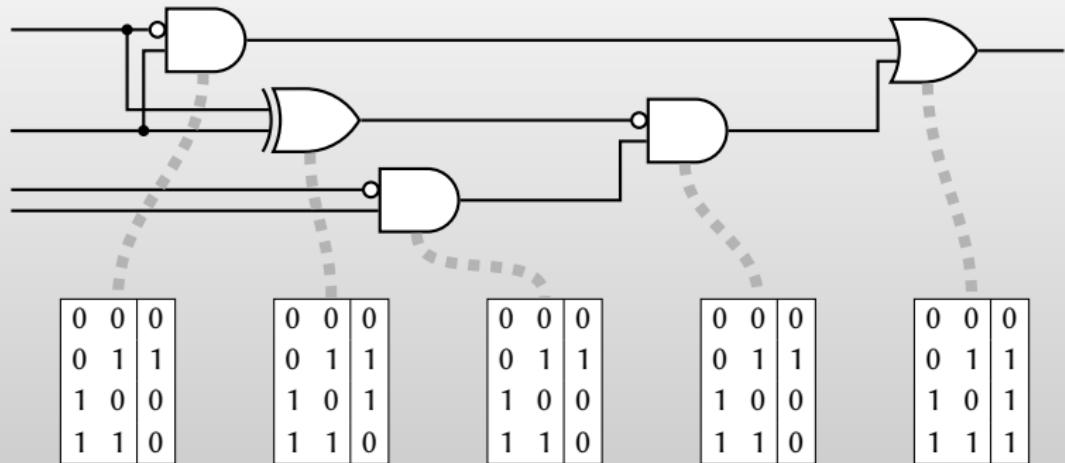
Garbled circuit framework

[Yao86]



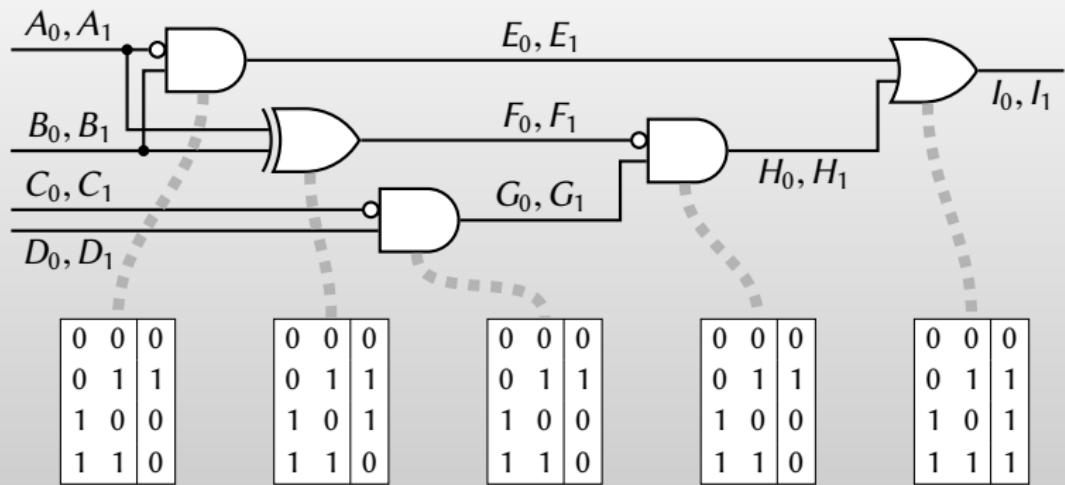
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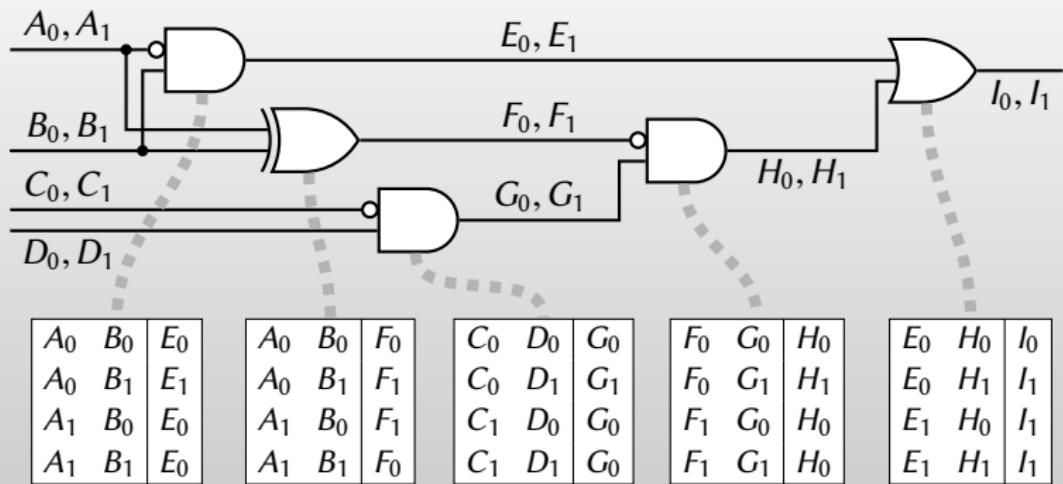


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- ▶ Pick random **labels** W_0, W_1 on each wire

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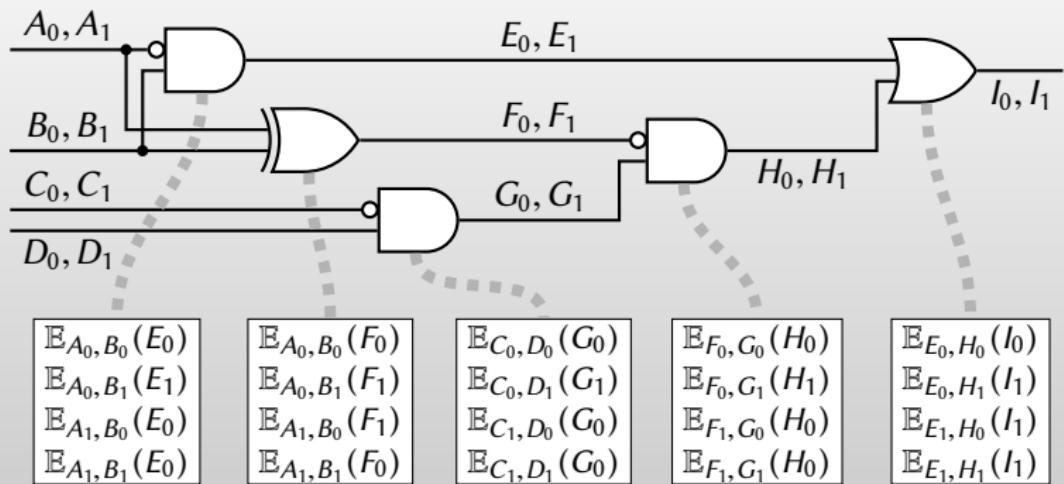


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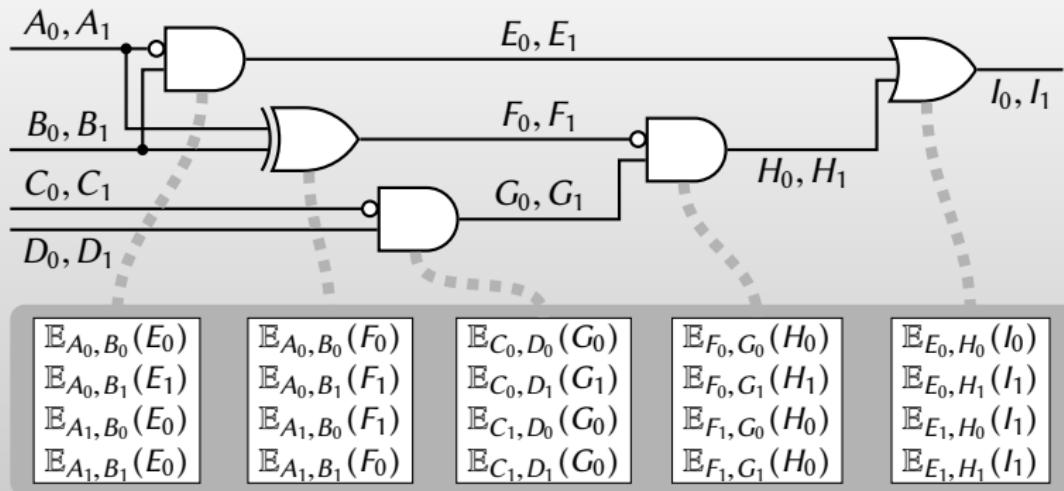


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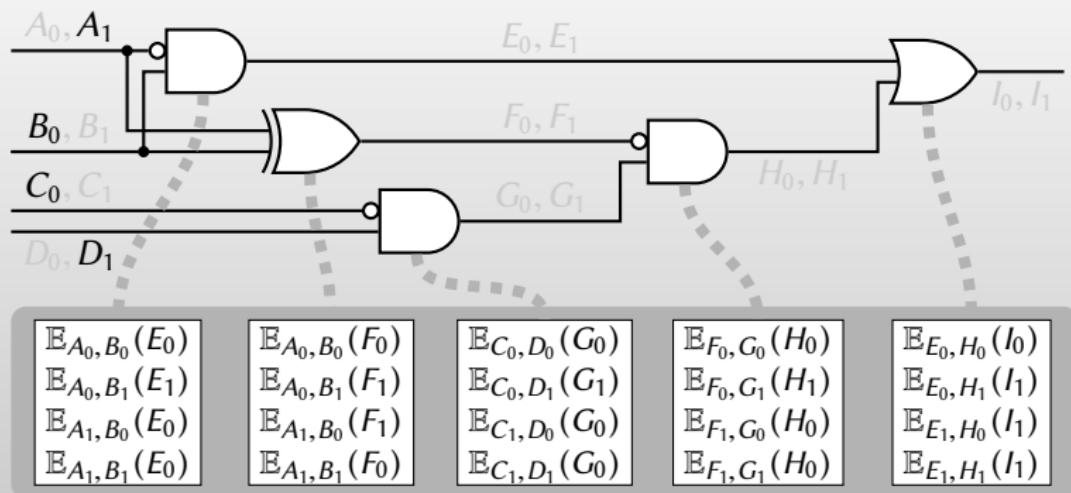
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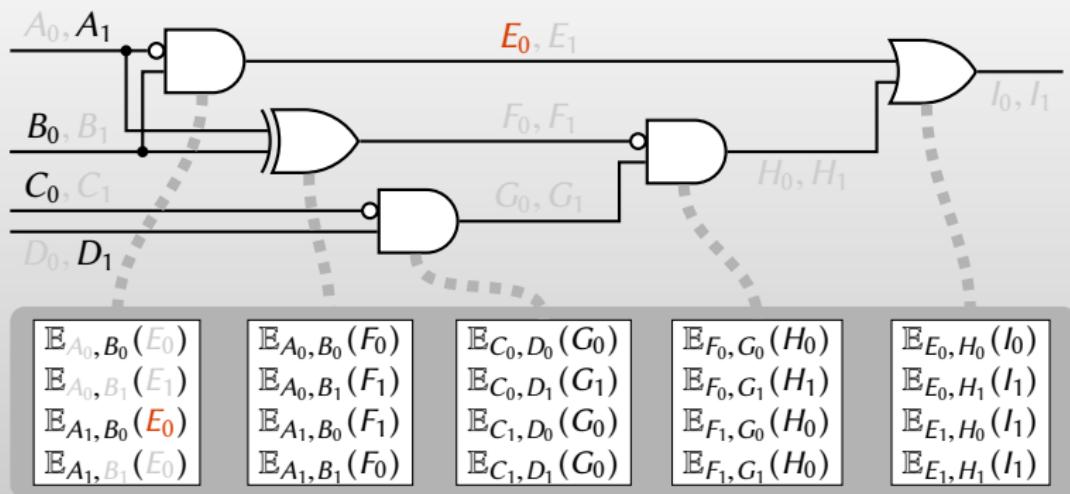
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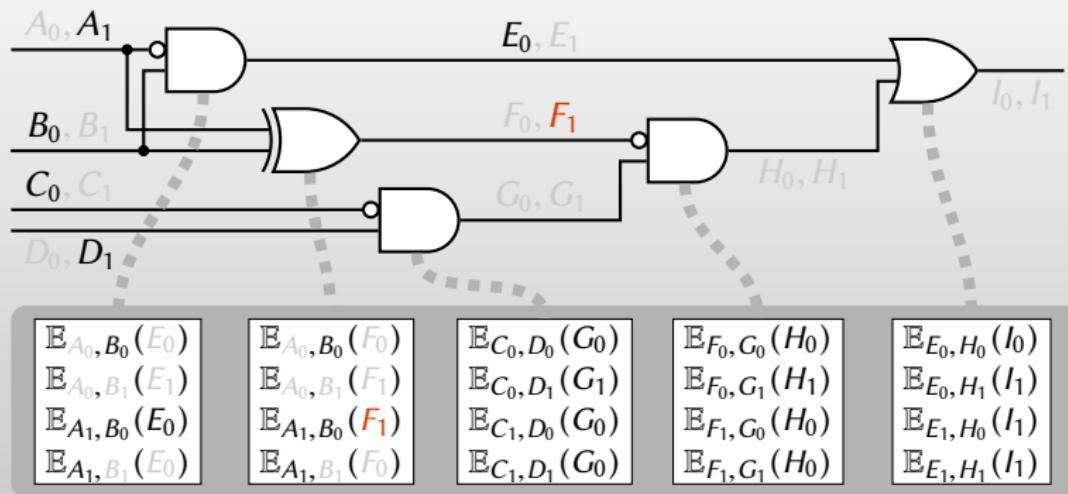
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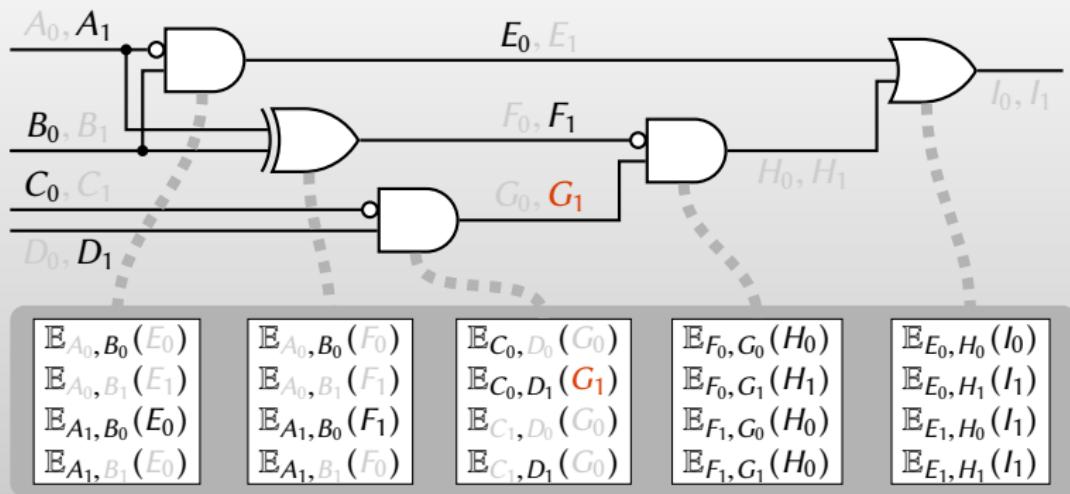
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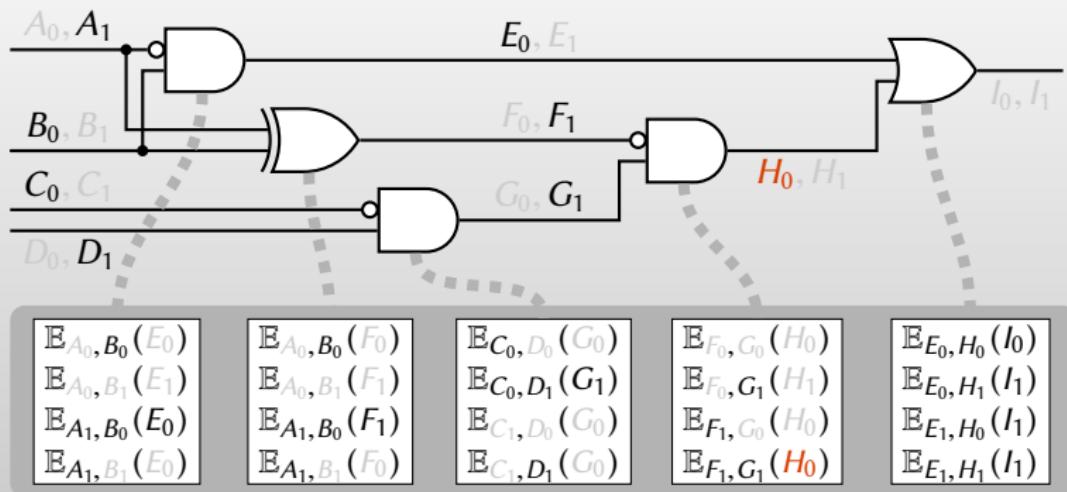
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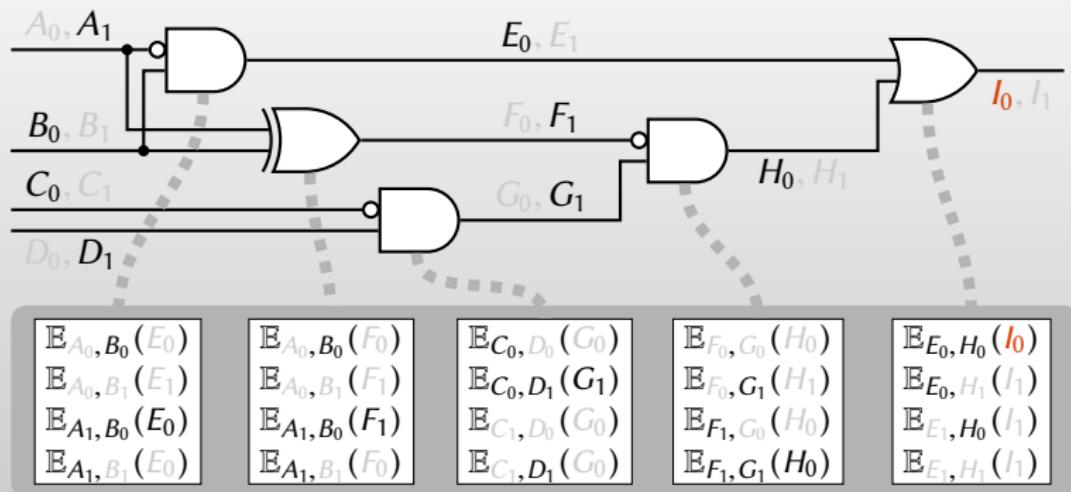
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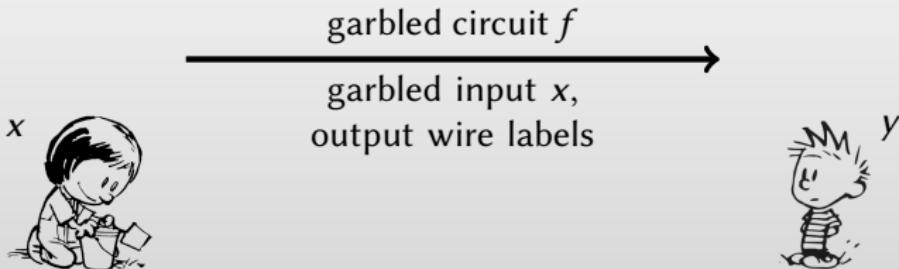
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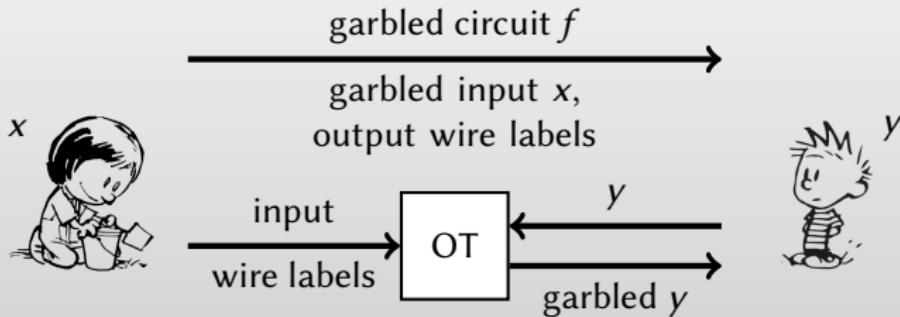
Applications: 2PC and more



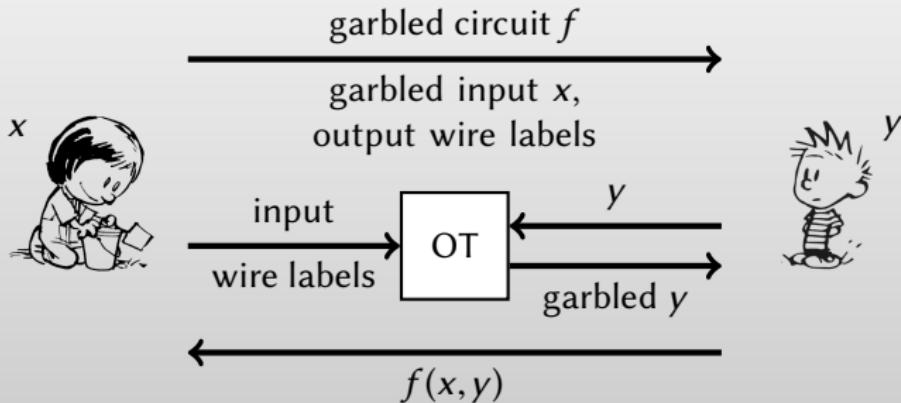
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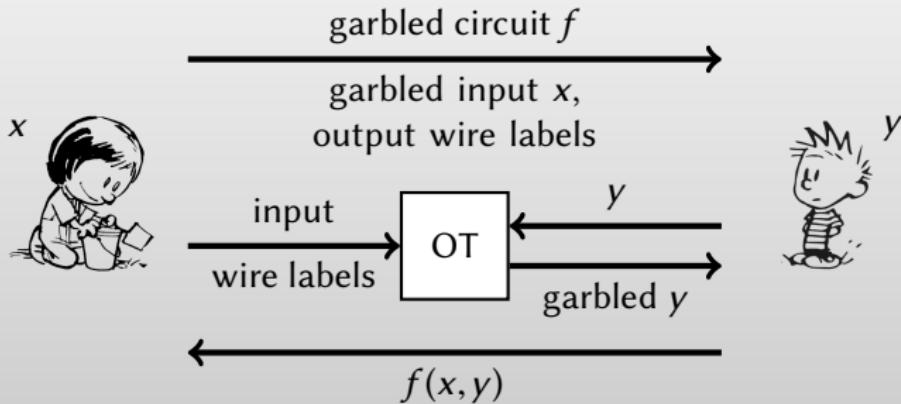
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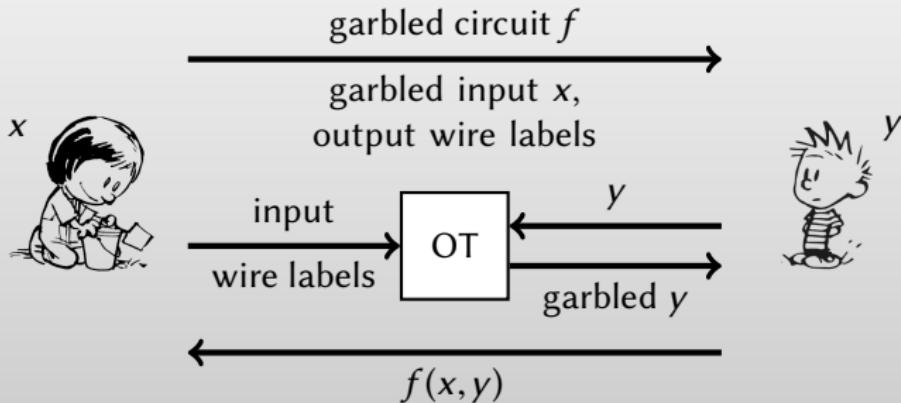


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Private function evaluation, zero-knowledge proofs, encryption with key-dependent message security, randomized encodings, secure outsourcing, one-time programs, . . .

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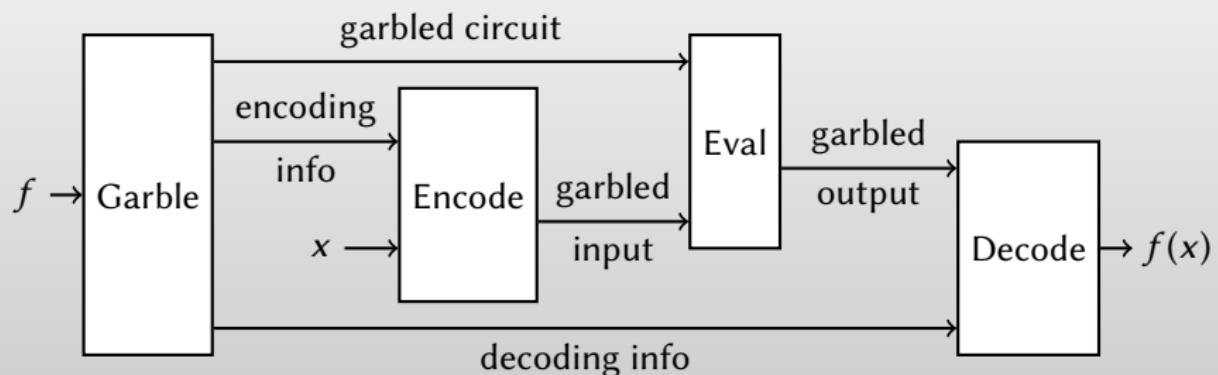


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Garbling is a fundamental primitive [BellareHoangRogaway12]

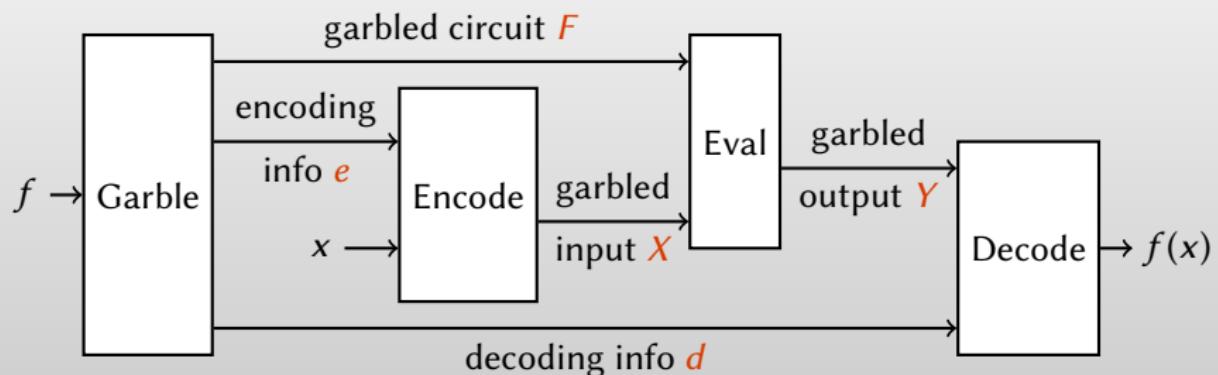
Syntax

[BellareHoangRogaway12]



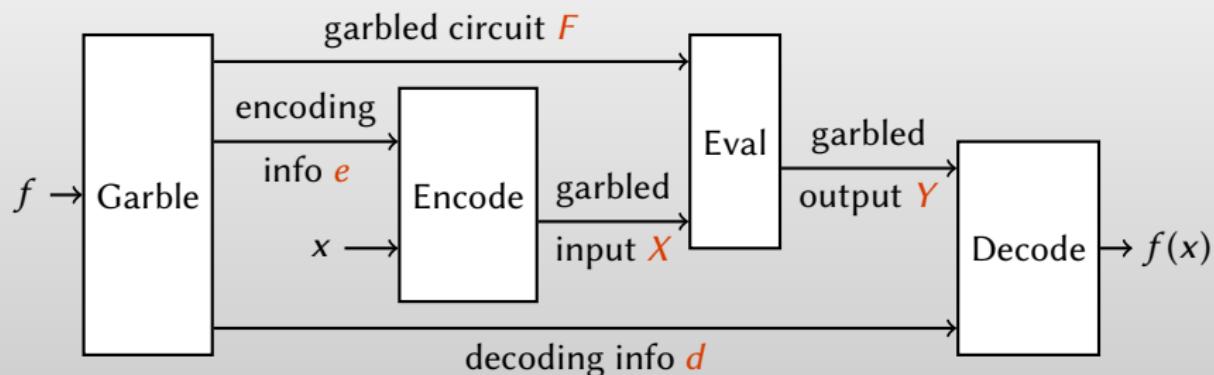
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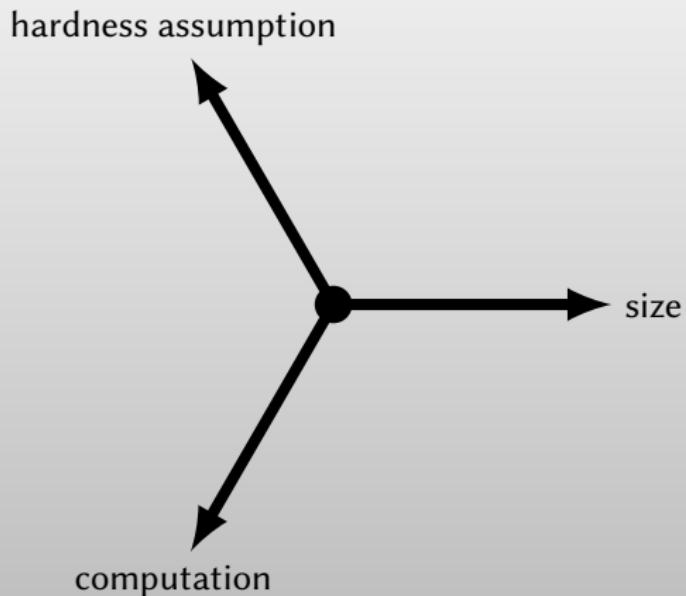
Security properties:

Privacy: (F, X, d) reveals nothing beyond $f(x)$

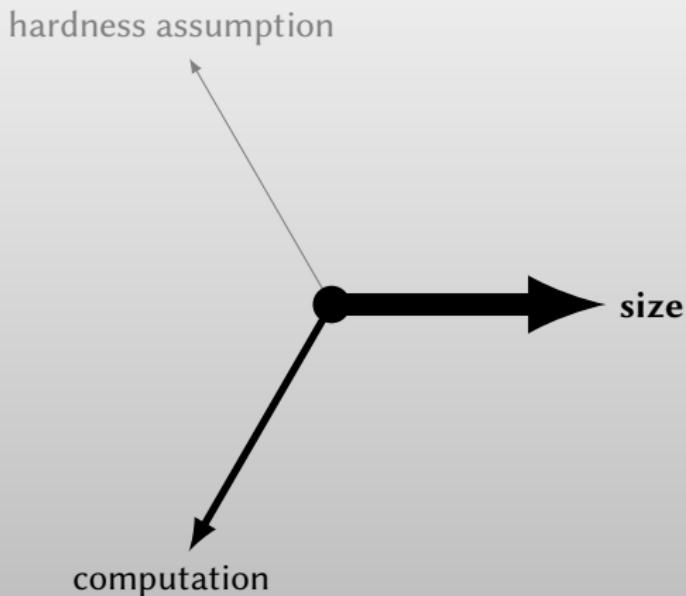
Obliviousness: (F, X) reveals nothing

Authenticity: given (F, X) , hard to find \tilde{Y} that decodes $\notin \{f(x), \perp\}$

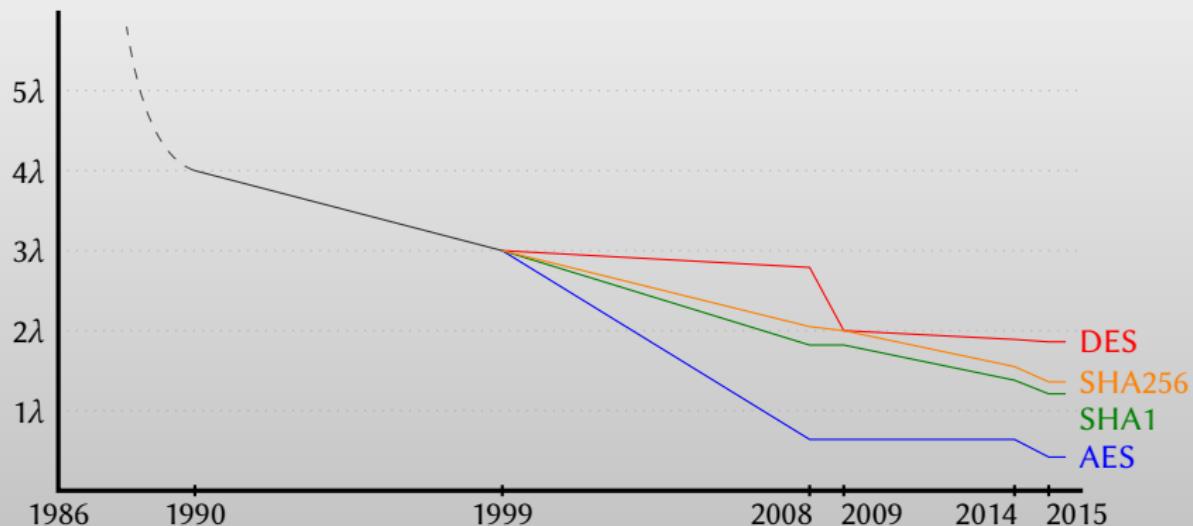
Parameters to optimize



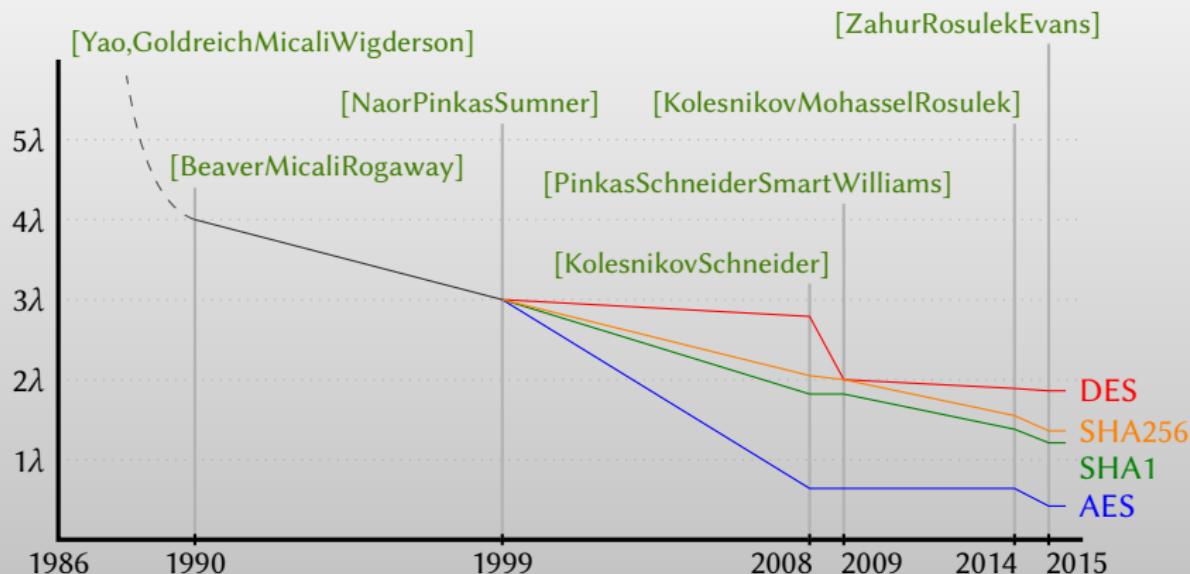
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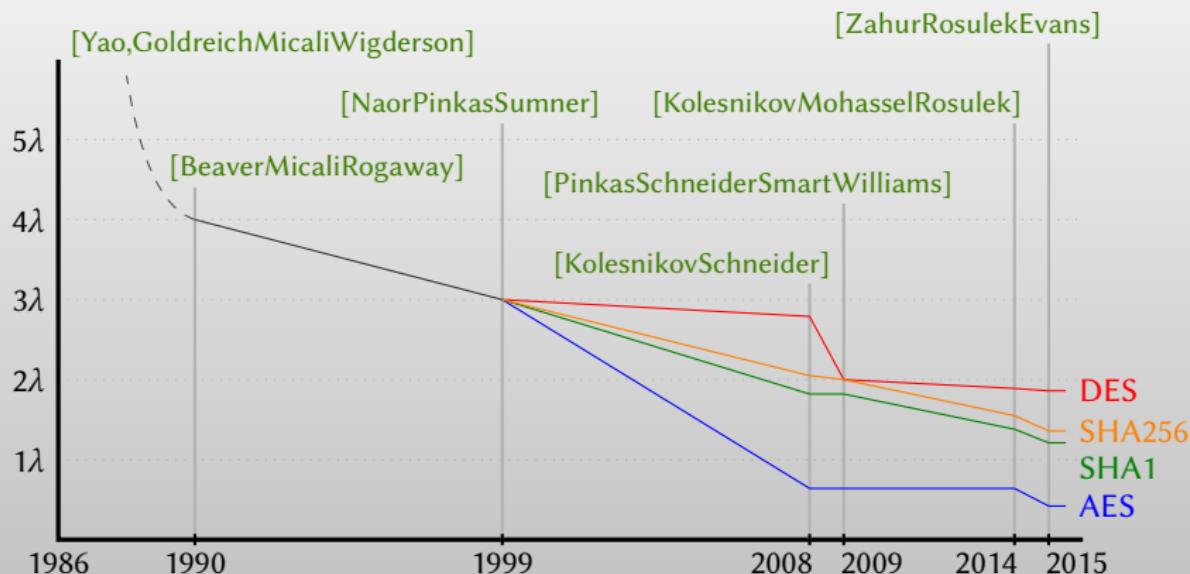
Average bits per garbled gate



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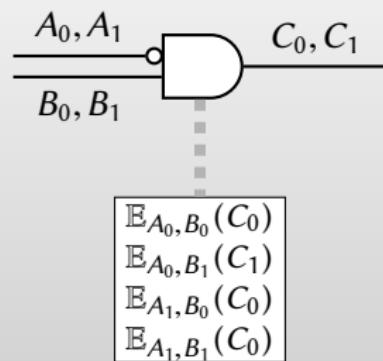


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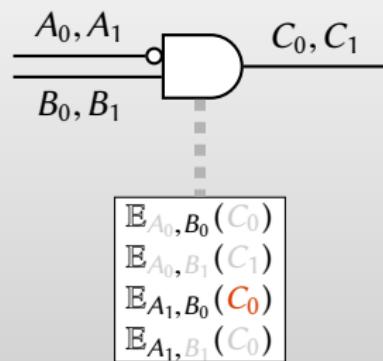
Prediction: by 2026, all garbled circuits will have zero size.

Murky beginnings [Yao86]



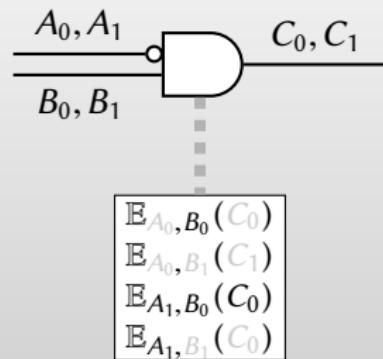
- ▶ Position in this list leaks semantic value

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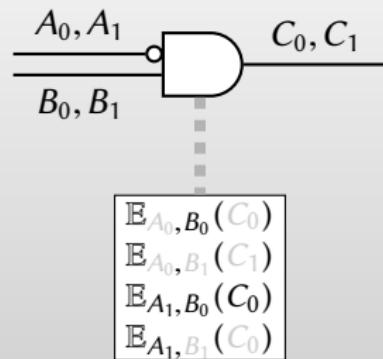
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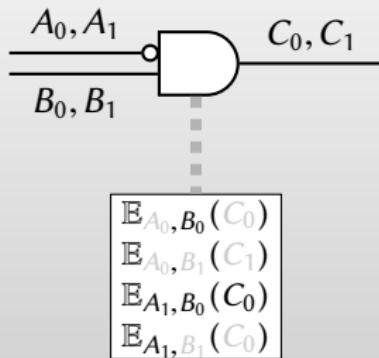
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- ▶ Need to **detect** [in]correct decryption

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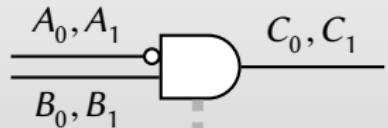
- ▶ Position in this list leaks semantic value \implies permute ciphertexts
- ▶ Need to **detect** [in]correct decryption
- ▶ (Apparently) no one knows exactly what Yao had in mind:
 - ▶ $\mathbb{E}_{K_0, K_1}(M) = \langle E(K_0, S_0), E(K_1, S_1) \rangle$ where $S_0 \oplus S_1 = M$

[GoldreichMicaliWigderson87]

▶ $\mathbb{E}_{K_0, K_1}(M) = E(K_1, E(K_0, M))$ [LindellPinkas09]

Permute-and-Point

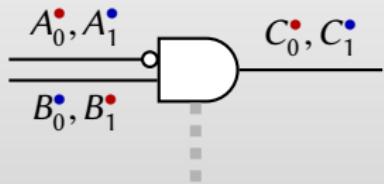
[BeaverMicaliRogaway90]



$\boxed{\begin{array}{l} \mathbb{E}_{A_0, B_0}(C_0) \\ \mathbb{E}_{A_0, B_1}(C_1) \\ \mathbb{E}_{A_1, B_0}(C_0) \\ \mathbb{E}_{A_1, B_1}(C_0) \end{array}}$

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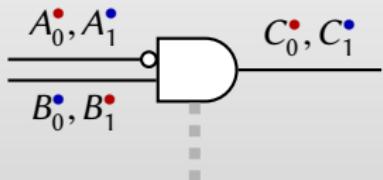


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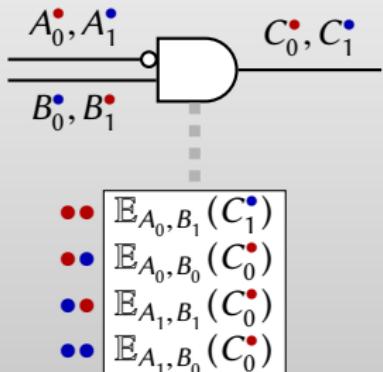


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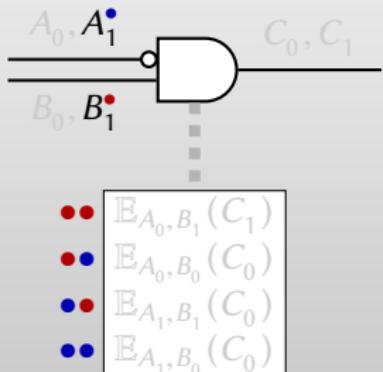
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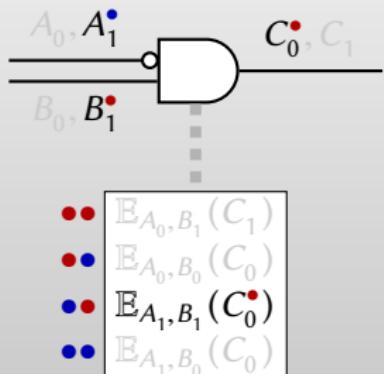
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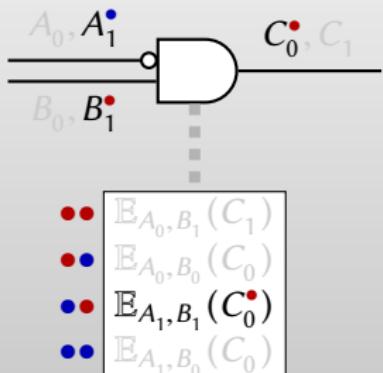
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Can use **one-time-secure** symmetric encryption!

Computational cost of garbling

$\mathbb{E}_{A,B}(C)$:

$\text{PRF}(A, \text{gateID}) \oplus \text{PRF}(B, \text{gateID}) \oplus C$
[NaorPinkasSumner99]

cost to garble AES

~6s [extrapolated]
time from Fairplay [MNPS04]: PRF = SHA256

Computational cost of garbling

2 hash ≈ 1 hash

<u>$\mathbb{E}_{A,B}(C)$:</u>	<u>cost to garble AES</u>
$\text{PRF}(A, \text{gateID}) \oplus \text{PRF}(B, \text{gateID}) \oplus C$ [NaorPinkasSumner99]	~6s [extrapolated] time from Fairplay [MNPS04]: PRF = SHA256
$\text{H}(A \ B \ \text{gateID}) \oplus C$ [LindellPinkasSmart08]	0.15s time from [sS12]; H = SHA256

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$2 \text{ hash} \gg 1 \text{ hash} \gg 1 \text{ block cipher}$

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[shelatShen12]

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Computational cost of garbling

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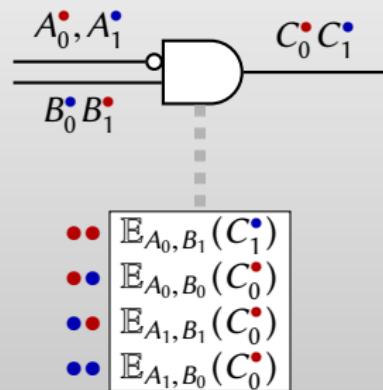
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$\text{AES256}(A \ B, \text{gateID}) \oplus C$ [shelatShen12]	0.12s
$\text{AES}(\text{const}, K) \oplus K \oplus C$ <i>where $K = 2A \oplus 4B \oplus \text{gateID}$</i> [BellareHoangKeelveedhiRogaway13]	0.0003s

Scoreboard

	size ($\times \lambda$)	garble cost	eval cost	assumption
Classical	large?	8	5	PKE
P&P	4	4/8	1/2	hash/PRF

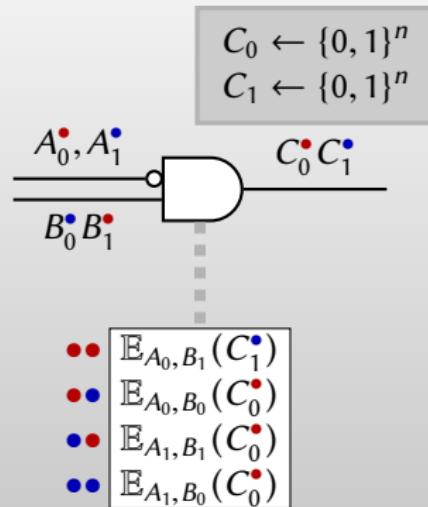
Garbled Row Reduction

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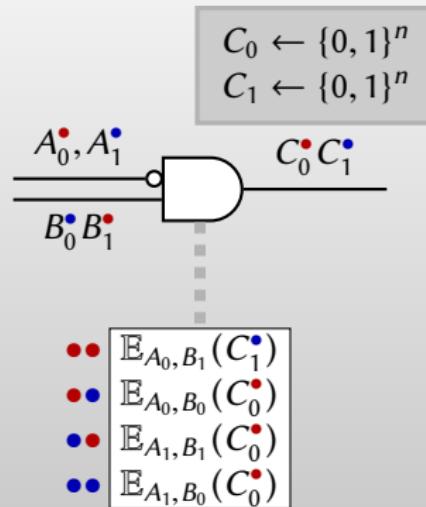
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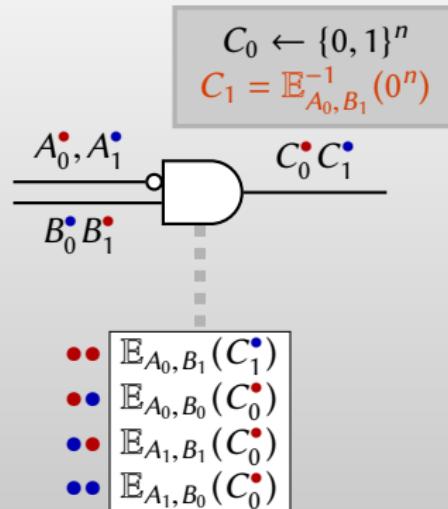
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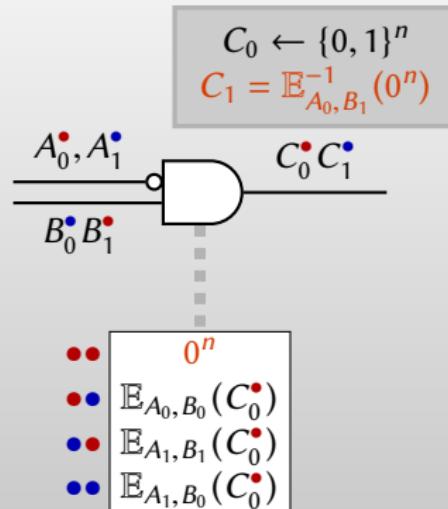
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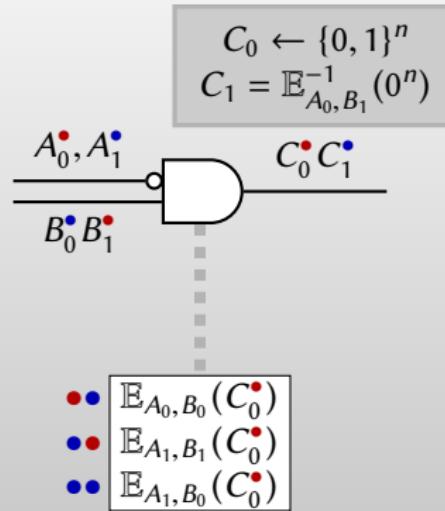
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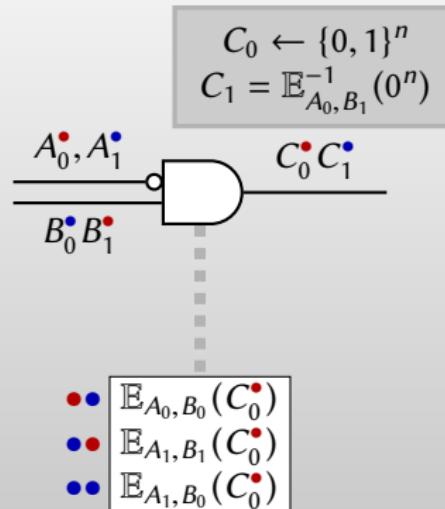
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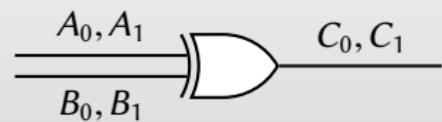
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- ▶ Evaluate as before, but imagine ciphertext 0^n if you got $\bullet\bullet$.

Scoreboard

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GRR3	3	4/8	1/2	hash/PRF

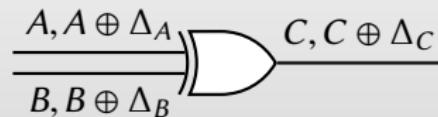
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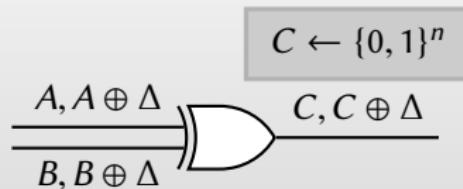
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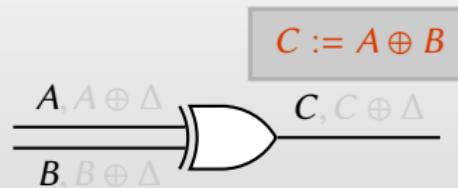
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Free XOR

[KolesnikovSchneider08]

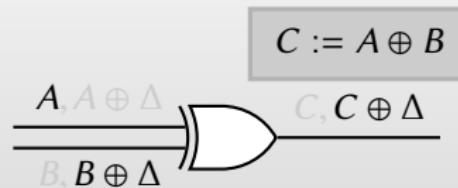


$$\underbrace{A}_{\text{FALSE}} \oplus \underbrace{B}_{\text{FALSE}} = \underbrace{A \oplus B}_{\text{FALSE}}$$

- ▶ Wire's **offset** \equiv XOR of its two labels
- ▶ Choose all wires to have same (secret) offset Δ
- ▶ Choose FALSE output = FALSE input \oplus FALSE input

Free XOR

[KolesnikovSchneider08]

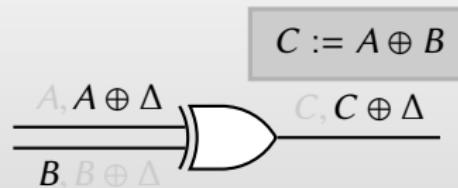


$$\underbrace{A}_{\text{FALSE}} \oplus \underbrace{B \oplus \Delta}_{\text{TRUE}} = \underbrace{A \oplus B \oplus \Delta}_{\text{TRUE}}$$

- ▶ Wire's **offset** \equiv XOR of its two labels
- ▶ Choose all wires to have same (secret) offset Δ
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- ▶ Evaluate by xorring input wire labels (no crypto)

Free XOR

[KolesnikovSchneider08]

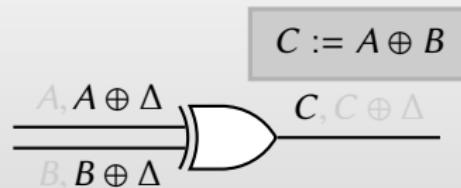


$$\underbrace{A \oplus \Delta}_{\text{TRUE}} \oplus \underbrace{B}_{\text{FALSE}} = \underbrace{A \oplus B}_{\text{TRUE}} \oplus \Delta$$

- ▶ Wire's **offset** \equiv XOR of its two labels
- ▶ Choose all wires to have same (secret) offset Δ
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Free XOR

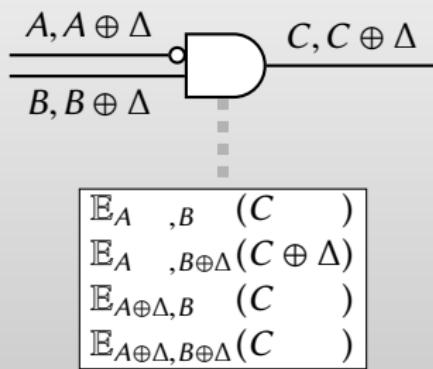
[KolesnikovSchneider08]



$$\underbrace{A \oplus \Delta}_{\text{TRUE}} \oplus \underbrace{B \oplus \Delta}_{\text{TRUE}} = \underbrace{A \oplus B}_{\text{FALSE}}$$

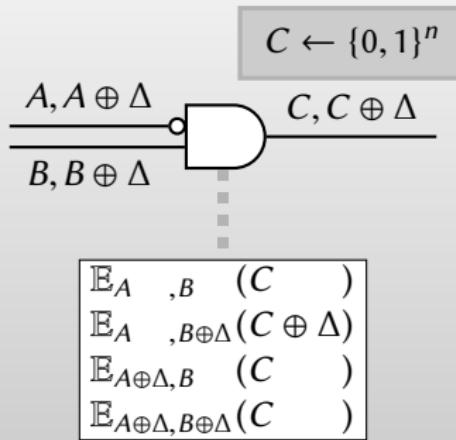
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Freedom at a cost. . .



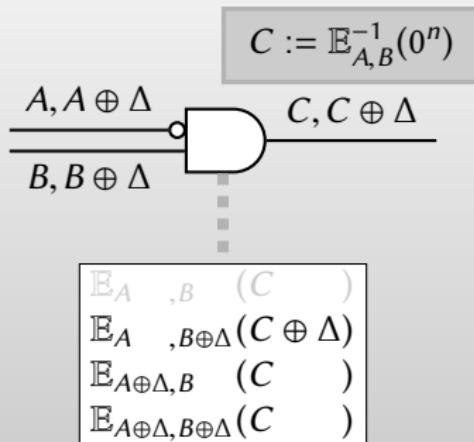
- ▶ Still need to garble AND gates

Freedom at a cost. . .



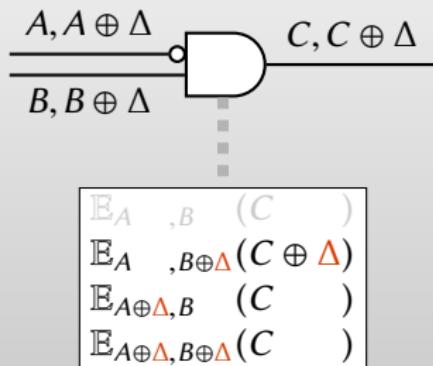
- ▶ Still need to garble AND gates
- ▶ Compatible with garbled row-reduction

Freedom at a cost. . .



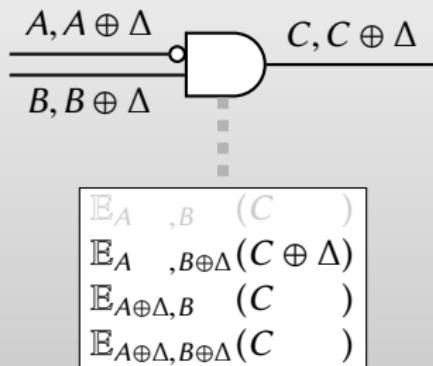
- ▶ Still need to garble AND gates
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Freedom at a cost. . .



- ▶ Still need to garble AND gates
- ▶ Compatible with garbled row-reduction
- ▶ Secret Δ used in key and payload of ciphertexts!

Freedom at a cost. . .



- ▶ Still need to garble AND gates
- ▶ Compatible with garbled row-reduction
- ▶ Secret Δ used in key and payload of ciphertexts!
- ▶ Requires related-key + circularity assumption [ChoiKatzKumaresanZhou12]

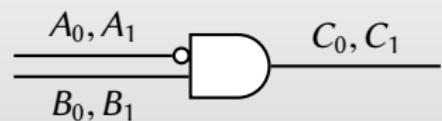
Scoreboard

	size ($\times \lambda$)		garble cost		eval cost		assumption
	XOR	AND	XOR	AND	XOR	AND	
Classical	large?		8		5		PKE
P&P	4	4	4/8	4/8	1/2	1/2	PRF/hash
GRR3	3	3	4/8	4/8	1/2	1/2	PRF/hash
Free XOR	0	3	0	4	0	1	circ. hash

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!



Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

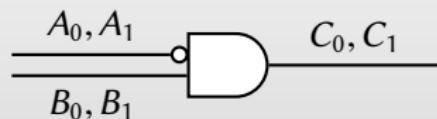
- ▶ Evaluator can know exactly one of:

$$K_1 = \mathbb{E}_{A_0, B_0}^{-1}(0^n)$$

$$K_2 = \mathbb{E}_{A_0, B_1}^{-1}(0^n)$$

$$K_3 = \mathbb{E}_{A_1, B_0}^{-1}(0^n)$$

$$K_4 = \mathbb{E}_{A_1, B_1}^{-1}(0^n)$$



Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

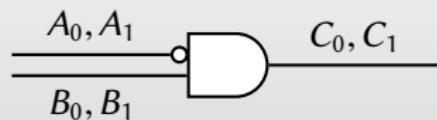
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$$K_1 = \mathbb{E}_{A_0, B_0}^{-1}(0^n) \rightsquigarrow \text{learn } C_0$$

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Row reduction ++

[PinkasSchneiderSmartWilliams09]

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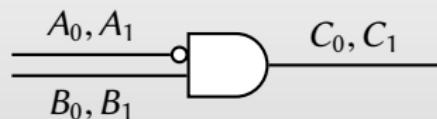
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(1, K_1), (3, K_3), (4, K_4)

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

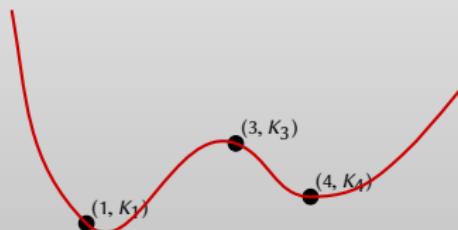
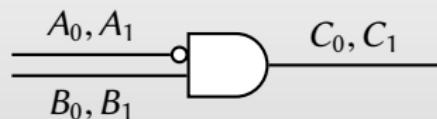
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$P =$ uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

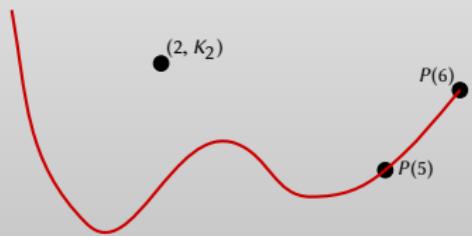
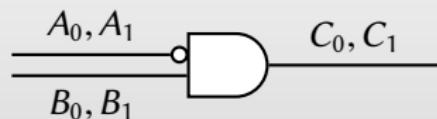
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P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

$(2, K_2), (5, P(5)), (6, P(6))$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

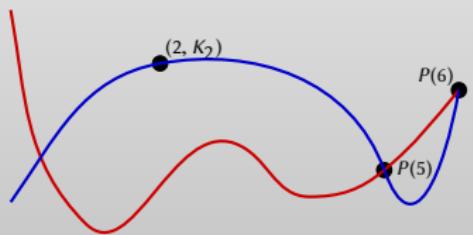
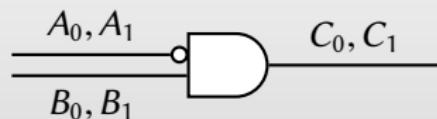
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P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

Q = uniq deg-2 poly thru
 $(2, K_2), (5, P(5)), (6, P(6))$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

- Evaluator can know exactly one of:

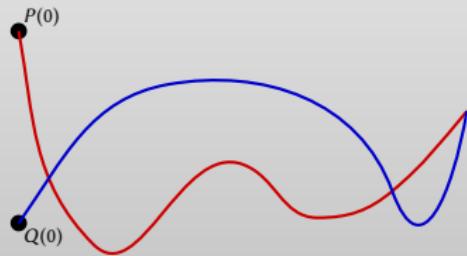
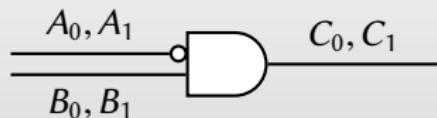
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$$C_0 = P(0); C_1 = Q(0)$$



P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

Q = uniq deg-2 poly thru
 $(2, K_2), (5, P(5)), (6, P(6))$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

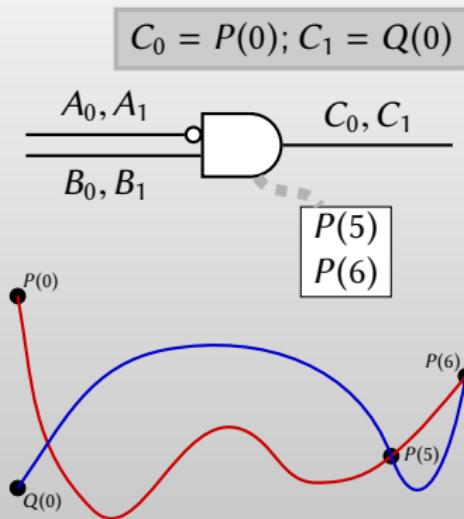
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$\textcolor{red}{P}$ = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

$\textcolor{blue}{Q}$ = uniq deg-2 poly thru
 $(2, K_2), (5, P(5)), (6, P(6))$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

- Evaluator can know exactly one of:

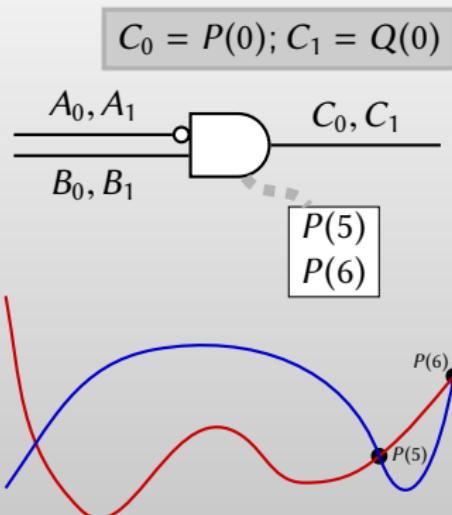
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- Evaluate by interpolating poly thru $K_i, P(5)$ and $P(6)$



P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

Q = uniq deg-2 poly thru
 $(2, K_2), (5, P(5)), (6, P(6))$

Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

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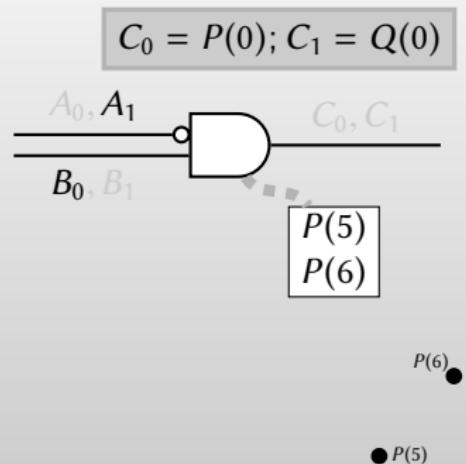
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Row reduction ++

[PinkasSchneiderSmartWilliams09]

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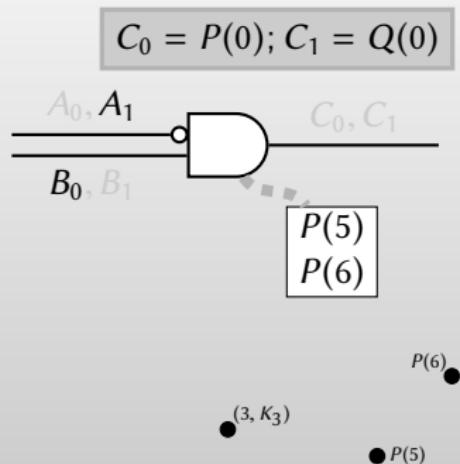
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- Evaluate by interpolating poly thru $K_i, P(5)$ and $P(6)$



P = uniq deg-2 poly thru

$$(1, K_1), (3, K_3), (4, K_4)$$

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Row reduction ++

[PinkasSchneiderSmartWilliams09]

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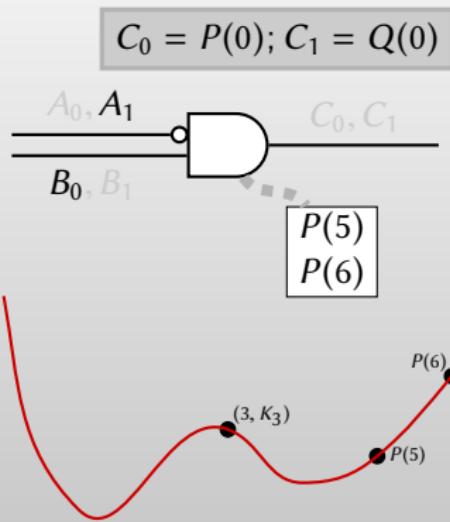
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- Evaluate by interpolating poly thru $K_i, P(5)$ and $P(6)$



P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

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Row reduction ++

[PinkasSchneiderSmartWilliams09]

Garbled gates with only **2** ciphertexts!

- Evaluator can know exactly one of:

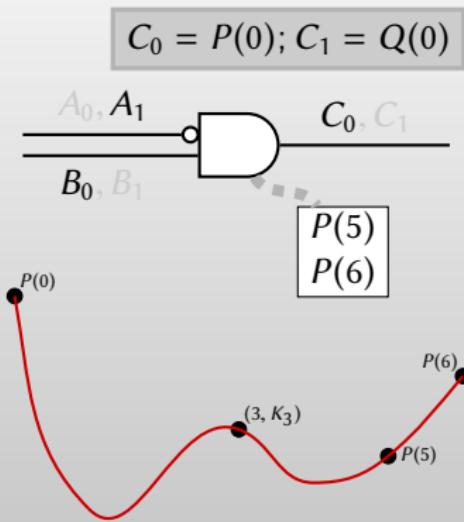
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Row reduction ++

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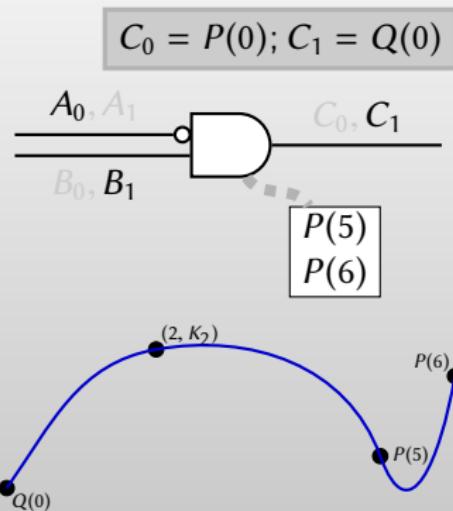
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Row reduction ++

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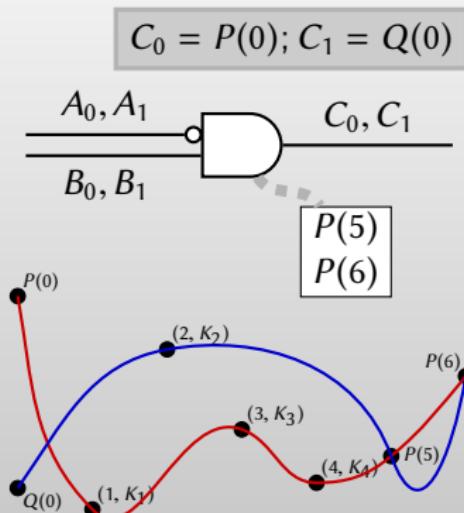
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- ▶ Evaluate by interpolating poly thru $K_i, P(5)$ and $P(6)$
- ▶ **Incompatible** with Free-XOR: can't ensure $C_0 \oplus C_1 = \Delta$



P = uniq deg-2 poly thru
 $(1, K_1), (3, K_3), (4, K_4)$

Q = uniq deg-2 poly thru
 $(2, K_2), (5, P(5)), (6, P(6))$

Scoreboard

	size ($\times \lambda$)		garble cost		eval cost		assumption
	XOR	AND	XOR	AND	XOR	AND	
Classical	large?		8		5		PKE
P&P	4	4	4/8	4/8	1/2	1/2	hash/PRF
GRR3	3	3	4/8	4/8	1/2	1/2	PRF/hash
Free XOR	0	3	0	4	0	1	circ. hash
GRR2	2	2	4/8	4/8	1/2	1/2	PRF/hash

FleXOR

[KolesnikovMohasselRosulek14]

$$\frac{A, A \oplus \Delta_1}{\longrightarrow}$$

FleXOR

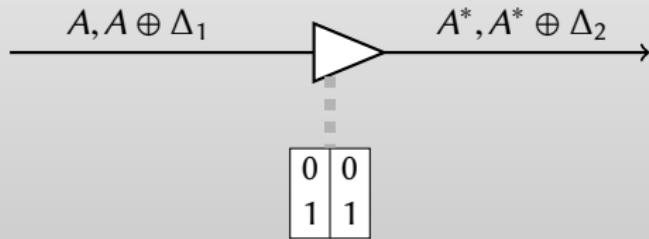
[KolesnikovMohasselRosulek14]

$$\frac{A, A \oplus \Delta_1}{A^*, A^* \oplus \Delta_2} \rightarrow$$

- ▶ Translate to a new wire offset

FleXOR

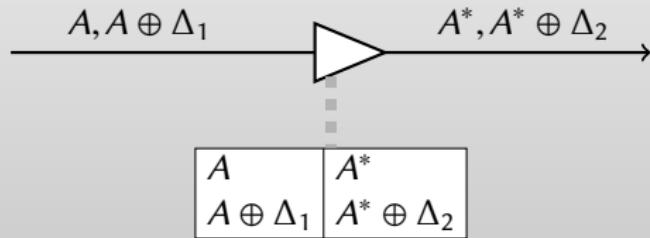
[KolesnikovMohasselRosulek14]



- ▶ Translate to a new wire offset (unary $a \mapsto a$ gate)

FleXOR

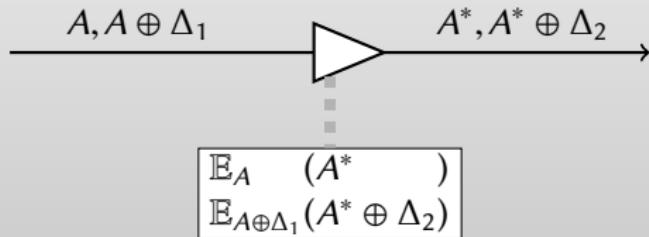
[KolesnikovMohasselRosulek14]



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FleXOR

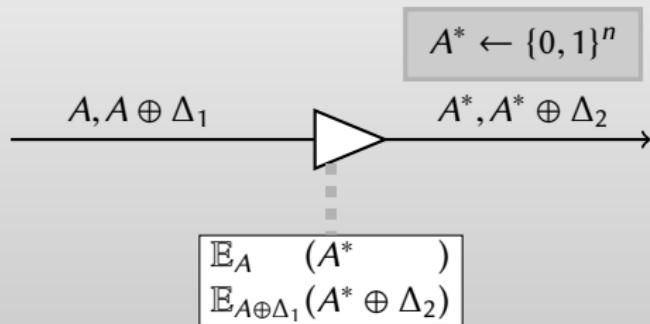
[KolesnikovMohasselRosulek14]



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FleXOR

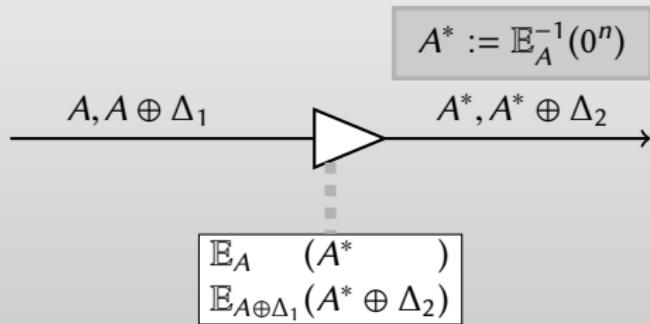
[KolesnikovMohasselRosulek14]



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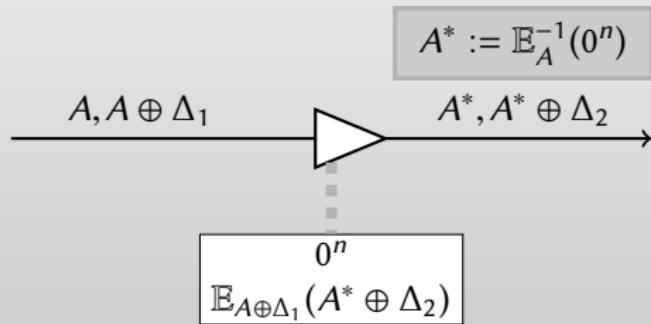
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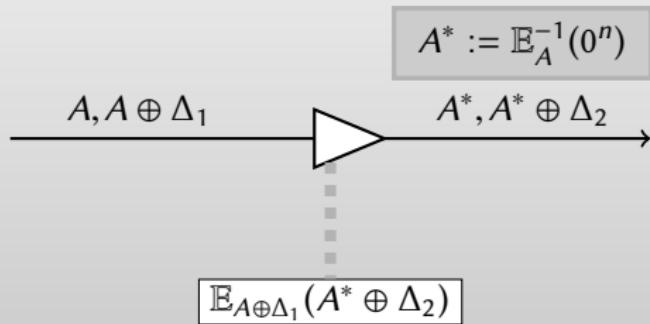
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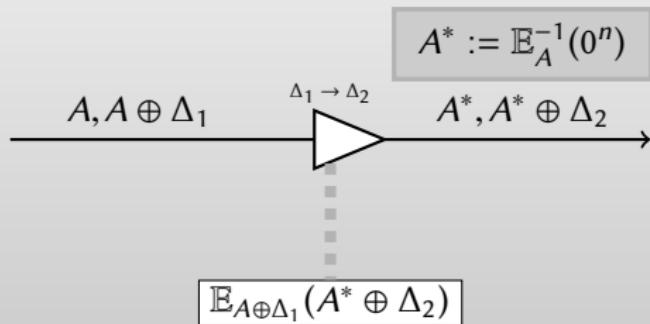
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- ▶ Translate to a new wire offset (unary $a \mapsto a$ gate) using 1 ciphertext

FleXOR

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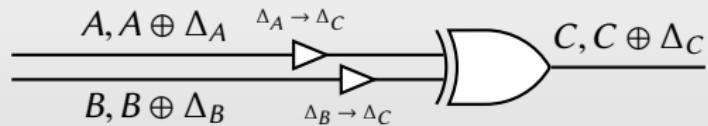
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[KolesnikovMohasselRosulek14]



FleXOR

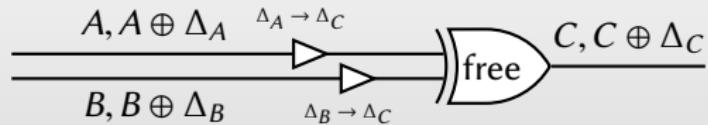
[KolesnikovMohasselRosulek14]



- ▶ Adjust inputs to target offset Δ_C (1 ciphertext each)

FleXOR

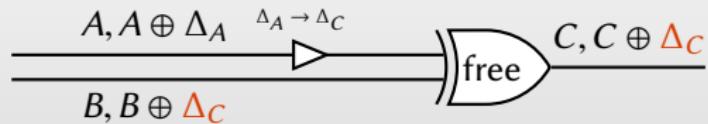
[KolesnikovMohasselRosulek14]



- ▶ Adjust inputs to target offset Δ_C (1 ciphertext each), then XOR is free

FleXOR

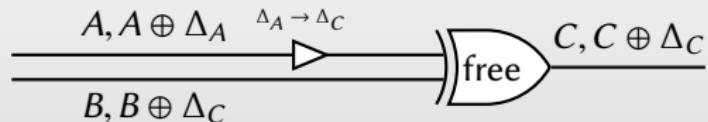
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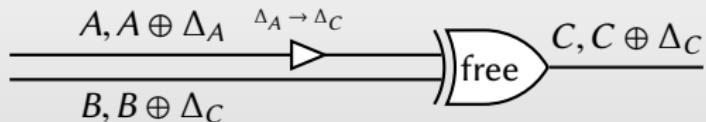
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[KolesnikovMohasselRosulek14]



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Combinatorial optimization problem: Choose an offset for each wire, minimizing total cost of XOR gates

- ▶ Subj. to compatibility with 2-ciphertext row-reduction of AND gates
- ▶ (or) Subj. to removing circularity property of free-XOR

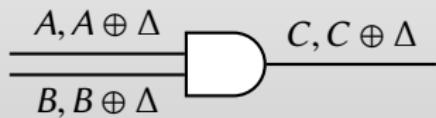
Scoreboard

	size ($\times \lambda$)		garble cost		eval cost		assumption
	XOR	AND	XOR	AND	XOR	AND	
Classical	large?		8		5		PKE
P&P	4	4	4/8	4/8	1/2	1/2	hash/PRF
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FleXOR	{0, 1, 2}	2	{0, 1, 2}	4	{0, 1, 2}	1	circ. hash

Half Gates

[ZahurRosulekEvans15]

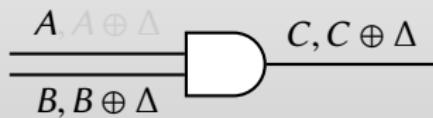
What if garbler knows in advance the truth value on one input wire?



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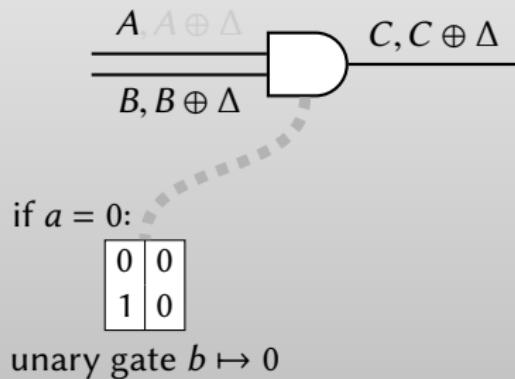
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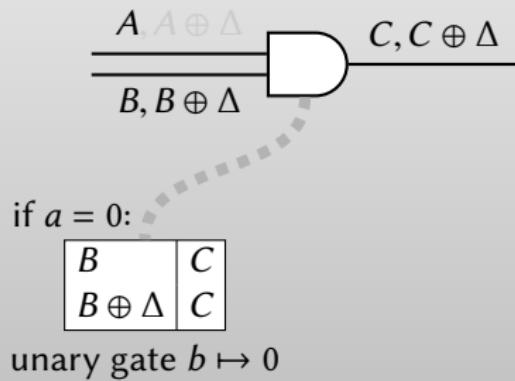
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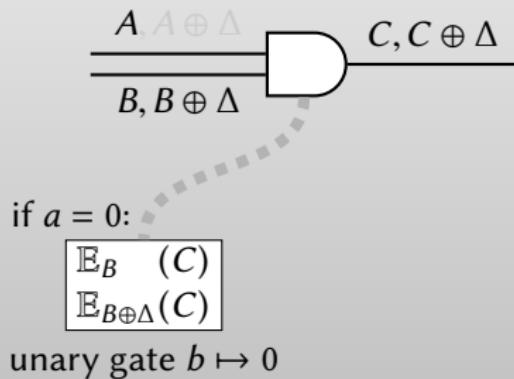
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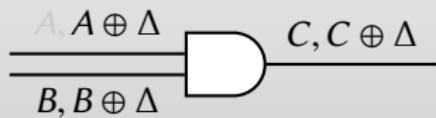
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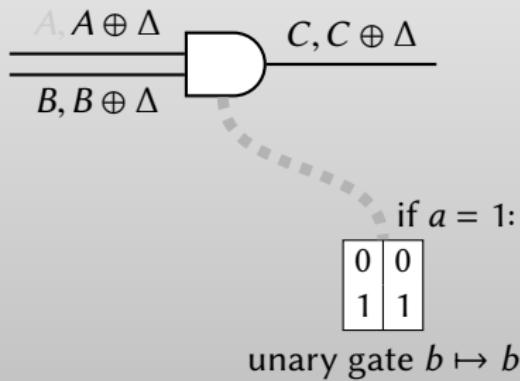
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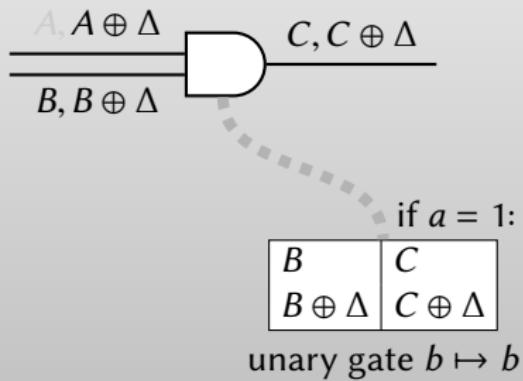
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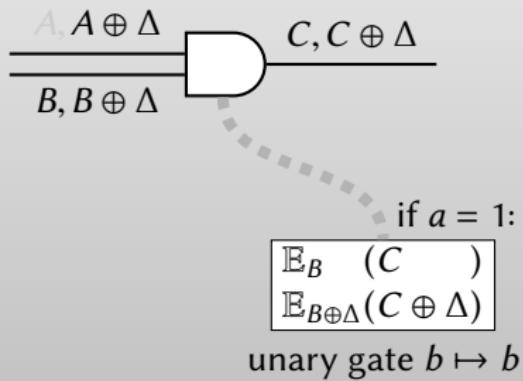
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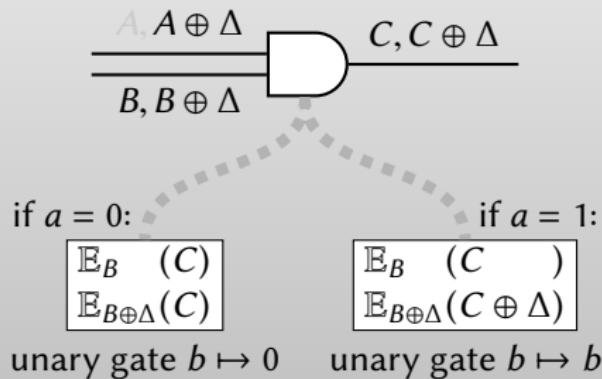
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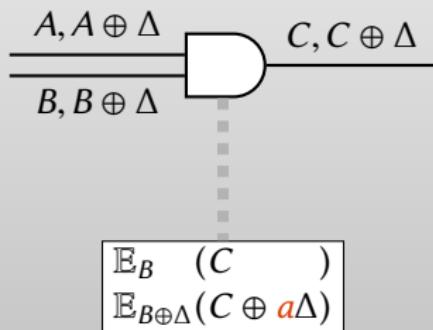
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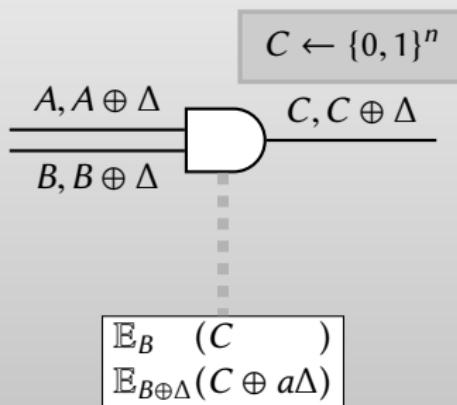
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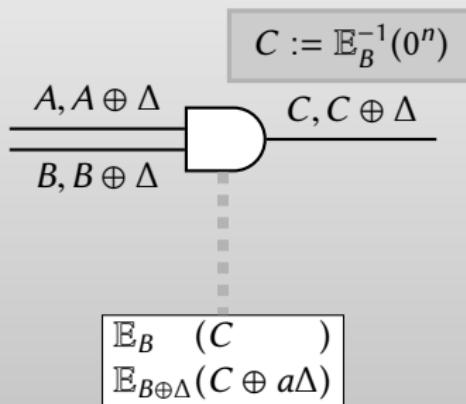
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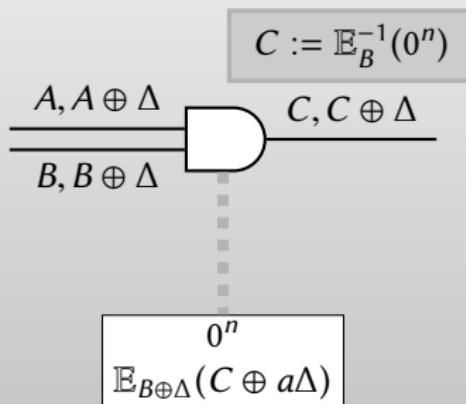
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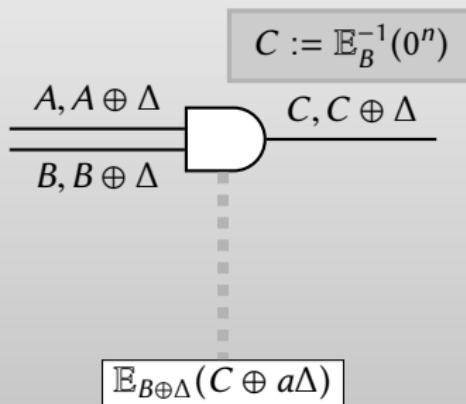
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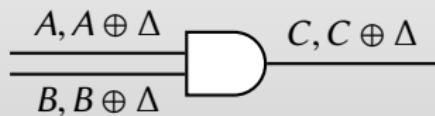


Fine print: permute ciphertexts with permute-and-point.

Half Gates

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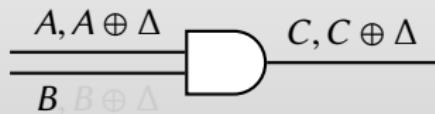
*What if **evaluator** knows the truth value on one input wire?*



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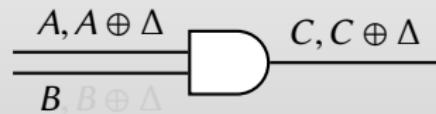
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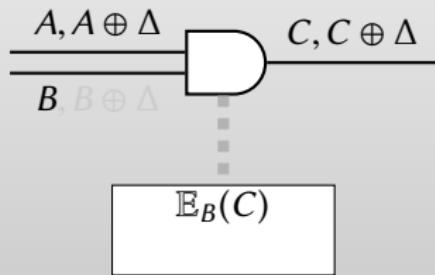
Evaluator has B (knows FALSE):

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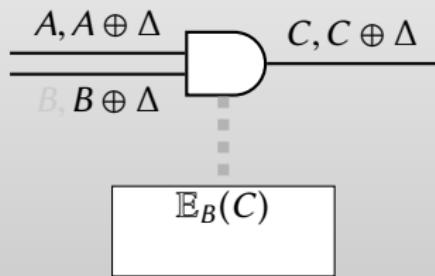
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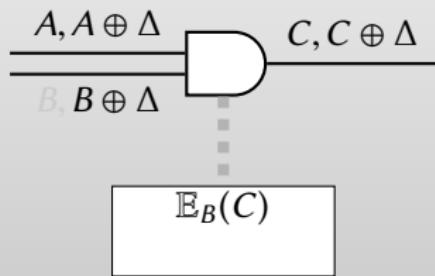
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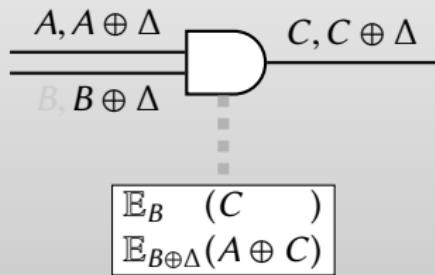
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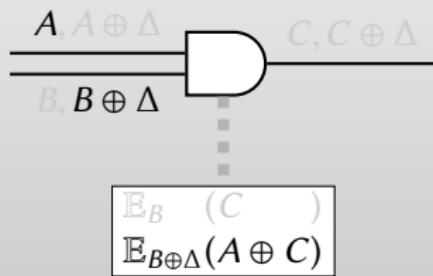
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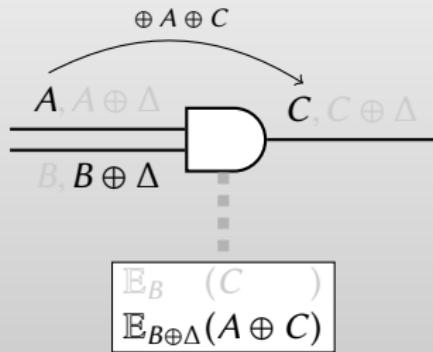
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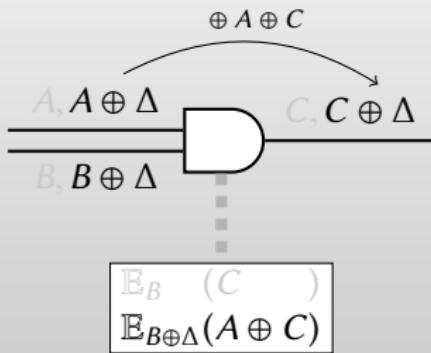
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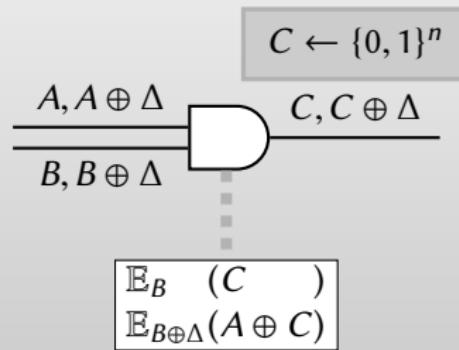
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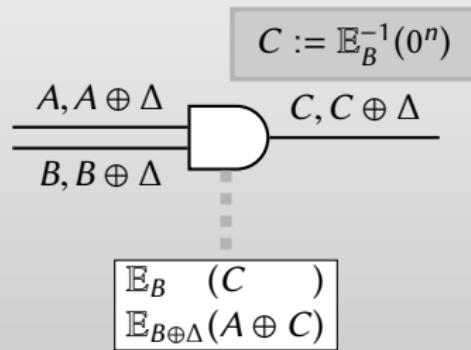
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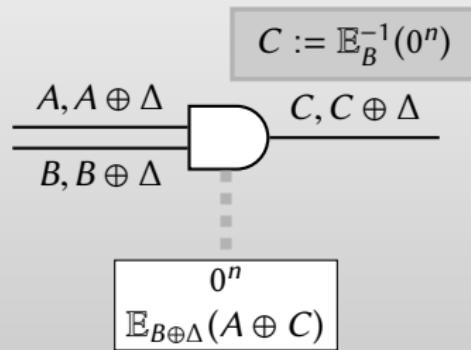
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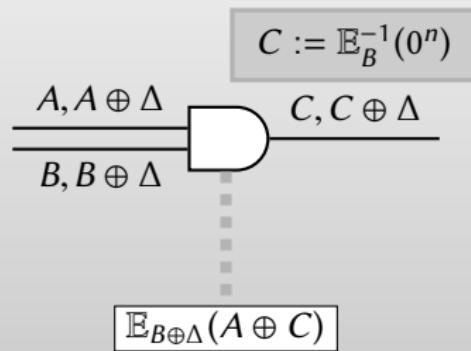
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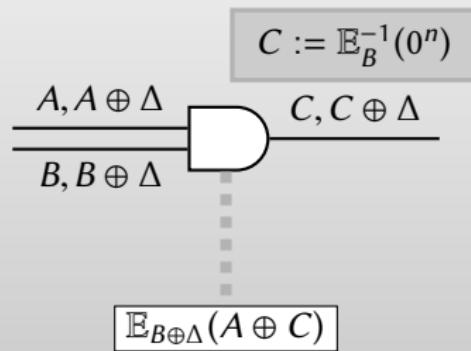
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Fine print: no need for permute-and-point here

Two halves make a whole!

$$a \wedge b$$

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$$a \wedge b = (a \oplus r \oplus r) \wedge b$$

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- ▶ Garbler chooses random bit r
 - ▶ r = color bit of FALSE wire label A
- ▶ Arrange for evaluator to learn $a \oplus r$ in the clear
 - ▶ $a \oplus r$ = color bit of wire label evaluator gets (A or $A \oplus \Delta$)
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Scoreboard

	size ($\times \lambda$)		garble cost		eval cost		assumption
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[XYZ26]?	0	< 2?	?	?	?	?	?

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Every practical garbling scheme is combination of:

- ▶ Calls to symmetric primitive (can be modeled as random oracle)
- ▶ $GF(2^\lambda)$ -linear operations (xOR, polynomial interpolation)

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Garbling a single AND gate requires 2 ciphertexts (2λ bits), if garbling scheme is “linear” in this sense.

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Theorem ([ZahurRosulekEvans15])

Garbling a single AND gate requires 2 ciphertexts (2λ bits), if garbling scheme is “linear” in this sense.

Half-gates construction is *size-optimal* among schemes that:

- ... use “known techniques”
- ... work gate-by-gate in { xor , AND , NOT } basis

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- 1: Consider larger “chunks” of circuit, beyond {XOR, AND, NOT} basis?

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- 2: Discover some clever non-linear approach to garbling?
- 3: Wait for break-even point for asymptotically superior methods?
- 4: Use weaker security when situation calls for it.

ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

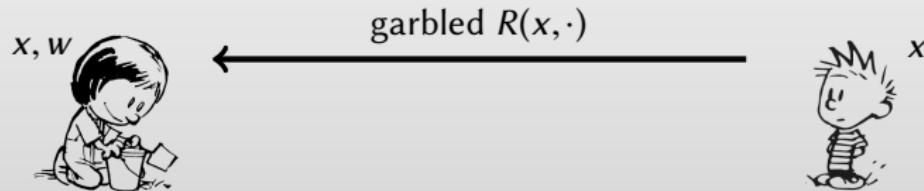
“ $\exists w : R(x, w) = 1$ ”



ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

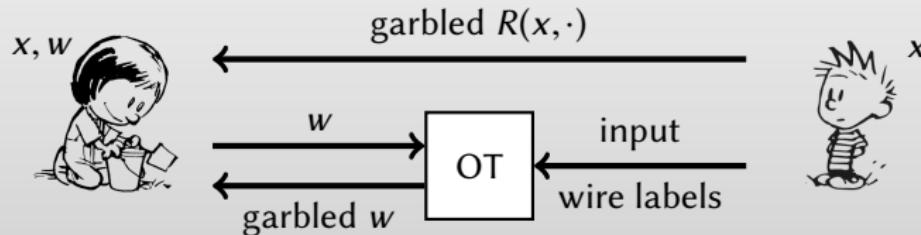
“ $\exists w : R(x, w) = 1$ ”



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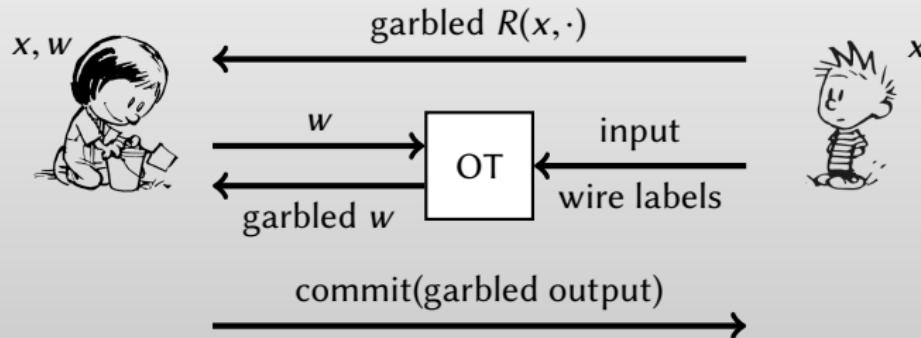
$$\exists w : R(x, w) = 1$$



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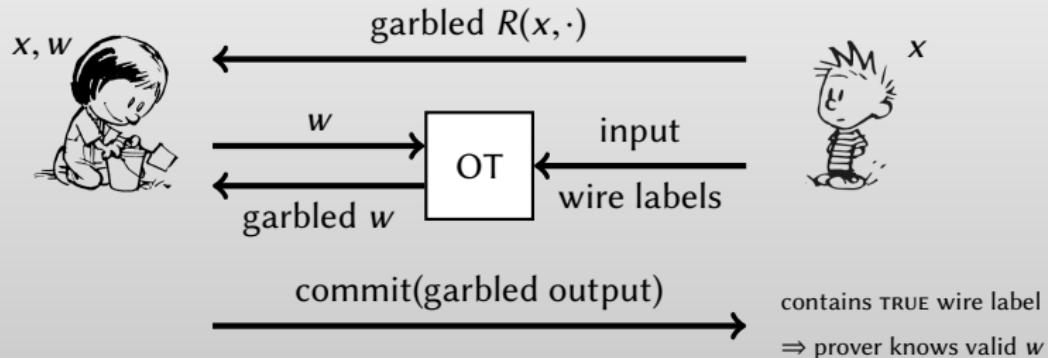
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ZK via garbled circuits

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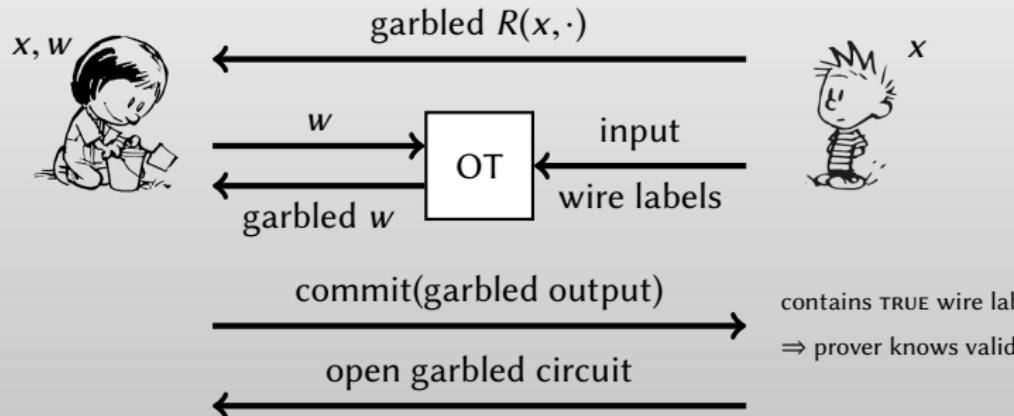
$$\exists w : R(x, w) = 1$$



ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

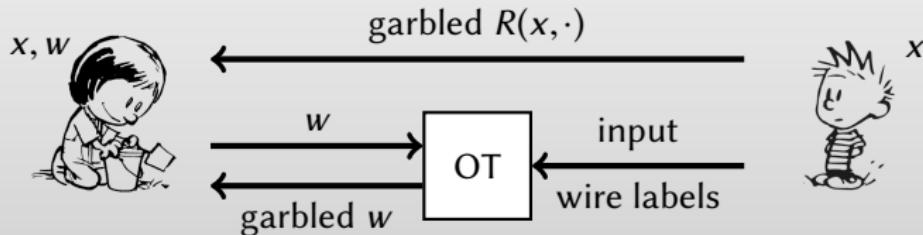
$$\exists w : R(x, w) = 1$$



ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

$$\exists w : R(x, w) = 1$$



commit(garbled output)

contains TRUE wire label
⇒ prover knows valid w

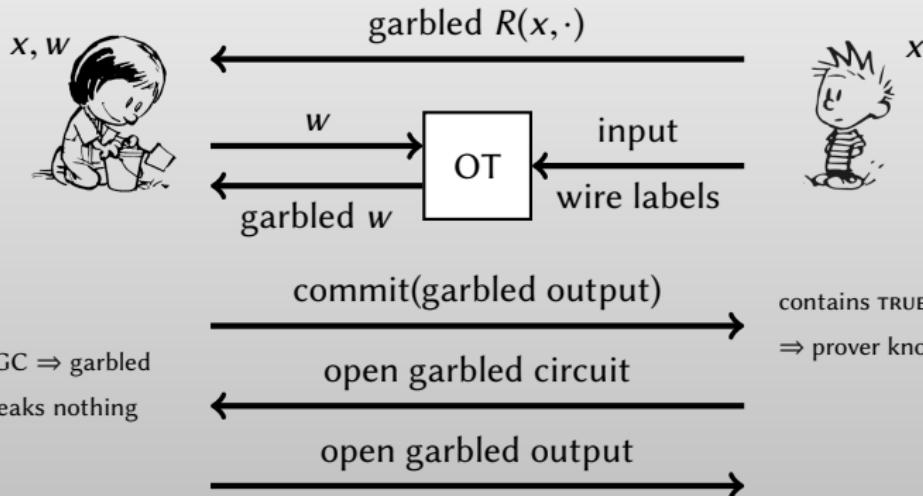
open garbled circuit

correct GC \Rightarrow garbled output leaks nothing about w

ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

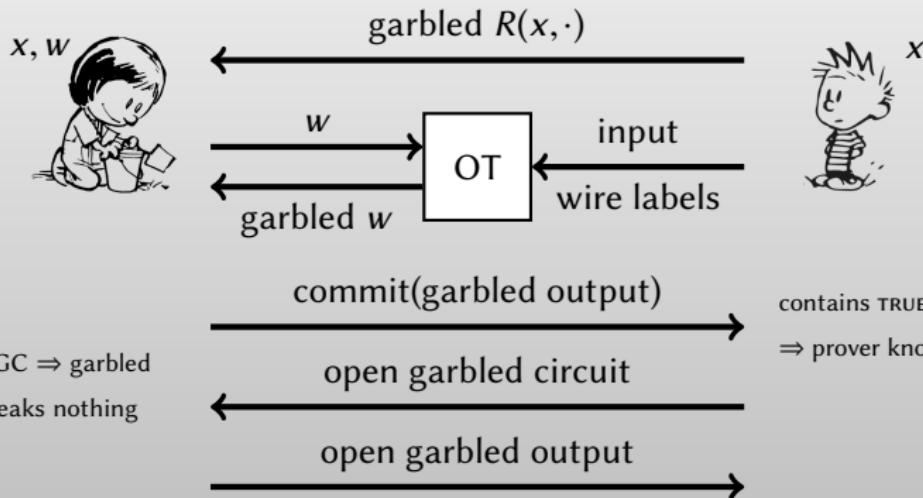
$$\exists w : R(x, w) = 1$$



ZK via garbled circuits

[JawurekKerschbaumOrlandi13]

$$\exists w : R(x, w) = 1$$



Prover knows entire input to garbled circuit!

Privacy-free garbling

[FrederiksenNielsenOrlandi15]

For this ZK protocol, garbled circuit does not require **privacy** property

- ▶ Only **authenticity** is needed
- ▶ Garbled circuits can be significantly smaller in this case

Privacy-free garbling

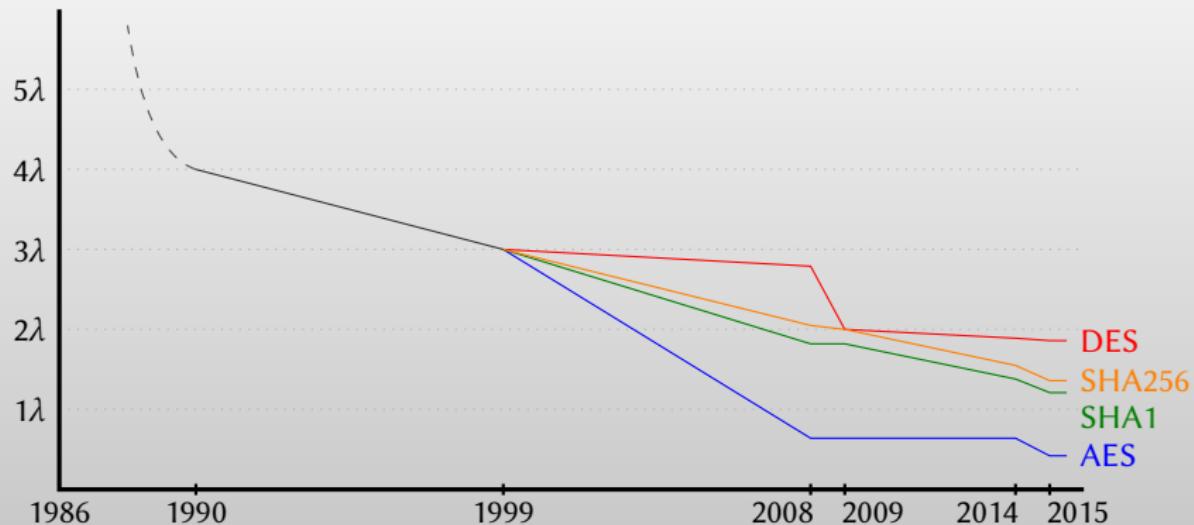
[FrederiksenNielsenOrlandi15]

For this ZK protocol, garbled circuit does not require **privacy** property

- ▶ Only **authenticity** is needed
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	size ($\times \lambda$)		garble cost		eval cost		assumption
	XOR	AND	XOR	AND	XOR	AND	
Classical	large?		8		5		PKE
P&P	4	4	4/8	4/8	1/2	1/2	hash/PRF
GRR3	3	3	4/8	4/8	1/2	1/2	hash/PRF
Free XOR	0	3	0	4	0	1	circ. hash
GRR2	2	2	4/8	4/8	1/2	1/2	hash/PRF
FleXOR	{0, 1, 2}	2	{0, 1, 2}	4	{0, 1, 2}	1	circ. hash
HalfGates	0	2	0	4	0	2	circ. hash
PrivFree *	0	1	0	2	0	1	circ. hash

A success story!



- ▶ Reduction in size by 10x
- ▶ Reduction in computation by 10000x

the end!

