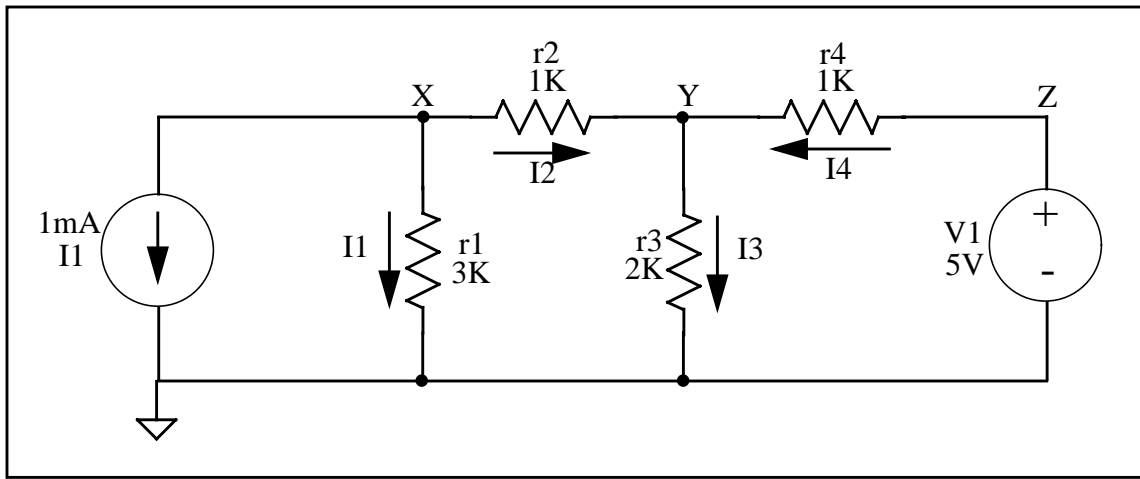


KCL Analysis - In Class Problem

Below is another circuit to analyze. The goal is to determine the voltage at nodes X and Y.



All the nodes have been named and all currents labeled. The common node is the is the ground node which usually appears as a common lead across the bottom of a circuit diagram.