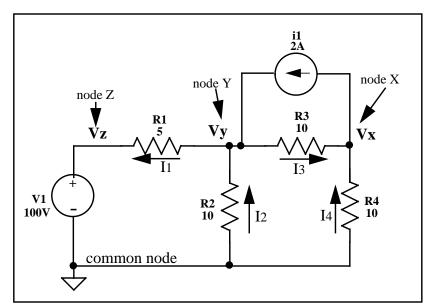
Using Spice for KCL Analysis

Below is a circuit analyzed by hand earlier. We shall now create a Spice netlist for this circuit and simulate it.



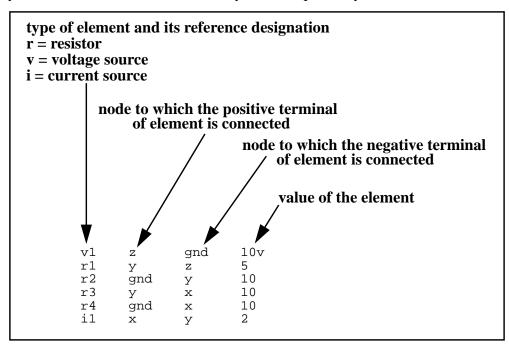
The Spice netlist for the circuit above is placed in a normal text file with whatever editor you like. It looks like this:

```
.title spice example for circuit kcl1.sp
.options
+ badchr=1 $detect bad chars
+ ingold=1 $combined exp and fixed output format
+ numdgt=4 $number of significant digits for output variables
$ netlist follows
v1
      z
             gnd
                    10v
r1
                    5
      У
              z
                    10
r2
      gnd
             У
r3
                    10
             х
      У
r4
                    10
      gnd
             х
i1
      х
             У
                    2
$ netlist done
.op $find dc operating point
.options post $save data for post processing
.end
```

The .options line sets up some convenient settings for your simulation. The "+" is the continuation character for continuing the .options selections.

The body of the netlist is shown below. A review of its format is given below. Note that nodes

Vx, Vy, and Vz have been abbreviated x, y and z respectively.



Hspice is invoked on the file kcll.sp at the unix prompt by typing: hspice kcll.sp > output

The results from the simulation are in the file *output*:

```
***** operating point information tnom = 25.000 temp = 25.000
node
        = voltage
                      node
                              = voltage
                                           node
                                                   = voltage
+0:x
          -5.7143
                      0:y
                               = 8.5714
                                           0:z
                                                   = 10.0000
        =
**** voltage sources
                                  Node voltages
subckt
element 0:v1
           10.0000
 volts
                      — Current is flowing out of "+" terminal of voltage source
           -0.2857 🗲
 current
            2.8571
 power
total voltage source power dissipation = 2.8571 watts
***** current sources
subckt
element 0:i1
 volts -14.2857
           2.0000
 current
           28.5714
 power
total current source power dissipation = 28.5714 watts
**** resistors
```

0:r1	0:r2	0:r3	0:r4
5.0000	10.0000	10.0000	10.0000
-1.4286	-8.5714	14.2857	5.7143
-0.2857	-0.8571	1.4286	0.5714
0.4082	7.3469	20.4082	3.2653
	5.0000 -1.4286 -0.2857	5.0000 10.0000 -1.4286 -8.5714 -0.2857 -0.8571	5.000010.000010.0000-1.4286-8.571414.2857-0.2857-0.85711.4286

We see that the node voltages are identical to the hand calculated values. The dc operating point analysis also gives the voltage across, current through, and power.