13. a) Rising edge shows a too-weak P-channel pullup 
   a falling edge is roughly correct so N-channel pulldown 
   resistance plus source terminating resistor \( \leq Z_0 \)

b) Opposite case of (a), P-channel \( R_{on} + R_t \leq Z_0 \), 
   and n-channel transistor is too weak

c) Both N and P output transistors \( R_{on} + R_t \leq Z_0 \) 
   correct operation

d) P-channel incident wave too big on rising edge, also 
   N channel falling edge equally too large + step. Suspect 
   external terminating resistor is too small.