2. [a] Draw the circuit corresponding to the code below:

```plaintext
if (en) { pop -> c, in
if (rise' in) { pop -> c, in
always @ (posedge c, in)
}

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3.

(a) Does it have a synchronous or asynchronous reset?
(b) How many states does it have?
(c) What output(s) is (are) formed from the present state?
(d) The present state vector is, how wide, assuming binary encoding?
(e) Does it have a synchronous or asynchronous reset?