

SIGGRAPH THINK BEYOND

Vulkan.

Introduction to the Vulkan Computer Graphics API

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

FULL.pptx mjb – July 24, 2020

Course Goals

- Give a sense of how Vulkan is different from OpenGL
- Show how to do basic drawing in Vulkan
- Leave you with working, documented sample code

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

Mike Bailey

- Professor of Computer Science, Oregon State University
- Has been in computer graphics for over 30 years
- Has had over 8,000 students in his university classes
- mjb@cs.oregonstate.edu

Welcome! I'm happy to be here. I hope you are too!




<http://cs.oregonstate.edu/~mjb/vulkan>

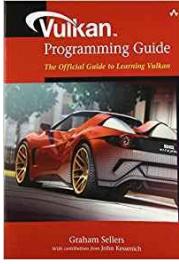
mjb – July 24, 2020

Sections

- Introduction
- Sample Code
- Drawing
- Shaders and SPIR-V
- Data Buffers
- GLFW
- GLM
- Instancing
- Graphics Pipeline Data Structure
- Descriptor Sets
- Textures
- Queues and Command Buffers
- Swap Chain
- Push Constants
- Physical Devices
- Logical Devices
- Dynamic State Variables
- Getting Information Back
- Compute Shaders
- Specialization Constants
- Synchronization
- Pipeline Barriers
- Multisampling
- Multipass
- Ray Tracing

mjb – July 24, 2020

My Favorite Vulkan Reference



Graham Sellers, *Vulkan Programming Guide*, Addison-Wesley, 2017.

Graham Sellers
With contributions from John Kremer

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

Vulkan.

Introduction

Mike Bailey
mjb@cs.oregonstate.edu

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

Acknowledgements



First of all, thanks to the inaugural class of 19 students who braved new, unrefined, and just-in-time course materials to take the first Vulkan class at Oregon State University – Winter Quarter, 2018. Thanks for your courage and patience!

Oregon State University

Second, thanks to NVIDIA for all of their support! 

Third, thanks to the Khronos Group for the great laminated Vulkan Quick Reference Cards! (Look at those happy faces in the photo holding them.)

SIGGRAPH THE LEARN BETTER

KHRONOS GROUP

mjb – July 24, 2020

History of Shaders

2004: OpenGL 2.0 / GLSL 1.10 includes Vertex and Fragment Shaders

2008: OpenGL 3.0 / GLSL 1.30 adds features left out before

2010: OpenGL 3.3 / GLSL 3.30 adds Geometry Shaders

2010: OpenGL 4.0 / GLSL 4.00 adds Tessellation Shaders

2012: OpenGL 4.3 / GLSL 4.30 adds Compute Shaders

2017: OpenGL 4.6 / GLSL 4.60

There is lots more detail at:
https://www.khronos.org/opengl/wiki/History_of_OpenGL

SIGGRAPH THE LEARN BETTER

mjb – July 24, 2020

History of Shaders

2014: Khronos starts Vulkan effort

2016: Vulkan 1.0

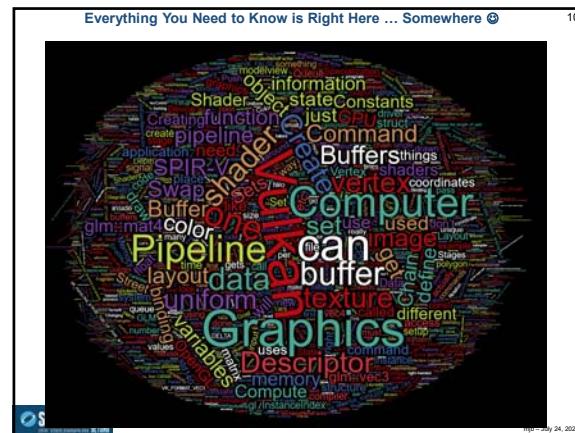
2016: Vulkan 1.1

2020: Vulkan 1.2

There is lots more detail at:
[https://en.wikipedia.org/wiki/Vulkan_\(API\)](https://en.wikipedia.org/wiki/Vulkan_(API))

SIGGRAPH THE LEARN BETTER

mjb – July 24, 2020



Top Three Reasons that Prompted the Development of Vulkan

1. Performance
2. Performance
3. Performance

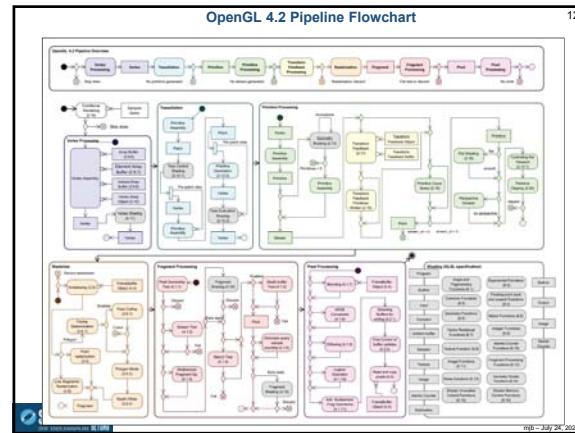
Vulkan is better at keeping the GPU busy than OpenGL is. OpenGL drivers need to do a lot of CPU work before handing work off to the GPU. Vulkan lets you get more power from the GPU card you already have.

This is especially important if you can hide the complexity of Vulkan from your customer base and just let them see the improved performance. Thus, Vulkan has had a lot of support and interest from game engine developers, 3rd party software vendors, etc.

As an aside, the Vulkan development effort was originally called "glNext", which created the false impression that this was a replacement for OpenGL. It's not.

SIGGRAPH THE LEARN BETTER

mjb – July 24, 2020



NVIDIA Titan V Specs vs. Titan Xp, 1080 Ti						
	Titan V	Titan V100	Titan P100	GTX 1080 Ti	GTX 1080	1080
GPU	GV100	GV100	GP100 Cut Down Pascal	GP102 Pascal	GP104-400 Pascal	
Transistor Count	21,188	21,188	15,380	12,728	7,228	
Flop Process	12nm FinFET	12nm FinFET	12nm FinFET	12nm FinFET	12nm FinFET	
CUDA Cores / Tensor Cores	5300 (1640)	5300 (1640)	3584 (8)	3584 (8)	2560 (8)	
TBUs	380	224	224	160		
SMs	7	96 (7)	88	64		
Clock	1200MHz	1200MHz	1200MHz	1200MHz	1200MHz	
Burst Clock	1450MHz	1370MHz	1400MHz	1400MHz	1730MHz	
FPE TFLOPs	1551.0PF	1410.0PF	10.871.0PF	-14.170.0PF	11.170.0PF	
Memory Type	HBM2	HBM2	HBM2	GeGDDR5X	GeGDDR5X	
Memory Capacity	15GB	16GB	16GB	11GB	8GB	
Memory Clock	1.70GHz	1.70GHz	1.70GHz	1.90GHz	1.90GHz	
Memory Bandwidth	307.04	408.04	408.04	302.44	250.44	
Memory Bandwidth	603.96GB/s	806.96GB/s	806.96GB/s	640.32GB/s	512.32GB/s	
Total Power Budget (TDP*)	250W	250W	300W	250W	180W	
Power Connectors	1x 6-pin	1x 6-pin	1x 8-pin	1x 8-pin	1x 8-pin	
Release Date	10/2016/2017	10/2016/2017	10/2016/2017	10/2016/2017	10/2016/2017	
Release Price	\$3000	\$10000		\$700	\$1000	

The table lists Titan V graphics cards as not targeted at gamers, but rather at scientific, medical, and machine learning applications. The GPU is a high-end product, so it's not surprising that it costs more than its predecessors. However, what makes it stand out is its capability of running games, which it does at a reasonable frame rate. The Titan V is also a derivative of the earlier released GP100 GPU, part of the Tesla architecture family. The key difference is that the Titan V has a much larger memory capacity and a higher clock speed. It also features 12nm FinFET technology, which provides better performance per milliwatt. The Titan V is designed for professional workloads, such as deep learning and scientific computing, but it can also handle some gaming tasks. Its memory capacity is 15GB, while the Titan Xp has 12GB. The Titan V also has a higher clock speed of 1200MHz compared to the Titan Xp's 1180MHz. The Titan V's memory bandwidth is 603.96GB/s, while the Titan Xp's is 512.32GB/s. The Titan V's power consumption is 250W, while the Titan Xp's is 200W. The Titan V's price is \$3000, while the Titan Xp's is \$10000.

Who was the original Vulcan?

The image features the Vulkan logo. At the top, the word "Vulkan." is written in a stylized, bold font where the letters are partially cut out, revealing a red background. Below this is a circular emblem containing a muscular white bull with a red circle on its forehead. To the left of the bull is a white rabbit, and to the right is a red dragon. The bottom part of the logo contains the words "INDUSTRY FORGED" in a bold, sans-serif font. In the bottom left corner, there is a small blue square with the SIGGRAPH logo and the word "SIGGRAPH".

Who is the Khronos Group?

16

The Khronos Group, Inc. is a non-profit member-funded industry consortium, focused on the creation of open standard, royalty-free application programming interfaces (APIs) for authoring and accelerated playback of dynamic media on a wide variety of platforms and devices. Khronos members may contribute to the development of Khronos API specifications, vote at various stages before public deployment, and accelerate delivery of their platforms and applications through early access to specification drafts and conformance tests.

The image displays a grid of 15 logos, each representing a different Khronos API or technology. The logos are arranged in three rows: the first row contains EGL, GLTF, and NNEF; the second row contains OpenGL, OpenGL SC, and OpenVG; the third row contains OpenXR, OpenVX, SPIR, SYCL, and Vulkan. The OpenGL logo is circled in red. The OpenVX, SPIR, and WebGL logos are also circled in red. The SIGGRAPH logo is located in the bottom left corner.

mp - July 24, 2020

PROMOTER MEMBERS

KHRONOS GROUP

Over 100 members worldwide
Any company is welcome to join

AMD Apple ARM EPIC GAMES Google Qualcomm

HUAWEI

imagination Intel NOKIA

SAMSUNG SONY VALVE

NVIDIA

VeriSilicon

3D Realtime Autodesk Adobe Amazon.com Axis Communications

Autodesk Infineon

BINOMIALS

BLIZZARD BROADCOM CEDARWILL cadence CANONICAL CEVA MUSICA codeplay

CORALINE CORSAIR DIGITAL STUDIO CAPTULIT omnis ETRI

CONTINENTAL HITACHI HYPERREAL

IGALIA Imperial College London

ITRI

KDAB KNU LG Linaro

LUMEN

METROW MAXON MUSICA Movidius mozilla

NEC NXP oculus PANASONIC PIXAR PLUTO

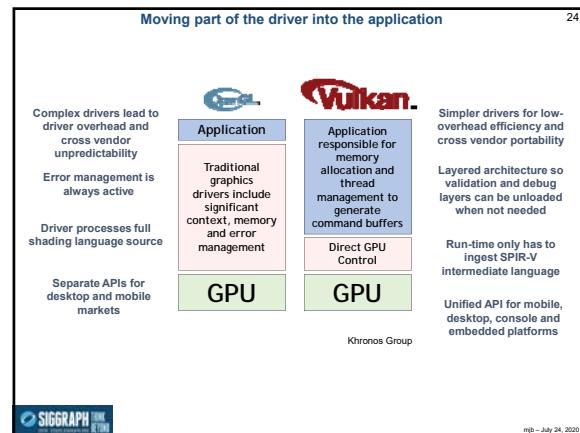
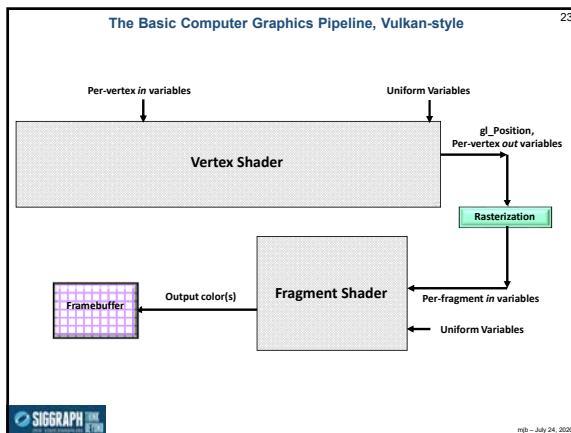
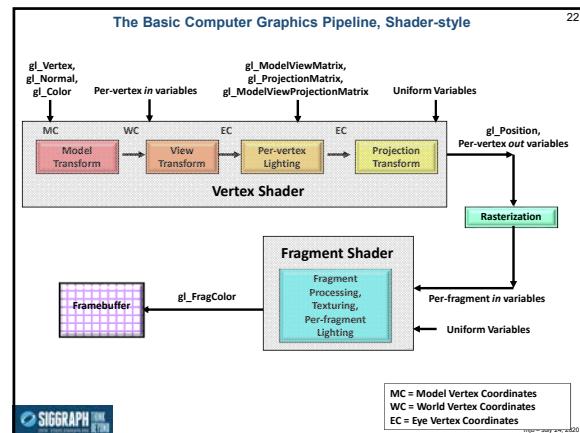
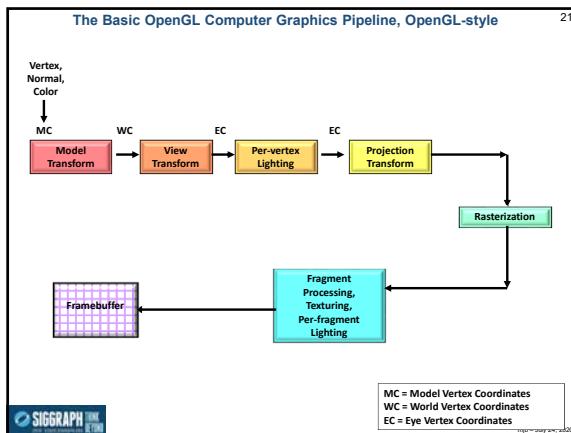
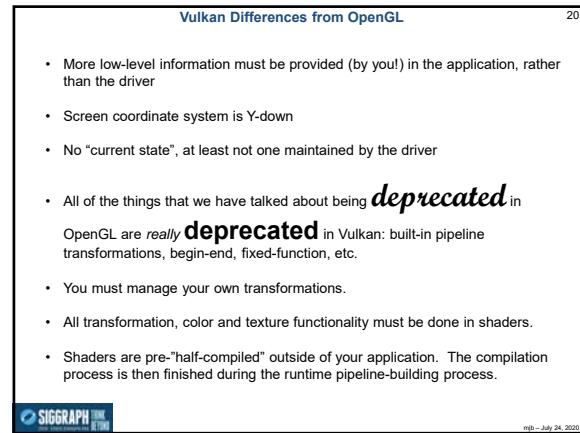
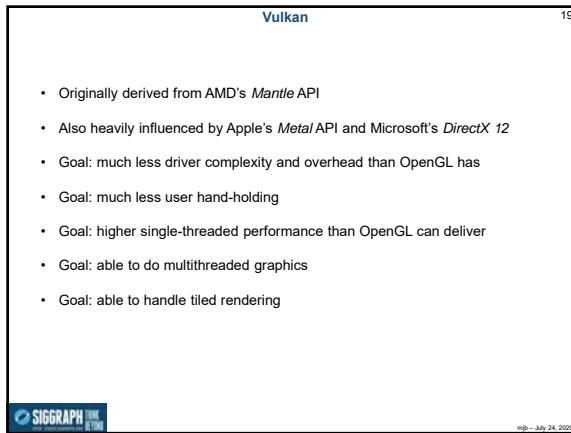
Razer Renesas Rockwell Collins SERES silicon studio

Synopsys TAKUMI THINCI VITUS VITUNIVERSE

Unity VITUNIVERSE VITUNIVERSE XILINX

SIGGRAPH

A collage of logos from various technology companies, including AMD, arm, Broadcom, Intel, NVIDIA, Qualcomm, Imagination Technologies, Alaris, Matrox, The Forge, id Tech, Int, SASCHI Williams, SOURCE, TORQUE, UBIQUITY, XENKO, and SIGGRAPH. The background is a dark blue gradient.



Vulkan Highlights: Command Buffers 25

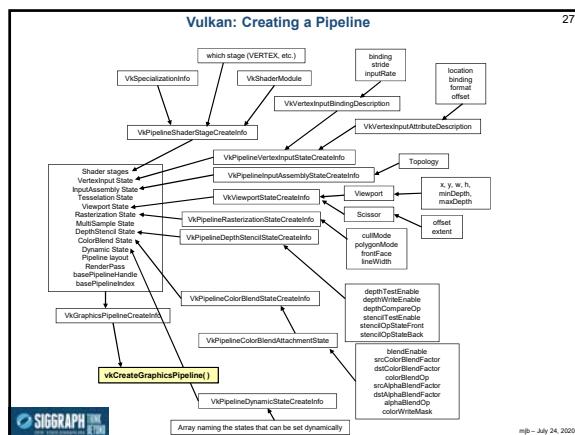
- Graphics commands are sent to command buffers
- E.g., `vkCmdDoSomething(cmdBuffer, ...);`
- You can have as many simultaneous Command Buffers as you want
- Buffers are flushed to Queues when the application wants them to be flushed
- Each command buffer can be filled from a different thread

mjb – July 24, 2020

Vulkan Highlights: Pipeline State Objects 26

- In OpenGL, your "pipeline state" is the combination of whatever your current graphics attributes are: color, transformations, textures, shaders, etc.
- Changing the state on-the-fly one item at-a-time is very expensive
- Vulkan forces you to set all your state variables at once into a "pipeline state object" (PSO) data structure and then invoke the entire PSO at once whenever you want to use that state combination
- Think of the pipeline state as being immutable.
- Potentially, you could have thousands of these pre-prepared pipeline state objects

mjb – July 24, 2020



Querying the Number of Something 28

```
uint32_t count;
result = vkEnumeratePhysicalDevices( Instance, OUT &count, OUT (VkPhysicalDevice *)nullptr );
```

```
VkPhysicalDevice * physicalDevices = new VkPhysicalDevice[ count ];
result = vkEnumeratePhysicalDevices( Instance, OUT &count, OUT physicalDevices );
```

This way of querying information is a recurring OpenCL and Vulkan pattern (get used to it):

How many total there are	Where to put them
result = vkEnumeratePhysicalDevices(Instance, &count,	nullptr);
result = vkEnumeratePhysicalDevices(Instance, &count,	physicalDevices);

mjb – July 24, 2020

Vulkan Code has a Distinct "Style" of Setting Information in structs and then Passing that Information as a pointer-to-the-struct 29

```
VkBufferCreateInfo vbc;
vbc.sType = VK_STRUCTURE_TYPE_BUFFER_CREATE_INFO;
vbc.pNext = nullptr;
vbc.flags = 0;
vbc.size = << buffer size in bytes >>;
vbc.usage = VK_USAGE_UNIFORM_BUFFER_BIT;
vbc.sharingMode = VK_SHARING_MODE_EXCLUSIVE;
vbc.queueFamilyIndexCount = 0;
vbc.pQueueFamilyIndices = nullptr;

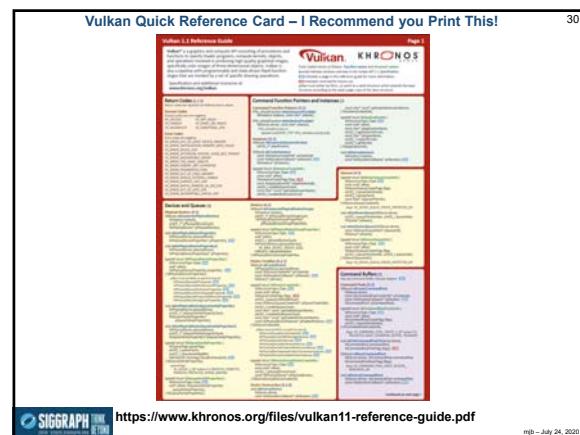
VK_RESULT result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &buffer );
```

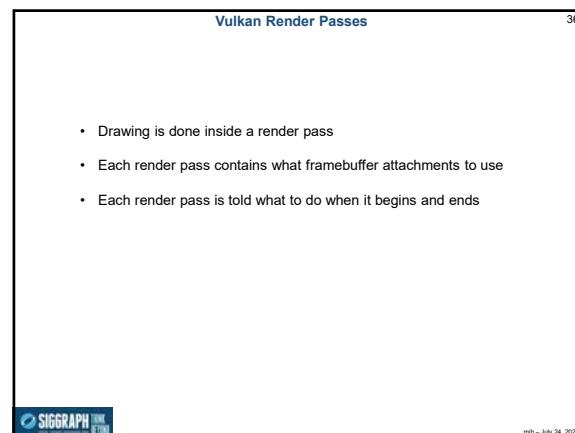
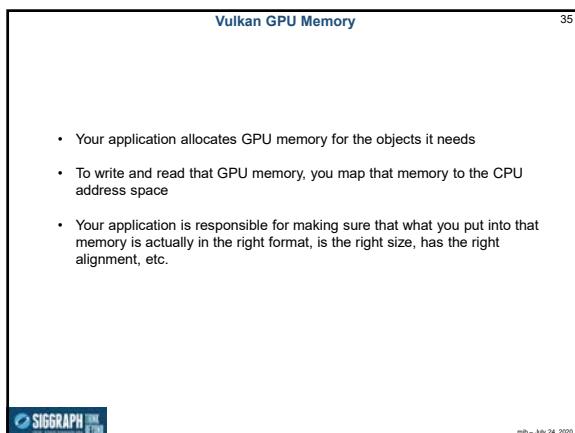
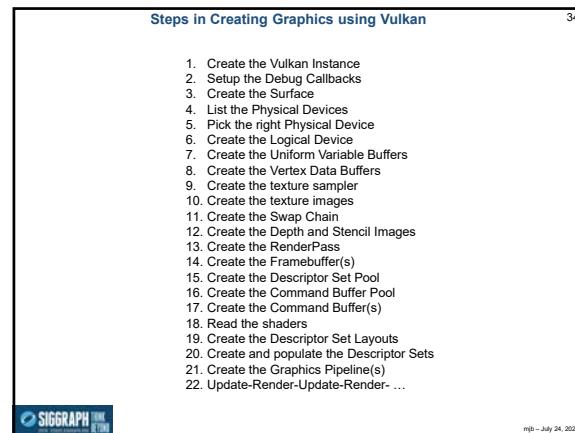
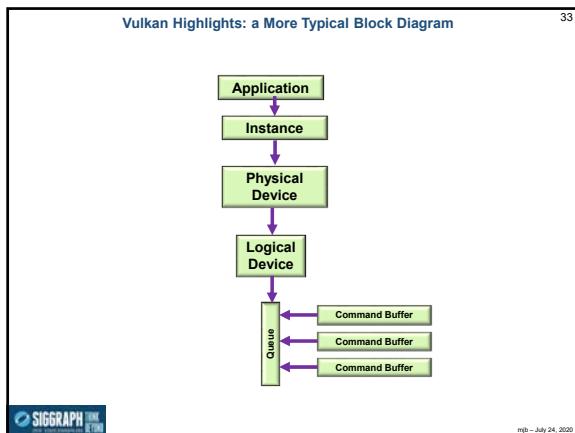
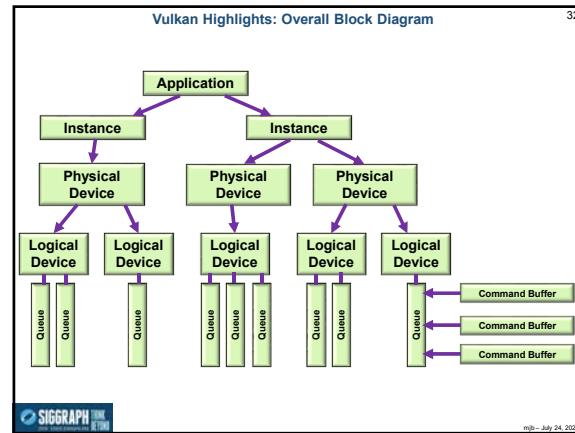
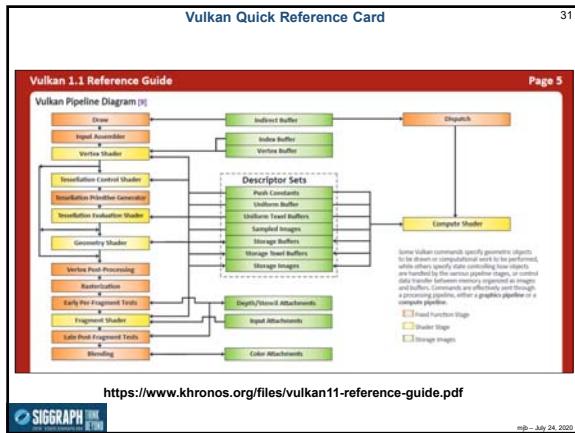
```
VkMemoryRequirements vmr;
result = vkGetBufferMemoryRequirements( LogicalDevice, Buffer, OUT &vmr ); // fills vmr
```

```
VkMemoryAllocateInfo vmai;
vmai.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
vmai.pNext = nullptr;
vmai.flags = 0;
vmai.allocationSize = vmr.size;
vmai.memoryTypeIndex = 0;

result = vkAllocateMemory( LogicalDevice, IN vmai, PALLOCATOR, OUT &matrixBufferMemoryHandle );
result = vkBindBufferMemory( LogicalDevice, Buffer, MatrixBufferMemoryHandle, 0 );
```

mjb – July 24, 2020





Vulkan Compute Shaders

37

- Compute pipelines are allowed, but they are treated as something special (just like OpenGL treats them)
- Compute passes are launched through dispatches
- Compute command buffers can be run asynchronously

The logo for SIGGRAPH Asia, featuring a stylized blue and white circular icon followed by the text "SIGGRAPH ASIA" in a bold, sans-serif font.

mjb - July 24, 2020

Vulkan Synchronization

41

The Vulkan Sample Code Included with These Notes

Mike Bailey

mjb@cs.oregonstate.edu

This work is licensed under a [Creative Commons
Attribution-NonCommercial-NoDerivatives 4.0
International License](#)

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb - July 24, 2020

The image displays two outputs from a computer program. The left output is a 3D surface plot of a color gradient, transitioning from red at the bottom to blue at the top, set against a black background. The right output is a distorted image of a dog's face, appearing as if it is being viewed through a prism or a curved lens, with multiple overlapping versions of the dog's eyes and nose.

Sample Program Keyboard Inputs		43
'L', 'L':	Toggle lighting off and on	
'm', 'M':	Toggle display mode (textures vs. colors, for now)	
'p', 'P':	Pause the animation	
'q', 'Q':	quit the program	
Esc:	quit the program	
'r', 'R':	Toggle rotation-animation and using the mouse	
'I', 'I':	Toggle using a vertex buffer only vs. an index buffer (in the index buffer version)	
'1', '4', '9'	Set the number of instances (in the instancing version)	

Caveats on the Sample Code, I		44
1.	I've written everything out in appalling longhand.	
2.	Everything is in one .cpp file (except the geometry data). It really should be broken up, but this way you can find everything easily.	
3.	At times, I could have hidden complexity, but I didn't. At all stages, I have tried to err on the side of showing you <i>everything</i> , so that nothing happens in a way that's kept a secret from you.	
4.	I've setup Vulkan structs every time they are used, even though, in many cases (most?), they could have been setup once and then re-used each time.	
5.	At times, I've setup things that didn't need to be setup just to show you what could go there.	

Caveats on the Sample Code, II		45
6.	There are great uses for C++ classes and methods here to hide some complexity, but I've not done that.	
7.	I've typedef'ed a couple things to make the Vulkan phraseology more consistent.	
8.	Even though it is not good software style, I have put persistent information in global variables, rather than a separate data structure	
9.	At times, I have copied lines from vulkan.h into the code as comments to show you what certain options could be.	
10.	I've divided functionality up into the pieces that make sense to me. Many other divisions are possible. Feel free to invent your own.	

Main Program		46
<pre>int main(int argc, char* argv[]) { Width = 800; Height = 600; errno_t err = fopen_s(&FpDebug, DEBUGFILE, "w"); if(err != 0) { fprintf(stderr, "Cannot open debug print file %s\n", DEBUGFILE); FpDebug = stderr; } fprintf(FpDebug, "#FpDebug: Width = %d ; Height = %d\n", Width, Height); Reset(); InitGraphics(); // loop until the user closes the window. while(glfwWindowShouldClose(MainWindow) == 0) { glfwPollEvents(); Time = glfwGetTime(); // elapsed time, in double-precision seconds UpdateScene(); RenderScene(); } fprintf(FpDebug, "Closing the GLFW window\n"); vkQueueWaitIdle(Queue); vkDeviceWaitIdle(LogicalDevice); DestroyAVulkan(); glfwDestroyWindow(MainWindow); glfwTerminate(); return 0; }</pre>		

InitGraphics(), I		47
<pre>void InitGraphics() { HERE_I_AM("InitGraphics"); VkResult result = VK_SUCCESS; Init01Instance(); InitGLFW(); Init02CreateDebugCallbacks(); Init03PhysicalDeviceAndGetQueueFamilyProperties(); Init04LogicalDeviceAndQueue(); Init05UniformBuffer(sizeof(Matrices), &MyMatrixUniformBuffer); Fill05DataBuffer(MyMatrixUniformBuffer, (void *) &Matrices); Init05UniformBuffer(sizeof(Light), &MyLightUniformBuffer); Fill05DataBuffer(MyLightUniformBuffer, (void *) &Light); Init05MyVertexDataBuffer(sizeof(VertexData), &MyVertexDataBuffer); Fill05DataBuffer(MyVertexDataBuffer, (void *) VertexData); Init06CommandPool(); Init06CommandBuffers(); }</pre>		

InitGraphics(), II		48
<pre>Init07TextureSampler(&MyPuppyTexture.texSampler); Init07TextureBufferAndFillFromBmpFile("puppy.bmp", &MyPuppyTexture); Init08Swapchain(); Init09DepthStencilImage(); Init10RenderPasses(); Init11Framebuffers(); Init12SpirvShader("sample-vert.spv", &ShaderModuleVertex); Init12SpirvShader("sample-frag.spv", &ShaderModuleFragment); Init13DescriptorSetPool(); Init13DescriptorGetLayouts(); Init13DescriptorSets(); Init14GraphicsVertexFragmentPipeline(ShaderModuleVertex, ShaderModuleFragment, VK_PRIMITIVE_TOPOLOGY_TRIANGLE_LIST, &GraphicsPipeline); }</pre>		

A Colored Cube

```

static GLfloat CubeColors[ ][3] =
{
    {0.0, 0.0}, {1.0, 0.0}, {0.0, 1.0}, {1.0, 1.0}, {0.0, 1.0}, {1.0, 0.0}, {0.0, 1.0}, {1.0, 1.0}
};

static GLuint CubeTriangleIndices[ ][3] =
{
    {0, 2, 3}, {0, 1, 2}, {4, 5, 7}, {4, 5, 6}, {1, 3, 7}, {1, 3, 5}, {0, 4, 5}, {0, 4, 6}, {2, 6, 7}, {2, 6, 3}, {0, 1, 5}, {0, 5, 4}
};

```

SIGGRAPH

mjb – July 24, 2020

A Colored Cube

```

struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec3 color;
    glm::vec2 texCoord;
};

struct vertex VertexData[] =
{
    // triangle 0-2-3:
    // vertex #0:
    {
        {-1., -1., -1.}, {0., 0., -1.}, {0., 0., 0.}, {1., 0.}
    },
    // vertex #2:
    {
        {-1., 1., -1.}, {0., 0., -1.}, {0., 1., 0.}, {1., 1.}
    },
    // vertex #3:
    {
        {1., 1., -1.}, {0., 0., -1.}, {0., 1., 0.}, {0., 1.}
    }
};

```

SIGGRAPH

mjb – July 24, 2020

The Vertex Data is in a Separate File

#include "SampleVertexData.cpp"

```

struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec3 color;
    glm::vec2 texCoord;
};

struct vertex VertexData[] =
{
    // triangle 0-2-3:
    // vertex #0:
    {
        {-1., -1., -1.}, {0., 0., -1.}, {0., 0., 0.}, {1., 0.}
    },
    // vertex #2:
    {
        {-1., 1., -1.}, {0., 0., -1.}, {0., 1., 0.}, {1., 1.}
    },
    ...
};

```

SIGGRAPH

mjb – July 24, 2020

What if you don't need all of this information?

```

struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec3 color;
    glm::vec2 texCoord;
};

```

For example, what if you are not doing texturing in this application? Should you re-do this struct and leave the texCoord element out?

As best as I can tell, the only costs for retaining vertex attributes that you aren't going to use are some GPU memory space and possibly some inefficient uses of the cache, but not gross performance. So, I recommend keeping this struct intact, and, if you don't need texturing, simply don't use the texCoord values in your vertex shader.

SIGGRAPH

mjb – July 24, 2020

Vulkan Software Philosophy

Vulkan has lots of typedefs that define C/C++ structs and enums

Vulkan takes a non-C++ object-oriented approach in that those typedefed structs pass all the necessary information into a function. For example, where we might normally say in C++:

```

result = LogicalDevice->vkGetDeviceQueue( queueFamilyIndex, queueIndex, OUT &Queue );

```

we would actually say in C:

```

result = vkGetDeviceQueue( LogicalDevice, queueFamilyIndex, queueIndex, OUT &Queue );

```

SIGGRAPH

mjb – July 24, 2020

Vulkan Conventions

VkXxx is a typedef, probably a struct

vkYyy() is a function call

VK_ZZZ is a constant

My Conventions

"Init" in a function call name means that something is being setup that only needs to be setup once

The number after "Init" gives you the ordering

In the source code, after main() comes InitGraphics(), then all of the InitXXYY() functions in numerical order. After that comes the helper functions

"Find" in a function call name means that something is being looked for

"Fill" in a function call name means that some data is being supplied to Vulkan

"IN" and "OUT" ahead of function call arguments are just there to let you know how an argument is going to be used by the function. Otherwise, IN and OUT have no significance. They are actually #define'd to nothing.

SIGGRAPH

mjb – July 24, 2020

Querying the Number of Something and Allocating Enough Structures to Hold Them All

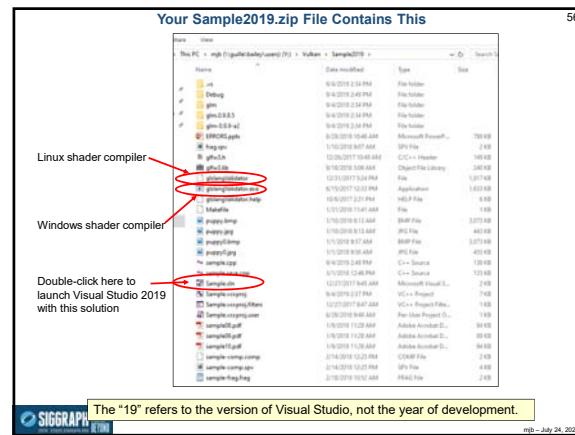
```
uint32_t count;
result = vkEnumeratePhysicalDevices( Instance, OUT &count, OUT (VkPhysicalDevice *)nulptr );

VkPhysicalDevice * physicalDevices = new VkPhysicalDevice[ count ];
result = vkEnumeratePhysicalDevices( Instance, OUT &count, OUT &physicalDevices[0] );
```

This way of querying information is a recurring OpenCL and Vulkan pattern (get used to it):

How many total there are	Where to put them
<code>result = vkEnumeratePhysicalDevices(Instance, &count, nulptr);</code>	<code>result = vkEnumeratePhysicalDevices(Instance, &count, &physicalDevices[0];)</code>

mjb – July 24, 2020



Reporting Error Results, I

```
struct ErrorCode
{
    VkResult resultCode;
    std::string meaning;
};

ErrorCode[] =
{
    { VK_NOT_READY, "Not Ready" },
    { VK_TIMEOUT, "Timeout" },
    { VK_EVENT_SET, "Event Set" },
    { VK_EVENT_RESET, "Event Reset" },
    { VK_INCOMPLETE, "Incomplete" },
    { VK_ERROR_OUT_OF_HOST_MEMORY, "Out of Host Memory" },
    { VK_ERROR_OUT_OF_DEVICE_MEMORY, "Out of Device Memory" },
    { VK_ERROR_INITIALIZATION_FAILED, "Initialization Failed" },
    { VK_ERROR_DEVICE_LOST, "Device Lost" },
    { VK_ERROR_LAYER_NOT_PRESENT, "Layer Not Present" },
    { VK_ERROR_EXTENSION_NOT_PRESENT, "Extension Not Present" },
    { VK_ERROR_FEATURE_NOT_PRESENT, "Feature Not Present" },
    { VK_ERROR_INCOMPATIBLE_DRIVER, "Incompatible Driver" },
    { VK_ERROR_TOO_MANY_OBJECTS, "Too Many Objects" },
    { VK_ERROR_FRAGMENTED_POOL, "Fragmented Pool" },
    { VK_ERROR_SURFACE_LOST_KHR, "Surface Lost" },
    { VK_ERROR_NATIVE_WINDOW_IN_USE_KHR, "Native Window in Use" },
    { VK_SUBOPTIMAL_KHR, "Suboptimal" },
    { VK_ERROR_OUT_OF_DATE_KHR, "Out of Date" },
    { VK_ERROR_INCOMPATIBLE_DISPLAY_KHR, "Incompatible Display" },
    { VK_ERROR_VALIDATION_FAILED_EXT, "Validation Failed" },
    { VK_ERROR_INVALID_SHADER_NV, "Invalid Shader" },
    { VK_ERROR_OUT_OF_POOL_MEMORY_KHR, "Out of Pool Memory" },
    { VK_ERROR_INVALID_EXTERNAL_HANDLE, "Invalid External Handle" }
}
```

mjb – July 24, 2020

Reporting Error Results, II

```
void PrintVkError( VkResult result, std::string prefix )
{
    if(Verbose && result == VK_SUCCESS)
    {
        fprintf(FpDebug, "%s: %s\n", prefix.c_str(), "Successful");
        fflush(FpDebug);
        return;
    }

    const int numErrorCodes = sizeof( ErrorCode ) / sizeof( struct ErrorCode );
    std::string meaning = "";
    for( int i = 0; i < numErrorCodes; i++ )
    {
        if( result == ErrorCode[i].resultCode )
        {
            meaning = ErrorCode[i].meaning;
            break;
        }
    }

    fprintf( FpDebug, "\n%s: %s\n", prefix.c_str(), meaning.c_str() );
    fflush(FpDebug);
}
```

mjb – July 24, 2020

Extras in the Code

```
#define REPORT(s) { PrintVkError(result, s); fflush(FpDebug); }

#define HERE_I_AM(s) if(Verbose) { fprintf( FpDebug, "***** %s *****\n", s ); fflush(FpDebug); }

bool Paused;
bool Verbose;

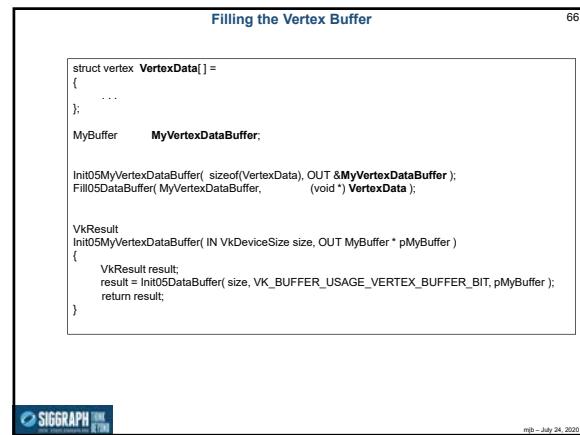
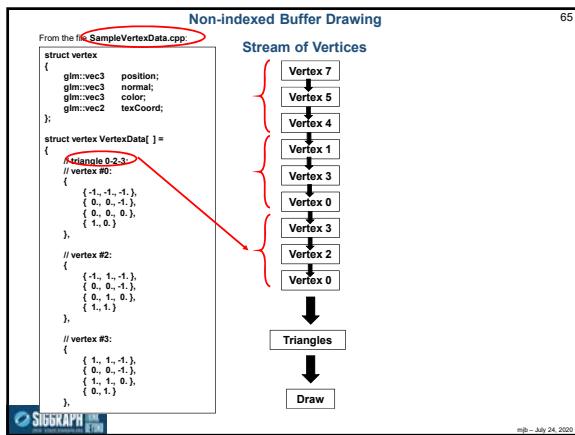
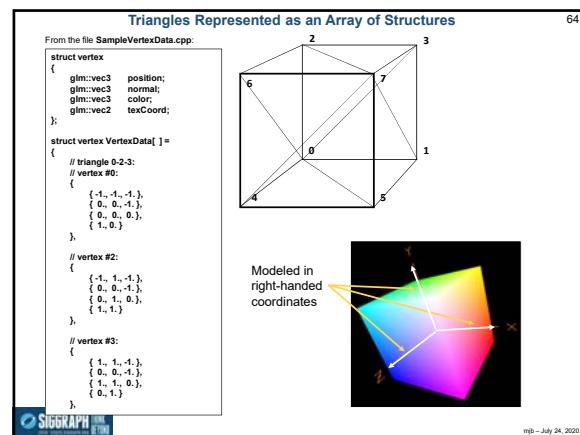
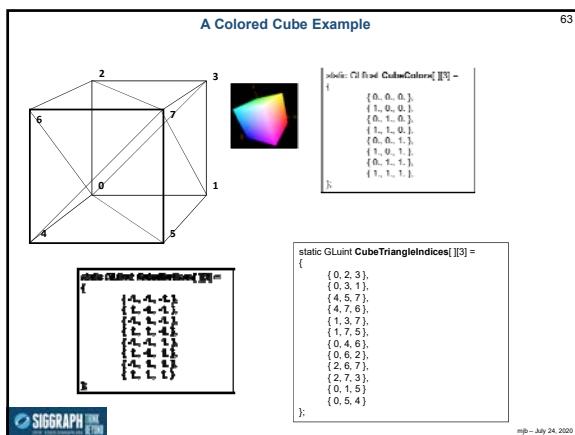
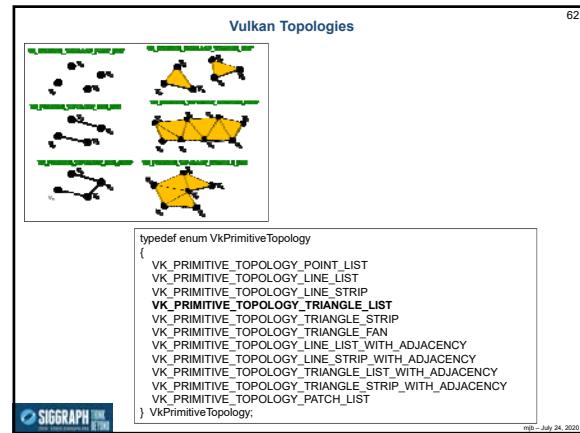
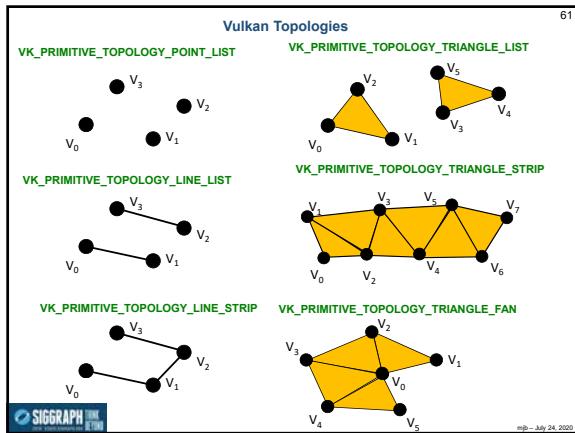
#define DEBUGFILE "VulkanDebug.txt"

errno_t err = fopen_s(&FpDebug, DEBUGFILE, "w" );

const int32_t OFFSET_ZERO =;
```

mjb – July 24, 2020





A Preview of What `Init5DataBuffer` Does

```

VkResult
Init5DataBuffer( VkDeviceSize size, VkBufferUsageFlags usage, OUT MyBuffer * pMyBuffer )
{
    VkResult result = VK_SUCCESS;
    VkBufferCreateInfo vbc;
    vbc.sType = VK_STRUCTURE_TYPE_BUFFER_CREATE_INFO;
    vbc.pNext = NULL;
    vbc.flags = 0;
    vbc.size = pMyBuffer->size;
    vbc.usage = usage;
    vbc.sharingMode = VK_SHARING_MODE_EXCLUSIVE;
    vbc.queueFamilyIndexCount = (const uint32_t *)NULL;
    vbc.pQueueFamilyIndices = (const uint32_t *)NULL;
    result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &pMyBuffer->buffer );

    VkMemoryRequirements vmr;
    vkGetBufferMemoryRequirements( LogicalDevice, IN pMyBuffer->buffer, OUT &vmr ); // fills vmr

    VkMemoryAllocateInfo vma;
    vma.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
    vma.pNext = NULL;
    vma.allocationSize = vmr.size;
    vma.memoryTypeIndex = FindMemory( vmaHostVisible );
    vma.alignment = 1;

    VkDeviceMemory vdm;
    result = vkAllocateMemory( LogicalDevice, IN &vma, PALLOCATOR, OUT &vdm );
    pMyBuffer->vdm = vdm;

    result = vBindBufferMemory( LogicalDevice, pMyBuffer->buffer, IN vdm, 0 ); // 0 is the offset
    return result;
}

```

mjb – July 24, 2020

Telling the Pipeline about its Input

We will come to the Pipeline later, but for now, know that a Vulkan pipeline is essentially a very large data structure that holds (what OpenGL would call) the state, including how to parse its input.

C/C++:

```

struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec3 color;
    glm::vec2 texCoord;
};

VkVertexInputBindingDescription vvibd[1]; // one of these per buffer data buffer
vvibd[0].binding = 0; // which binding # this is
vvibd[0].stride = sizeof( struct vertex ); // bytes between successive structs
vvibd[0].inputRate = VK_VERTEX_INPUT_RATE_VERTEX;

```

GLSL Shader:

```

layout( location = 0 ) in vec3 vertex;
layout( location = 1 ) in vec3 aNormal;
layout( location = 2 ) in vec3 aColor;
layout( location = 3 ) in vec2 aTexCoord;

```

mjb – July 24, 2020

Telling the Pipeline about its Input

C/C++:

```

struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec3 color;
    glm::vec2 texCoord;
};

VkVertexInputAttributeDescription vviad[4]; // array per vertex input attribute
// 4 = vertex, normal, color, texture coord
vviad[0].location = 0; // location in the layout decoration
vviad[0].binding = 0; // which binding description this part of
vviad[0].format = VK_FORMAT_VEC3; // x, y, z
vviad[0].offset = offsetof( struct vertex, position ); // 0

vviad[1].location = 1;
vviad[1].binding = 0;
vviad[1].format = VK_FORMAT_VEC3; // nx, ny, nz
vviad[1].offset = offsetof( struct vertex, normal ); // 12

vviad[2].location = 2;
vviad[2].binding = 0;
vviad[2].format = VK_FORMAT_VEC3; // r, g, b
vviad[2].offset = offsetof( struct vertex, color ); // 24

vviad[3].location = 3;
vviad[3].binding = 0;
vviad[3].format = VK_FORMAT_VEC2; // s, t
vviad[3].offset = offsetof( struct vertex, texCoord ); // 36

```

Always use the C/C++ construct `offsetof`, rather than hardcoding the value!

mjb – July 24, 2020

Telling the Pipeline about its Input

We will come to the Pipeline later, but for now, know that a Vulkan Pipeline is essentially a very large data structure that holds (what OpenGL would call) the state, including how to parse its vertex input.

VkPipelineVertexInputStateCreateInfo

```

vpvisci.sType = VK_STRUCTURE_TYPE_PIPELINE_VERTEX_INPUT_STATE_CREATE_INFO;
vpvisci.pNext = NULL;
vpvisci.flags = 0;
vpvisci.vertexBindingDescriptionCount = 1;
vpvisci.pVertexBindingDescriptions = vvibd;
vpvisci.vertexAttributeDescriptionCount = 4;
vpvisci.pVertexAttributeDescriptions = vviad;

```

VkPipelineInputAssemblyStateCreateInfo

```

vpisci.sType = VK_STRUCTURE_TYPE_PIPELINE_INPUT_ASSEMBLY_STATE_CREATE_INFO;
vpisci.pNext = NULL;
vpisci.flags = 0;
vpisci.topology = VK_PRIMITIVE_TOPOLOGY_TRIANGLE_LIST;

```

mjb – July 24, 2020

Telling the Pipeline about its Input

We will come to the Pipeline later, but for now, know that a Vulkan Pipeline is essentially a very large data structure that holds (what OpenGL would call) the state, including how to parse its vertex input.

VkGraphicsPipelineCreateInfo

```

vgpci.sType = VK_STRUCTURE_TYPE_GRAPHICS_PIPELINE_CREATE_INFO;
vgpci.pNext = NULL;
vgpci.flags = 0;
vgpci.stageCount = 2; // number of shader stages in this pipeline
vgpci.pVertexState = &vpvisci;
vgpci.pVertexInputState = &vpvisci;
vgpci.pInputAssemblyState = &vpisci;
vgpci.pTessellationState = (VkPipelineTessellationStateCreateInfo *)NULL;
vgpci.pViewportState = &vpisci;
vgpci.pRasterizationState = &vpisci;
vgpci.pMultisampleState = &vpmsci;
vgpci.pDepthStencilState = &vpdscci;
vgpci.pColorBlendState = &vpccbsci;
vgpci.pDynamicState = &vpdscli;
vgpci.layout = IN GraphicsPipelineLayout;
vgpci.renderPass = IN RenderPass;
vgpci.subpass = 0; // subpass number
vgpci.basePipelineHandle = (VkPipeline)VK_NULL_HANDLE;
vgpci.basePipelineIndex = 0;

result = vkCreateGraphicsPipelines( LogicalDevice, VK_NULL_HANDLE, 1, IN &vgpci, PALLOCATOR, OUT &GraphicsPipeline );

```

mjb – July 24, 2020

Telling the Command Buffer what Vertices to Draw

We will come to Command Buffers later, but for now, know that you will specify the vertex buffer that you want drawn.

VkBuffer buffers[1] = MyVertexDataBuffer.buffer;

VkCmdBindVertexBuffers(CommandBuffers[nextImageIndex], 0, 1, vertexDataBuffers, offsets);

```

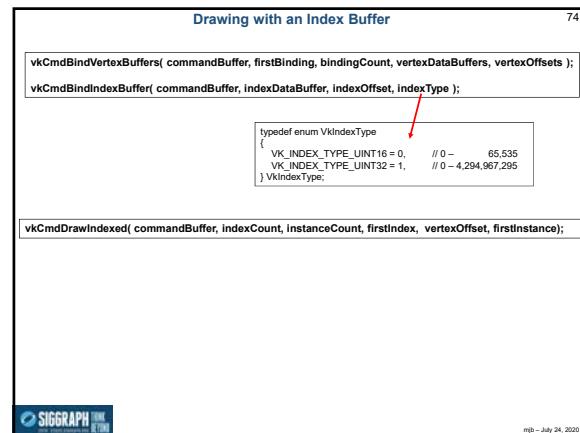
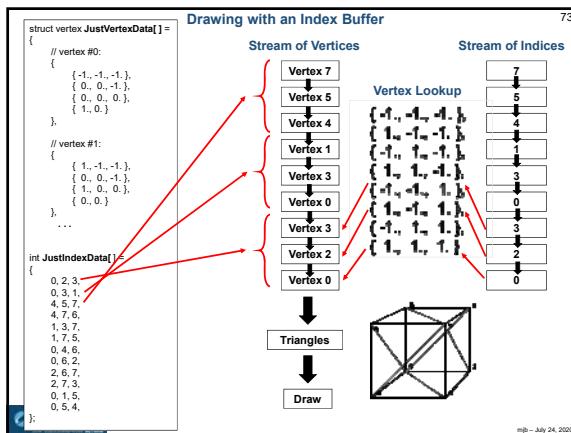
const uint32_t vertexCount = sizeof( VertexData ) / sizeof( VertexData[0] );
const uint32_t instanceCount = 1;
const uint32_t firstVertex = 0;
const uint32_t firstInstance = 0;

```

Always use the C/C++ construct `sizeof`, rather than hardcoding a count!

vkCmdDraw(CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstVertex, firstInstance);

mjb – July 24, 2020



Drawing with an Index Buffer

```

VkResult
Init05MyIndexDataBuffer(IN VkDeviceSize size, OUT MyBuffer * pMyBuffer)
{
    VkResult result = Init05DataBuffer(size, VK_BUFFER_USAGE_INDEX_BUFFER_BIT, pMyBuffer);
    // fills pMyBuffer
    return result;
}

Init05MyVertexDataBuffer( sizeof(JustVertexData), IN &MyJustVertexDataBuffer );
Fill05DataBuffer( MyJustVertexDataBuffer, (void *) JustVertexData );

Init05MyIndexDataBuffer( sizeof(JustIndexData), IN &MyJustIndexDataBuffer );
Fill05DataBuffer( MyJustIndexDataBuffer, (void *) JustIndexData );

```

mjb – July 24, 2020

Drawing with an Index Buffer

```

VkBuffer vBuffers[1] = { MyJustVertexDataBuffer.buffer };
VkBuffer iBuffer     = { MyJustIndexDataBuffer.buffer };

vkCmdBindVertexBuffers( CommandBuffers[nextImageIndex], 0, 1, vBuffers, offsets );
vkCmdBindIndexBuffer( CommandBuffers[nextImageIndex], iBuffer, 0, VK_INDEX_TYPE_UINT32 );

const uint32_t t_vertexCount = sizeof(JustVertexData) / sizeof(JustVertexData[0]);
const uint32_t t_instanceCount = sizeof(JustIndexData) / sizeof(JustIndexData[0]);
const uint32_t t_firstVertex = 0;
const uint32_t t_firstIndex = 0;
const uint32_t t_firstInstance = 0;
const uint32_t t_vertexOffset = 0;

vkCmdDrawIndexed( CommandBuffers[nextImageIndex], indexCount, instanceCount, firstIndex,
                  vertexOffset, firstInstance );

```

mjb – July 24, 2020

Indirect Drawing (not to be confused with Indexed)

```

typedef struct
VKDrawIndirectCommand
{
    uint32_t vertexCount;
    uint32_t instanceCount;
    uint32_t firstVertex;
    uint32_t firstInstance;
} VKDrawIndirectCommand;

vkCmdDrawIndirect( CommandBuffers[nextImageIndex], buffer, offset, drawCount, stride );

```

Compare this with:

```

vkCmdDraw( CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstIndex, firstInstance );

```

mjb – July 24, 2020

Indexed Indirect Drawing (i.e., both Indexed and Indirect)

```

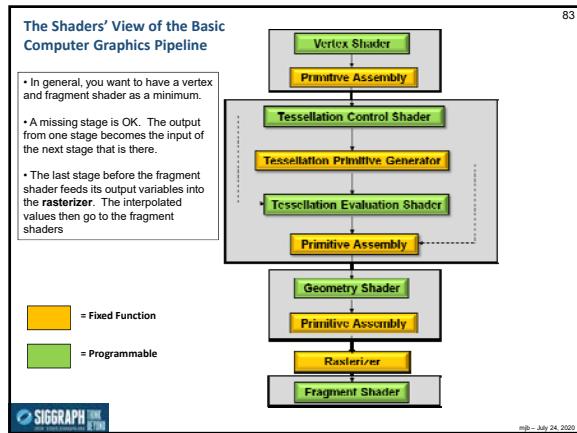
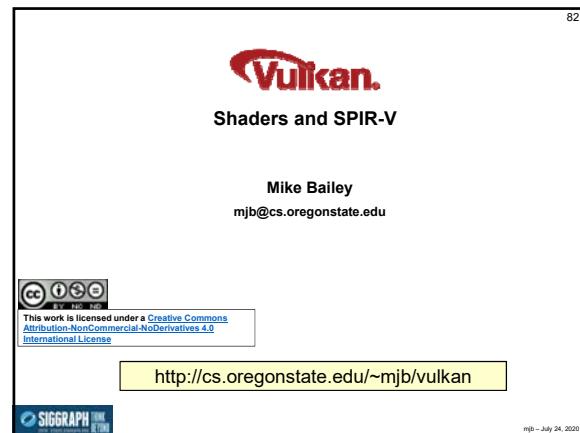
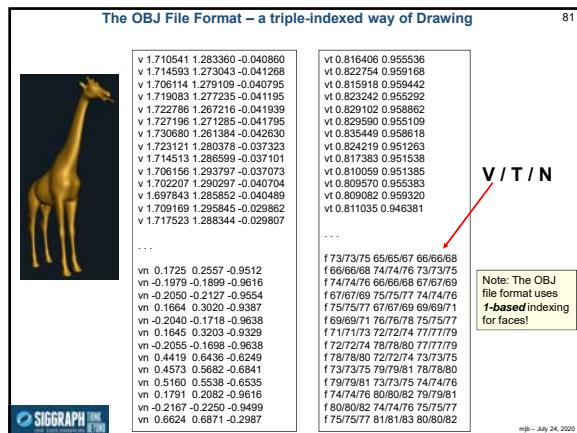
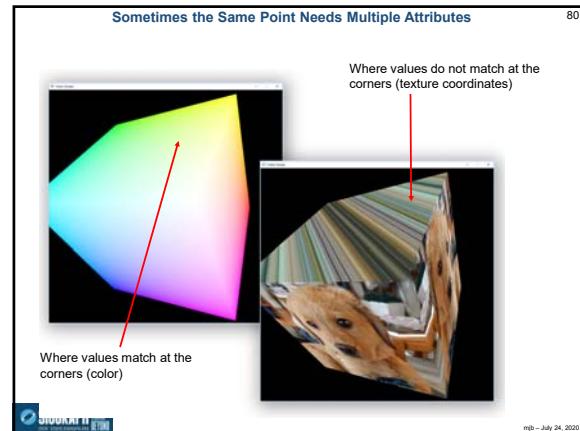
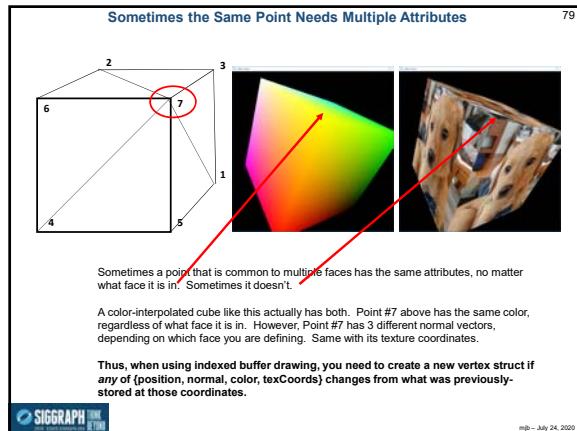
vkCmdDrawIndexedIndirect( commandBuffer, buffer, offset, drawCount, stride );

typedef struct
VKDrawIndirectCommand
{
    uint32_t indexCount;
    uint32_t instanceCount;
    uint32_t firstIndex;
    int32_t vertexOffset;
    int32_t firstInstance;
} VKDrawIndirectCommand;

Compare this with:
vkCmdDraw( CommandBuffers[nextImageIndex], instanceCount, firstIndex, firstInstance );

```

mjb – July 24, 2020



Vulkan Shader Stages 84

Shader stages

```
typedef enum VkPipelineStageFlagBits {
    VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT = 0x00000001,
    VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT = 0x00000002,
    VK_PIPELINE_STAGE_VERTEX_INPUT_BIT = 0x00000004,
    VK_PIPELINE_STAGE_VERTEX_SHADER_BIT = 0x00000008,
    VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT = 0x00000010,
    VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT = 0x00000020,
    VK_PIPELINE_STAGE_GEOMETRY_SHADER_BIT = 0x00000040,
    VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT = 0x00000080,
    VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT = 0x00000100,
    VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT = 0x00000200,
    VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT = 0x00000400,
    VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT = 0x00000800,
    VK_PIPELINE_STAGE_TRANSFER_BIT = 0x00001000,
    VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT = 0x00002000,
    VK_PIPELINE_STAGE_HOST_BIT = 0x00004000,
    VK_PIPELINE_STAGE_ALL_GRAPHICS_BIT = 0x00008000,
    VK_PIPELINE_STAGE_ALL_COMMANDS_BIT = 0x00010000,
} VkPipelineStageFlagBits;
```

mjb – July 24, 2020

How Vulkan GLSL Differs from OpenGL GLSL 85

Detecting that a GLSL Shader is being used with Vulkan/SPIR-V:

- In the compiler, there is an automatic `#define VULKAN 100`

Vulkan Vertex and Instance indices:

<code>gl_VertexIndex</code>	<code>gl_VertexID</code>
<code>gl_InstanceIndex</code>	<code>gl_InstanceID</code>

Both are 0-based

`gl_FragColor`:

- In OpenGL, `gl_FragColor` broadcasts to all color attachments
- In Vulkan, it just broadcasts to color attachment location #0
- Best idea: don't use it at all – explicitly declare out variables to have specific location numbers

 mjb – July 24, 2020

How Vulkan GLSL Differs from OpenGL GLSL 86

Shader combinations of separate texture data and samplers:

```
uniform sampler s;
uniform texture2D t;
vec4 rgba = texture( sampler2D(t, s), vST );
```

Note: our sample code doesn't use this.

Descriptor Sets:

```
layout( set=0, binding=0 ) . . . ;
```

Push Constants:

```
layout( push_constant ) . . . ;
```

Specialization Constants:

```
layout( constant_id = 3 ) const int N = 5;
```

- Only for scalars, but a vector's components can be constructed from specialization constants

Specialization Constants for Compute Shaders:

```
layout( local_size_x = 8, local_size_y = 16 );
```

- This sets `gl_WorkGroupSize.x` and `gl_WorkGroupSize.y`
`gl_WorkGroupSize.z` is set as a constant

 mjb – July 24, 2020

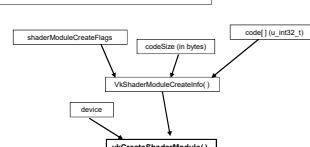
Vulkan: Shaders' use of Layouts for Uniform Variables 87

// non-sampler variables must be in a uniform block:
`layout(std140, set = 0, binding = 0) uniform matBuf`
{
 mat4 uModelMatrix;
 mat4 uViewMatrix;
 mat4 uProjectionMatrix;
 mat3 uNormalMatrix;
} Matrices;

// non-sampler variables must be in a uniform block:
`layout(std140, set = 1, binding = 0) uniform lightBuf`
{
 vec4 uLightPos;
} Light;

`layout(set = 2, binding = 0) uniform sampler2D uTexUnit;`

All non-sampler uniform variables must be in block buffers



 mjb – July 24, 2020

Vulkan Shader Compiling 88

- You half-precompile your shaders with an external compiler
- Your shaders get turned into an intermediate form known as SPIR-V, which stands for **Standard Portable Intermediate Representation**.
- SPIR-V gets turned into fully-compiled code at runtime, when the pipeline structure is finally created
- The SPIR-V spec has been public for a few years – new shader languages are surely being developed
- OpenGL and OpenCL have now adopted SPIR-V as well



Advantages:

- Software vendors don't need to ship their shader source
- Syntax errors appear during the SPIR-V step, not during runtime
- Software can launch faster because half of the compilation has already taken place
- This guarantees a common front-end place
- This allows for other language front-ends

 mjb – July 24, 2020

SPIR-V: Standard Portable Intermediate Representation for Vulkan 89

`glslangValidator shaderFile -V [-H] [-I<dir>] [-S <stage>] -o shaderBinaryFile.spv`

Shaderfile extensions:

- .vert Vertex
- .tesc Tessellation Control
- .tese Tessellation Evaluation
- .geom Geometry
- .frag Fragment
- .comp Compute

(Can be overridden by the `-S` option)

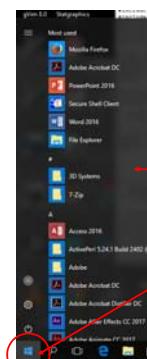
`-V` Compile for Vulkan
`-G` Compile for OpenGL
`-I` Directory(ies) to look in for #includes
`-S` Specify stage rather than get it from shaderfile extension
`-c` Print out the maximum sizes of various properties

Windows: `glslangValidator.exe`
Linux: `glslangValidator`

 mjb – July 24, 2020

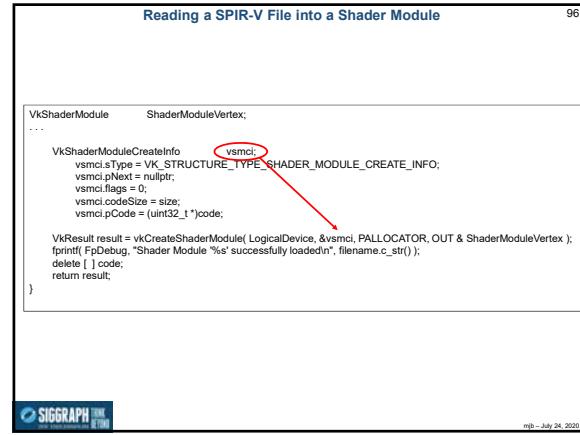
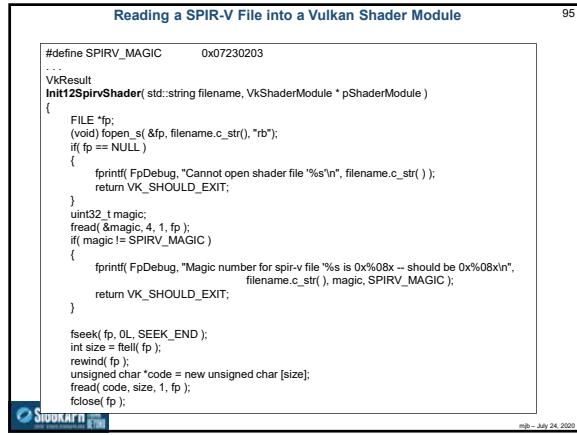
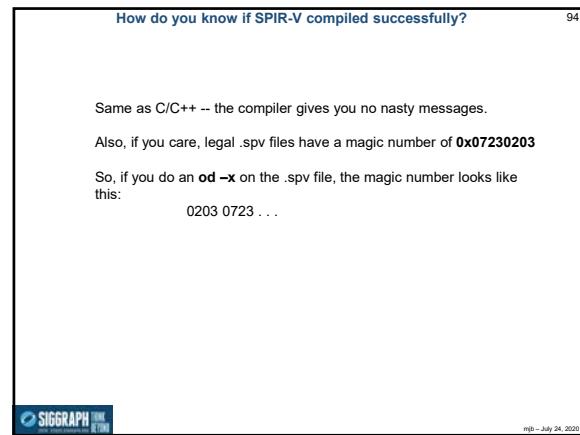
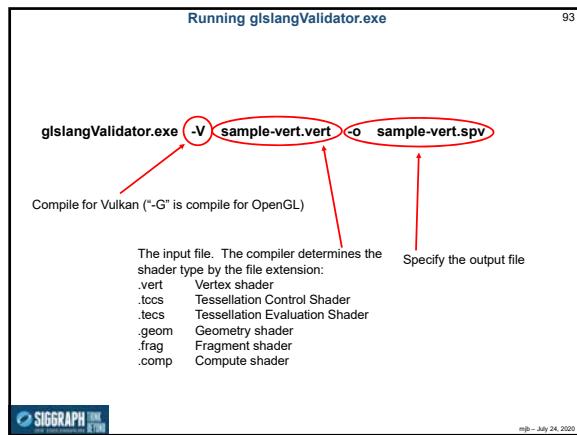
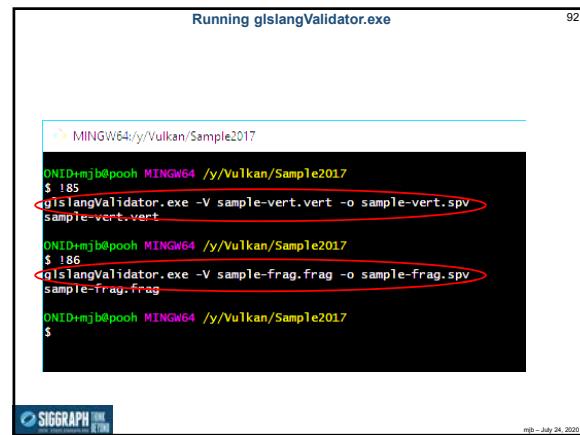
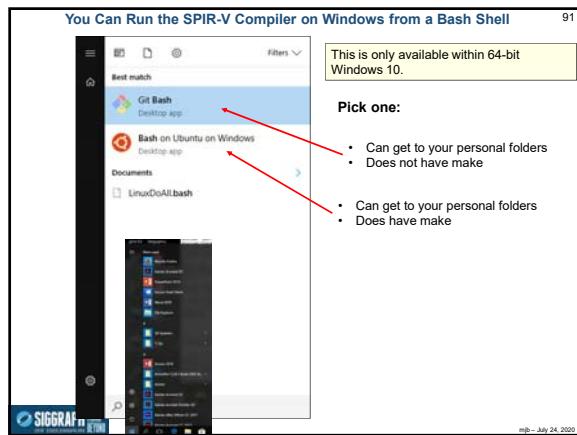
You Can Run the SPIR-V Compiler on Windows from a Bash Shell 90

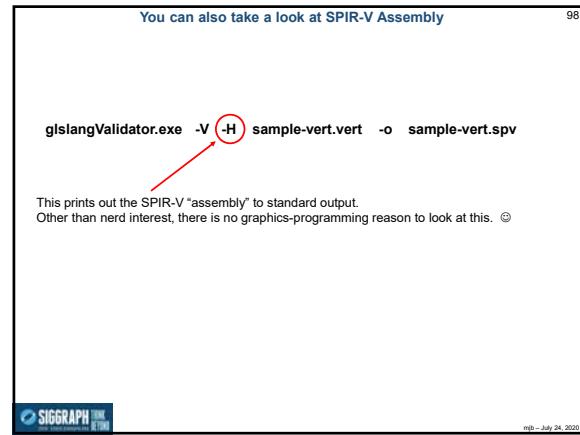
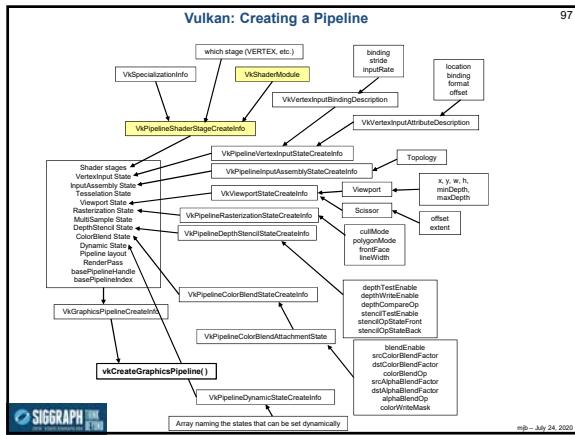
This is only available within 64-bit Windows 10.



1. Click on the Microsoft Start icon
2. Type the word bash

 mjb – July 24, 2020





For example, if this is your Shader Source

```
#version 400
#extension GL_ARB_separate_shader_objects : enable
#extension GL_ARB_shading_language_420pack : enable
layout(std140, set = 0, binding = 0) uniform matBuf
{
    mat4 uModelMatrix;
    mat4 uViewMatrix;
    mat4 uProjectionMatrix;
    mat3 uNormalMatrix;
} Matrices;

// Non-opaque must be in a uniform block:
layout(std140, set = 1, binding = 0) uniform lightBuf
{
    vec4 uLightPos;
} Light;

layout(location = 0) in vec3 aVert;
layout(location = 1) in vec3 aNormal;
layout(location = 2) in vec3 aColor;
layout(location = 3) in vec2 aTexCoord;

void main()
{
    mat PVM = Matrices.uProjectionMatrix * Matrices.uViewMatrix * Matrices.uModelMatrix;
    gl_Position = PVM * vec4(aVert, 1.);

    vNormal = Matrices.uNormalMatrix * aNormal;
    vColor = aColor;
    vTexCoord = aTexCoord;
}
```

This is the SPIR-V Assembly, Part I

```
Capability Shader
ExecutionGLSL.std.400
MemoryModel Logical GLSL.std.450
EntryPoint Vertex "main" 37 48 53 56 57 61 63
SourceExtension "GL_ARB_separate_shader_objects"
SourceExtension "GL_ARB_shading_language_420pack"
Name 0 "PVM"
Name 1 "uModelMatrix"
MemberName 13(matBuf) 0 "uModelMatrix"
MemberName 13(matBuf) 1 "uViewMatrix"
MemberName 13(matBuf) 2 "uProjectionMatrix"
MemberName 13(matBuf) 3 "uNormalMatrix"
Name 32 "g_LightPos"
MemberName 32(g_PVertex) 0 "g_Pos"
MemberName 32(g_PVertex) 1 "g_Normals"
MemberName 32(g_PVertex) 2 "g_ClipDistance"
Name 37 "g_VexPos"
Name 48 "g_Normal"
Name 56 "g_Color"
Name 63 "gTexCoord"
Name 63 "g_TexCoord"
Name 67 "Light"
MemberName 67(matBuf) 0 "uLightPos"
MemberName 67(matBuf) 1 "Offset"
MemberDecorate 13(matBuf) 0 Offset 16
MemberDecorate 13(matBuf) 1 ClipDistance 16
MemberDecorate 13(matBuf) 1 OffSet 64
MemberDecorate 13(matBuf) 2 OffSet 128
MemberDecorate 13(matBuf) 3 OffSet 192
MemberDecorate 13(matBuf) 2 MatrixStride 16
MemberDecorate 13(matBuf) 3 MatrixStride 16
Decorate 13(matBuf) Block
Decorate 13(matBuf) DescriptorSet 0
Decorate 13(matBuf) Uniform
```

This is the SPIR-V Assembly, Part II

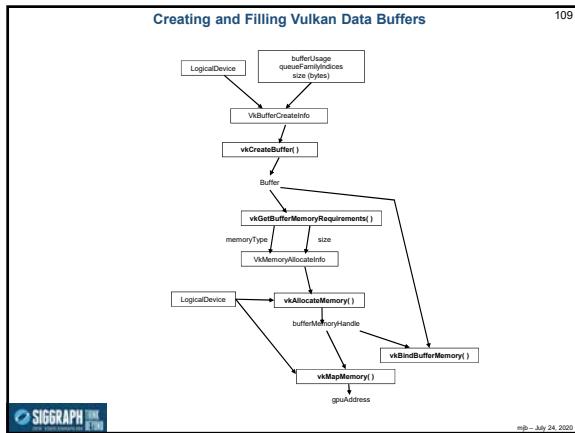
```
Decorate 32(g_PVertex) BindGroup 0
MemberDecorate 32(g_PVertex) 0 Bulitin Position
MemberDecorate 32(g_PVertex) 1 Bulitin PointSize
MemberDecorate 32(g_PVertex) 2 Bulitin ClipDistance
Decorate 32(g_VexPos) Binding 0
Decorate 32(g_VexPos) Location 0
Decorate 32(g_VexPos) Type 0
Decorate 32(g_VexPos) 1 Location 1
Decorate 32(g_VexPos) 2 Location 2
Decorate 32(g_VexPos) 3 Location 3
MemberDecorate 65(gLightBuf) 0 Offset 0
Decorate 65(gLightBuf) Block
Decorate 65(gLightBuf) DescriptorSet 1
Decorate 65(gLightBuf) Binding 0
Decorate 65(gLightBuf) Uniform
```

- 2: TypePointer Uniform 12
- 3: TypePointer Variable Input
- 4: TypePointer Variable Output
- 5: TypePointer Input 2
- 6: TypePointer Output 59(ivec2)
- 7: TypePointer Input 4
- 8: TypeMatrix 7(ivec4)
- 9: TypePointer Input 8
- 11: TypeVector 6(ivec3)
- 12: TypeMatrix 11(ivec3)
- 13(matBuf): 14(matBuf) Uniform 12
- 14: TypePointer Uniform 13(matBuf)
- 15(matBuf): 14(matBuf) Variable Uniform
- 16: TypePointer Input 1
- 17: TypePointer Input 2
- 18: TypePointer Input 8
- 21: 16(int) Constant 1
- 25: 16(int) Constant 2
- 29: 16(int) Constant 20
- 30: 29(int) Constant 1
- 31: 16(int) Constant 30
- 33: TypePointer Output 32(g_PVertex) 31
- 34: 32(g_PVertex) 32(g_PVertex)
- 36: TypePointer Input 11(ivec3)
- 37: TypePointer Input 1 Variable Input
- 39: 6(ivec2) Constant 10363216
- 45: TypePointer Output 7(ivec3)
- 47: TypePointer Input 11(ivec3)
- 48(vNormal): 47(ptr) Variable Output
- 49: 16(int) Constant 3

This is the SPIR-V Assembly, Part III

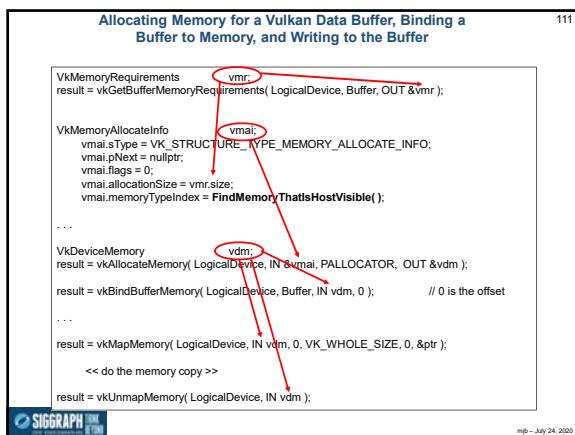
```
50: 50(ivec2) 51(ivec2) 52(ivec2) 53(ivec2) 54(ivec2) 55(ivec2) 56(ivec2) 57(ivec2) 58(ivec2) 59(ivec2) 60: TypePointer Uniform 12
59(ivec2) 60(ivec2) 61(ivec2) 62(ivec2) 63(ivec2) 64(ivec2) 65(ivec2) 66(ivec2) 67(ivec2) 68(ivec2) 69(ivec2) 70(ivec2) 71(ivec2) 72(ivec2) 73(ivec2) 74(ivec2) 75(ivec2) 76(ivec2) 77(ivec2) 78(ivec2) 79(ivec2) 80(ivec2) 81(ivec2) 82(ivec2) 83(ivec2) 84(ivec2) 85(ivec2) 86(ivec2) 87(ivec2) 88(ivec2) 89(ivec2) 90(ivec2) 91(ivec2) 92(ivec2) 93(ivec2) 94(ivec2) 95(ivec2) 96(ivec2) 97(ivec2) 98(ivec2) 99(ivec2) 100(ivec2) 101(ivec2) 102(ivec2) 103(ivec2) 104(ivec2) 105(ivec2) 106(ivec2) 107(ivec2) 108(ivec2) 109(ivec2) 110(ivec2) 111(ivec2) 112(ivec2) 113(ivec2) 114(ivec2) 115(ivec2) 116(ivec2) 117(ivec2) 118(ivec2) 119(ivec2) 120(ivec2) 121(ivec2) 122(ivec2) 123(ivec2) 124(ivec2) 125(ivec2) 126(ivec2) 127(ivec2) 128(ivec2) 129(ivec2) 130(ivec2) 131(ivec2) 132(ivec2) 133(ivec2) 134(ivec2) 135(ivec2) 136(ivec2) 137(ivec2) 138(ivec2) 139(ivec2) 140(ivec2) 141(ivec2) 142(ivec2) 143(ivec2) 144(ivec2) 145(ivec2) 146(ivec2) 147(ivec2) 148(ivec2) 149(ivec2) 150(ivec2) 151(ivec2) 152(ivec2) 153(ivec2) 154(ivec2) 155(ivec2) 156(ivec2) 157(ivec2) 158(ivec2) 159(ivec2) 160(ivec2) 161(ivec2) 162(ivec2) 163(ivec2) 164(ivec2) 165(ivec2) 166(ivec2) 167(ivec2) 168(ivec2) 169(ivec2) 170(ivec2) 171(ivec2) 172(ivec2) 173(ivec2) 174(ivec2) 175(ivec2) 176(ivec2) 177(ivec2) 178(ivec2) 179(ivec2) 180(ivec2) 181(ivec2) 182(ivec2) 183(ivec2) 184(ivec2) 185(ivec2) 186(ivec2) 187(ivec2) 188(ivec2) 189(ivec2) 190(ivec2) 191(ivec2) 192(ivec2) 193(ivec2) 194(ivec2) 195(ivec2) 196(ivec2) 197(ivec2) 198(ivec2) 199(ivec2) 200(ivec2) 201(ivec2) 202(ivec2) 203(ivec2) 204(ivec2) 205(ivec2) 206(ivec2) 207(ivec2) 208(ivec2) 209(ivec2) 210(ivec2) 211(ivec2) 212(ivec2) 213(ivec2) 214(ivec2) 215(ivec2) 216(ivec2) 217(ivec2) 218(ivec2) 219(ivec2) 220(ivec2) 221(ivec2) 222(ivec2) 223(ivec2) 224(ivec2) 225(ivec2) 226(ivec2) 227(ivec2) 228(ivec2) 229(ivec2) 230(ivec2) 231(ivec2) 232(ivec2) 233(ivec2) 234(ivec2) 235(ivec2) 236(ivec2) 237(ivec2) 238(ivec2) 239(ivec2) 240(ivec2) 241(ivec2) 242(ivec2) 243(ivec2) 244(ivec2) 245(ivec2) 246(ivec2) 247(ivec2) 248(ivec2) 249(ivec2) 250(ivec2) 251(ivec2) 252(ivec2) 253(ivec2) 254(ivec2) 255(ivec2) 256(ivec2) 257(ivec2) 258(ivec2) 259(ivec2) 260(ivec2) 261(ivec2) 262(ivec2) 263(ivec2) 264(ivec2) 265(ivec2) 266(ivec2) 267(ivec2) 268(ivec2) 269(ivec2) 270(ivec2) 271(ivec2) 272(ivec2) 273(ivec2) 274(ivec2) 275(ivec2) 276(ivec2) 277(ivec2) 278(ivec2) 279(ivec2) 280(ivec2) 281(ivec2) 282(ivec2) 283(ivec2) 284(ivec2) 285(ivec2) 286(ivec2) 287(ivec2) 288(ivec2) 289(ivec2) 290(ivec2) 291(ivec2) 292(ivec2) 293(ivec2) 294(ivec2) 295(ivec2) 296(ivec2) 297(ivec2) 298(ivec2) 299(ivec2) 300(ivec2) 301(ivec2) 302(ivec2) 303(ivec2) 304(ivec2) 305(ivec2) 306(ivec2) 307(ivec2) 308(ivec2) 309(ivec2) 310(ivec2) 311(ivec2) 312(ivec2) 313(ivec2) 314(ivec2) 315(ivec2) 316(ivec2) 317(ivec2) 318(ivec2) 319(ivec2) 320(ivec2) 321(ivec2) 322(ivec2) 323(ivec2) 324(ivec2) 325(ivec2) 326(ivec2) 327(ivec2) 328(ivec2) 329(ivec2) 330(ivec2) 331(ivec2) 332(ivec2) 333(ivec2) 334(ivec2) 335(ivec2) 336(ivec2) 337(ivec2) 338(ivec2) 339(ivec2) 340(ivec2) 341(ivec2) 342(ivec2) 343(ivec2) 344(ivec2) 345(ivec2) 346(ivec2) 347(ivec2) 348(ivec2) 349(ivec2) 350(ivec2) 351(ivec2) 352(ivec2) 353(ivec2) 354(ivec2) 355(ivec2) 356(ivec2) 357(ivec2) 358(ivec2) 359(ivec2) 360(ivec2) 361(ivec2) 362(ivec2) 363(ivec2) 364(ivec2) 365(ivec2) 366(ivec2) 367(ivec2) 368(ivec2) 369(ivec2) 370(ivec2) 371(ivec2) 372(ivec2) 373(ivec2) 374(ivec2) 375(ivec2) 376(ivec2) 377(ivec2) 378(ivec2) 379(ivec2) 380(ivec2) 381(ivec2) 382(ivec2) 383(ivec2) 384(ivec2) 385(ivec2) 386(ivec2) 387(ivec2) 388(ivec2) 389(ivec2) 390(ivec2) 391(ivec2) 392(ivec2) 393(ivec2) 394(ivec2) 395(ivec2) 396(ivec2) 397(ivec2) 398(ivec2) 399(ivec2) 400(ivec2) 401(ivec2) 402(ivec2) 403(ivec2) 404(ivec2) 405(ivec2) 406(ivec2) 407(ivec2) 408(ivec2) 409(ivec2) 410(ivec2) 411(ivec2) 412(ivec2) 413(ivec2) 414(ivec2) 415(ivec2) 416(ivec2) 417(ivec2) 418(ivec2) 419(ivec2) 420(ivec2) 421(ivec2) 422(ivec2) 423(ivec2) 424(ivec2) 425(ivec2) 426(ivec2) 427(ivec2) 428(ivec2) 429(ivec2) 430(ivec2) 431(ivec2) 432(ivec2) 433(ivec2) 434(ivec2) 435(ivec2) 436(ivec2) 437(ivec2) 438(ivec2) 439(ivec2) 440(ivec2) 441(ivec2) 442(ivec2) 443(ivec2) 444(ivec2) 445(ivec2) 446(ivec2) 447(ivec2) 448(ivec2) 449(ivec2) 450(ivec2) 451(ivec2) 452(ivec2) 453(ivec2) 454(ivec2) 455(ivec2) 456(ivec2) 457(ivec2) 458(ivec2) 459(ivec2) 460(ivec2) 461(ivec2) 462(ivec2) 463(ivec2) 464(ivec2) 465(ivec2) 466(ivec2) 467(ivec2) 468(ivec2) 469(ivec2) 470(ivec2) 471(ivec2) 472(ivec2) 473(ivec2) 474(ivec2) 475(ivec2) 476(ivec2) 477(ivec2) 478(ivec2) 479(ivec2) 480(ivec2) 481(ivec2) 482(ivec2) 483(ivec2) 484(ivec2) 485(ivec2) 486(ivec2) 487(ivec2) 488(ivec2) 489(ivec2) 490(ivec2) 491(ivec2) 492(ivec2) 493(ivec2) 494(ivec2) 495(ivec2) 496(ivec2) 497(ivec2) 498(ivec2) 499(ivec2) 500(ivec2) 501(ivec2) 502(ivec2) 503(ivec2) 504(ivec2) 505(ivec2) 506(ivec2) 507(ivec2) 508(ivec2) 509(ivec2) 510(ivec2) 511(ivec2) 512(ivec2) 513(ivec2) 514(ivec2) 515(ivec2) 516(ivec2) 517(ivec2) 518(ivec2) 519(ivec2) 520(ivec2) 521(ivec2) 522(ivec2) 523(ivec2) 524(ivec2) 525(ivec2) 526(ivec2) 527(ivec2) 528(ivec2) 529(ivec2) 530(ivec2) 531(ivec2) 532(ivec2) 533(ivec2) 534(ivec2) 535(ivec2) 536(ivec2) 537(ivec2) 538(ivec2) 539(ivec2) 540(ivec2) 541(ivec2) 542(ivec2) 543(ivec2) 544(ivec2) 545(ivec2) 546(ivec2) 547(ivec2) 548(ivec2) 549(ivec2) 550(ivec2) 551(ivec2) 552(ivec2) 553(ivec2) 554(ivec2) 555(ivec2) 556(ivec2) 557(ivec2) 558(ivec2) 559(ivec2) 560(ivec2) 561(ivec2) 562(ivec2) 563(ivec2) 564(ivec2) 565(ivec2) 566(ivec2) 567(ivec2) 568(ivec2) 569(ivec2) 570(ivec2) 571(ivec2) 572(ivec2) 573(ivec2) 574(ivec2) 575(ivec2) 576(ivec2) 577(ivec2) 578(ivec2) 579(ivec2) 580(ivec2) 581(ivec2) 582(ivec2) 583(ivec2) 584(ivec2) 585(ivec2) 586(ivec2) 587(ivec2) 588(ivec2) 589(ivec2) 590(ivec2) 591(ivec2) 592(ivec2) 593(ivec2) 594(ivec2) 595(ivec2) 596(ivec2) 597(ivec2) 598(ivec2) 599(ivec2) 600(ivec2) 601(ivec2) 602(ivec2) 603(ivec2) 604(ivec2) 605(ivec2) 606(ivec2) 607(ivec2) 608(ivec2) 609(ivec2) 610(ivec2) 611(ivec2) 612(ivec2) 613(ivec2) 614(ivec2) 615(ivec2) 616(ivec2) 617(ivec2) 618(ivec2) 619(ivec2) 620(ivec2) 621(ivec2) 622(ivec2) 623(ivec2) 624(ivec2) 625(ivec2) 626(ivec2) 627(ivec2) 628(ivec2) 629(ivec2) 630(ivec2) 631(ivec2) 632(ivec2) 633(ivec2) 634(ivec2) 635(ivec2) 636(ivec2) 637(ivec2) 638(ivec2) 639(ivec2) 640(ivec2) 641(ivec2) 642(ivec2) 643(ivec2) 644(ivec2) 645(ivec2) 646(ivec2) 647(ivec2) 648(ivec2) 649(ivec2) 650(ivec2) 651(ivec2) 652(ivec2) 653(ivec2) 654(ivec2) 655(ivec2) 656(ivec2) 657(ivec2) 658(ivec2) 659(ivec2) 660(ivec2) 661(ivec2) 662(ivec2) 663(ivec2) 664(ivec2) 665(ivec2) 666(ivec2) 667(ivec2) 668(ivec2) 669(ivec2) 670(ivec2) 671(ivec2) 672(ivec2) 673(ivec2) 674(ivec2) 675(ivec2) 676(ivec2) 677(ivec2) 678(ivec2) 679(ivec2) 680(ivec2) 681(ivec2) 682(ivec2) 683(ivec2) 684(ivec2) 685(ivec2) 686(ivec2) 687(ivec2) 688(ivec2) 689(ivec2) 690(ivec2) 691(ivec2) 692(ivec2) 693(ivec2) 694(ivec2) 695(ivec2) 696(ivec2) 697(ivec2) 698(ivec2) 699(ivec2) 700(ivec2) 701(ivec2) 702(ivec2) 703(ivec2) 704(ivec2) 705(ivec2) 706(ivec2) 707(ivec2) 708(ivec2) 709(ivec2) 710(ivec2) 711(ivec2) 712(ivec2) 713(ivec2) 714(ivec2) 715(ivec2) 716(ivec2) 717(ivec2) 718(ivec2) 719(ivec2) 720(ivec2) 721(ivec2) 722(ivec2) 723(ivec2) 724(ivec2) 725(ivec2) 726(ivec2) 727(ivec2) 728(ivec2) 729(ivec2) 730(ivec2) 731(ivec2) 732(ivec2) 733(ivec2) 734(ivec2) 735(ivec2) 736(ivec2) 737(ivec2) 738(ivec2) 739(ivec2) 740(ivec2) 741(ivec2) 742(ivec2) 743(ivec2) 744(ivec2) 745(ivec2) 746(ivec2) 747(ivec2) 748(ivec2) 749(ivec2) 750(ivec2) 751(ivec2) 752(ivec2) 753(ivec2) 754(ivec2) 755(ivec2) 756(ivec2) 757(ivec2) 758(ivec2) 759(ivec2) 760(ivec2) 761(ivec2) 762(ivec2) 763(ivec2) 764(ivec2) 765(ivec2) 766(ivec2) 767(ivec2) 768(ivec2) 769(ivec2) 770(ivec2) 771(ivec2) 772(ivec2) 773(ivec2) 774(ivec2) 775(ivec2) 776(ivec2) 777(ivec2) 778(ivec2) 779(ivec2) 780(ivec2) 781(ivec2) 782(ivec2) 783(ivec2) 784(ivec2) 785(ivec2) 786(ivec2) 787(ivec2) 788(ivec2) 789(ivec2) 790(ivec2) 791(ivec2) 792(ivec2) 793(ivec2) 794(ivec2) 795(ivec2) 796(ivec2) 797(ivec2) 798(ivec2) 799(ivec2) 800(ivec2) 801(ivec2) 802(ivec2) 803(ivec2) 804(ivec2) 805(ivec2) 806(ivec2) 807(ivec2) 808(ivec2) 809(ivec2) 810(ivec2) 811(ivec2) 812(ivec2) 813(ivec2) 814(ivec2) 815(ivec2) 816(ivec2) 817(ivec2) 818(ivec2) 819(ivec2) 820(ivec2) 821(ivec2) 822(ivec2) 823(ivec2) 824(ivec2) 825(ivec2) 826(ivec2) 827(ivec2) 828(ivec2) 829(ivec2) 830(ivec2) 831(ivec2) 832(ivec2) 833(ivec2) 834(ivec2) 835(ivec2) 836(ivec2) 837(ivec2) 838(ivec2) 839(ivec2) 840(ivec2) 841(ivec2) 842(ivec2) 843(ivec2) 844(ivec2) 845(ivec2) 846(ivec2) 847(ivec2) 848(ivec2) 849(ivec2) 850(ivec2) 851(ivec2) 852(ivec2) 853(ivec2) 854(ivec2) 855(ivec2) 856(ivec2) 857(ivec2) 858(ivec2) 859(ivec2) 860(ivec2) 861(ivec2) 862(ivec2) 863(ivec2) 864(ivec2) 865(ivec2) 866(ivec2) 867(ivec2) 868(ivec2) 869(ivec2) 870(ivec2) 871(ivec2) 872(ivec2) 873(ivec2) 874(ivec2) 875(ivec2) 876(ivec2) 877(ivec2) 878(ivec2) 879(ivec2) 880(ivec2) 881(ivec2) 882(ivec2) 883(ivec2) 884(ivec2) 885(ivec2) 886(ivec2) 887(ivec2) 888(ivec2) 889(ivec2) 890(ivec2) 891(ivec2) 892(ivec2) 893(ivec2) 894(ivec2) 895(ivec2) 896(ivec2) 897(ivec2) 898(ivec2) 899(ivec2) 900(ivec2) 901(ivec2) 902(ivec2) 903(ivec2) 904(ivec2) 905(ivec2) 906(ivec2) 907(ivec2) 908(ivec2) 909(ivec2) 910(ivec2) 911(ivec2) 912(ivec2) 913(ivec2) 914(ivec2) 915(ivec2) 916(ivec2) 917(ivec2) 918(ivec2) 919(ivec2) 920(ivec2) 921(ivec2) 922(ivec2) 923(ivec2) 924(ivec2) 925(ivec2) 926(ivec2) 927(ivec2) 928(ivec2) 929(ivec2) 930(ivec2) 931(ivec2) 932(ivec2) 933(ivec2) 934(ivec2) 935(ivec2) 936(ivec2) 937(ivec2) 938(ivec2) 939(ivec2) 940(ivec2) 941(ivec2) 942(ivec2) 943(ivec2) 944(ivec2) 945(ivec2) 946(ivec2) 947(ivec2) 948(ivec2) 949(ivec2) 950(ivec2) 951(ivec2) 952(ivec2) 953(ivec2) 954(ivec2) 955(ivec2) 956(ivec2) 957(ivec2) 958(ivec2) 959(ivec2) 960(ivec2) 961(ivec2) 962(ivec2) 963(ivec2) 964(ivec2) 965(ivec2) 966(ivec2) 967(ivec2) 968(ivec2) 969(ivec2) 970(ivec2) 971(ivec2) 972(ivec2) 973(ivec2) 974(ivec2) 975(ivec2) 976(ivec2) 977(ivec2) 978(ivec2) 979(ivec2) 980(ivec2) 981(ivec2) 982(ivec2) 983(ivec2) 984(ivec2) 985(ivec2) 986(ivec2) 987(ivec2) 988(ivec2) 989(ivec2) 990(ivec2) 991(ivec2) 992(ivec2) 993(ivec2) 994(ivec2) 995(ivec2) 996(ivec2) 997(ivec2) 998(ivec2) 999(ivec2) 1000(ivec2) 1001(ivec2) 1002(ivec2) 1003(ivec2) 1004(ivec2) 1005(ivec2) 1006(ivec2) 1007(ivec2) 1008(ivec2) 1009(ivec2) 1010(ivec2) 1011(ivec2) 1012(ivec2) 1013(ivec2) 1014(ivec2) 1015(ivec2) 1016(ivec2) 1017(ivec2) 1018(ivec2) 1019(ivec2) 1020(ivec2) 1021(ivec2) 1022(ivec2) 1023(ivec2) 1024(ivec2) 1025(ivec2) 1026(ivec2) 1027(ivec2) 1028(ivec2) 1029(ivec2) 1030(ivec2) 1031(ivec2) 1032(ivec2) 1033(ivec2) 1034(ivec2) 1035(ivec2) 1036(ivec2) 1037(ivec2) 1038(ivec2) 1039(ivec2) 1040(ivec2) 1041(ivec2) 1042(ivec2) 1043(ivec2) 1044(ivec2) 1045(ivec2) 1046(ivec2) 1047(ivec2) 1048(ivec2) 1049(ivec2) 1050(ivec2) 1051(ivec2) 1052(ivec2) 1053(ivec2) 1054(ivec2) 1055(ivec2) 1056(ivec2) 1057(ivec2) 1058(ivec2) 1059(ivec2) 1060(ivec2) 1061(ivec2) 1062(ivec2) 1063(ivec2) 1064(ivec2) 1065(ivec2) 1066(ivec2) 1067(ivec2) 1068(ivec2) 1069(ivec2) 1070(ivec2) 1071(ivec2) 1072(ivec2) 1073(ivec2) 1074(ivec2) 1075(ivec2) 1076(ivec2) 1077(ivec2) 1078(ivec2) 1079(ivec2) 1080(ivec2) 1081(ivec2) 1082(ivec2) 1083(ivec2) 1084(ivec2) 1085(ivec2) 1086(ivec2) 1087(ivec2) 1088(ivec2) 1089(ivec2) 1090(ivec2) 1091(ivec2) 1092(ivec2) 1093(ivec2) 1094(ivec2) 1095(ivec2) 1096(ivec2) 1097(ivec2) 1098(ivec2) 1099(ivec2) 1100(ivec2) 1101(ivec2) 1102(ivec2) 1103(ivec2) 1104(ivec2) 1105(ivec2) 1106(ivec2) 1107(ivec2) 1108(ivec2) 1109(ivec2) 1110(ivec2) 1111(ivec2) 1112(ivec2) 1113(ivec2) 1114(ivec2) 1115(ivec2) 1116(ivec2) 1117(ivec2) 1118(ivec2) 1119(ivec2) 1120(ivec2) 1121(ivec2) 1122(ivec2) 1123(ivec2) 1124(ivec2) 1125(ivec2) 1126(ivec2) 1127(ivec2) 1128(ivec2) 1129(ivec2) 1130(ivec2) 1131(ivec2) 1132(ivec2) 1133(ivec2) 1134(ivec2) 1135(ivec2) 1136(ivec2) 1137(ivec2) 1138(ivec2) 1139(ivec2) 1140(ivec2) 1141(ivec2) 1142(ivec2) 1143(ivec2) 1144(ivec2) 1145(ivec2) 1146(ivec2) 1147(ivec2) 1148(ivec2) 1149(ivec2) 1150(ivec2) 1151(ivec2) 1152(ivec2) 1153(ivec2) 1154(
```

SPIR-V: Printing the Configuration		103
<pre>MaxLights 32 MaxLightComponents 4 MaxTessellationUnits 32 MaxTessLevel 32 MaxVaryingComponents 64 MaxVaryingComponents 4096 MaxVaryingFloats 24 MaxVaryingIntegers 32 MaxVaryingTextureSamples 32 MaxVaryingUniformComponents 32 MaxTessLevel 96 MaxTessLevel 192 MaxTessLevel 384 MaxTessLevel 768 MaxTessLevel 1536 MaxTessLevel 3072 MaxTessLevel 6144 MaxTessLevel 12288 MaxTessLevel 24576 MaxTessLevel 49152 MaxTessLevel 98304 MaxTessLevel 196608 MaxTessLevel 393216 MaxTessLevel 786432 MaxTessLevel 1572864 MaxTessLevel 3145728 MaxTessLevel 6291456 MaxTessLevel 12582912 MaxTessLevel 25165824 MaxTessLevel 50331648 MaxTessLevel 100663296 MaxTessLevel 201326592 MaxTessLevel 402653184 MaxTessLevel 805306368 MaxTessLevel 1610612736 MaxTessLevel 3221225472 MaxTessLevel 6442450944 MaxTessLevel 12884901888 MaxTessLevel 25769803776 MaxTessLevel 51539607552 MaxTessLevel 103079215088 MaxTessLevel 206158430176 MaxTessLevel 412316860352 MaxTessLevel 824633720704 MaxTessLevel 1649267441408 MaxTessLevel 3298534882816 MaxTessLevel 6597069765632 MaxTessLevel 13194139531264 MaxTessLevel 26388279062528 MaxTessLevel 52776558125056 MaxTessLevel 10555311625112 MaxTessLevel 21110623250224 MaxTessLevel 42221246500448 MaxTessLevel 84442493000896 MaxTessLevel 168884986001792 MaxTessLevel 337769972003584 MaxTessLevel 675539944007168 MaxTessLevel 1351079888014336 MaxTessLevel 2702159776028672 MaxTessLevel 5404319552057344 MaxTessLevel 1080863910414688 MaxTessLevel 2161727820829376 MaxTessLevel 4323455641658752 MaxTessLevel 8646911283217504 MaxTessLevel 1729382256643504 MaxTessLevel 3458764513287008 MaxTessLevel 6917529026574016 MaxTessLevel 1383505805314824 MaxTessLevel 2767011610629648 MaxTessLevel 5534023221259296 MaxTessLevel 11068046442518592 MaxTessLevel 22136092885037184 MaxTessLevel 44272185770074368 MaxTessLevel 88544371540148736 MaxTessLevel 17708874308029744 MaxTessLevel 35417748616059488 MaxTessLevel 70835497232118976 MaxTessLevel 141670994464237952 MaxTessLevel 283341988928475904 MaxTessLevel 566683977856951808 MaxTessLevel 1133367955713803616 MaxTessLevel 2266735911427607232 MaxTessLevel 4533471822855214464 MaxTessLevel 9066943645710428928 MaxTessLevel 18133887291420857856 MaxTessLevel 36267774582841715712 MaxTessLevel 72535549165683431424 MaxTessLevel 145071098331366862848 MaxTessLevel 290142196662733725696 MaxTessLevel 580284393325467451392 MaxTessLevel 116056878665093490272 MaxTessLevel 232113757330186980544 MaxTessLevel 464227514660373961088 MaxTessLevel 928455029320747922176 MaxTessLevel 1856910058641495844352 MaxTessLevel 3713820117282991688704 MaxTessLevel 7427640234565983377408 MaxTessLevel 14855280469131966754816 MaxTessLevel 29710560938263933509632 MaxTessLevel 59421121876527867019264 MaxTessLevel 11884224375305573403856 MaxTessLevel 23768448750611146807712 MaxTessLevel 47536897501222293615424 MaxTessLevel 95073795002444587230848 MaxTessLevel 190147590004889754461696 MaxTessLevel 380295180009779508923392 MaxTessLevel 760590360019559017846784 MaxTessLevel 152118072003918803569352 MaxTessLevel 304236144007837607138704 MaxTessLevel 608472288015675214277408 MaxTessLevel 1216944576031350428554816 MaxTessLevel 2433889152062700857109632 MaxTessLevel 4867778304125401714219264 MaxTessLevel 9735556608250803428438528 MaxTessLevel 19471113216501606856877056 MaxTessLevel 38942226432003213713754112 MaxTessLevel 77884452864006427427508224 MaxTessLevel 15576890572801285485501648 MaxTessLevel 31153781145602565971003296 MaxTessLevel 62307562291205131942006592 MaxTessLevel 12461512458241026388401384 MaxTessLevel 24923024916482052776802768 MaxTessLevel 49846049832964105553605536 MaxTessLevel 99692099665928211107211072 MaxTessLevel 19938419933185642221422144 MaxTessLevel 39876839866371284442844288 MaxTessLevel 79753679732742568885688576 MaxTessLevel 159507359465485137771377152 MaxTessLevel 319014718930970275542754304 MaxTessLevel 638029437861940551085508608 MaxTessLevel 1276058875723881102170177216 MaxTessLevel 2552117751447762204340354432 MaxTessLevel 5104235502895524408680708864 MaxTessLevel 10208471005791048817361417728 MaxTessLevel 20416942011582097634722835456 MaxTessLevel 40833884023164195269445670912 MaxTessLevel 81667768046328390538891341824 MaxTessLevel 163335536092656781077782683648 MaxTessLevel 326671072185313562155565367296 MaxTessLevel 653342144370627124311130734592 MaxTessLevel 130668428644125424622261468184 MaxTessLevel 261336857288250849244522936368 MaxTessLevel 522673714576501698489045873136 MaxTessLevel 1045347429153003396978091762736 MaxTessLevel 2090694858306006793956183525472 MaxTessLevel 4181389716612013587912367050944 MaxTessLevel 8362779433224027175824734101888 MaxTessLevel 16725558866480554351649488203776 MaxTessLevel 33451117732961108703298976407552 MaxTessLevel 66902235465922217406597952815104 MaxTessLevel 13380447093184443481319505620208 MaxTessLevel 26760894186368886962638511240416 MaxTessLevel 53521788372737773925277022480832 MaxTessLevel 107043576745475547850554044961664 MaxTessLevel 214087153490951095701108089923328 MaxTessLevel 428174306981902191402216179846656 MaxTessLevel 856348613963804382804432359693312 MaxTessLevel 171269722792760876560864671938624 MaxTessLevel 342539445585521753121729343877248 MaxTessLevel 685078891171043506243458687754496 MaxTessLevel 137015778234208701248691737550992 MaxTessLevel 274031556468417402497383475101984 MaxTessLevel 548063112936834804994766950203968 MaxTessLevel 1096126225873669609989339004407936 MaxTessLevel 2192252451747339219978678008815872 MaxTessLevel 4384504903494678439957356001631744 MaxTessLevel 8769009806989356879914712003263488 MaxTessLevel 17538019613978713759829440064526976 MaxTessLevel 35076039227957427519658880012853952 MaxTessLevel 70152078455914855039317760025707808 MaxTessLevel 14030415691182911007863520051415616 MaxTessLevel 28060831382365822015727040102831232 MaxTessLevel 56121662764731644031454080205662464 MaxTessLevel 11224332532946328806290816041132928 MaxTessLevel 22448665065892657612581632082265856 MaxTessLevel 44897330131785315225163264164531712 MaxTessLevel 89794660263570630450326528329062824 MaxTessLevel 179589320527141260800653056658125648 MaxTessLevel 359178641054282521601306113316253296 MaxTessLevel 718357282108565043202612226632506592 MaxTessLevel 1436714564217130086405244453265013184 MaxTessLevel 2873429128434260172810488906530026336 MaxTessLevel 5746858256868520345620977813060052672 MaxTessLevel 1149371651373704069121955562612010544 MaxTessLevel 2298743302747408138243911125224021088 MaxTessLevel 4597486605494816276487822250448042176 MaxTessLevel 9194973210989632552975644500896084352 MaxTessLevel 1838994642197926510595128900179216864 MaxTessLevel 3677989284395853021185257800358433288 MaxTessLevel 7355978568791706042370515600716866776 MaxTessLevel 1471195713758341208474103120143373352 MaxTessLevel 2942391427516682416948206240286746704 MaxTessLevel 5884782855033364833896412480573493408 MaxTessLevel 11769565710066729667792824961146986816 MaxTessLevel 23539131420133459335585649922293973632 MaxTessLevel 47078262840266918671171299844587947264 MaxTessLevel 94156525680533837342342599689175894528 MaxTessLevel 18831305136106767468468519937835178856 MaxTessLevel 37662610272213534936937039875670357712 MaxTessLevel 75325220544427069873874079751340715424 MaxTessLevel 15065044108885413974758159502681422848 MaxTessLevel 30130088217770827949516319005362845696 MaxTessLevel 60260176435541655899032638010725691392 MaxTessLevel 12052035287072321179806527602145138384 MaxTessLevel 24104070574144642359613055204285276768 MaxTessLevel 48208141148289284719226110408570553536 MaxTessLevel 9641628229657856943845222081714110712 MaxTessLevel 19283256459315713887690440163428220424 MaxTessLevel 38566512918631427775380880326856440848 MaxTessLevel 77133025837262855550761760653712881696 MaxTessLevel 154266051674525711115323321307425763392 MaxTessLevel 308532103349051422230646642614851526784 MaxTessLevel 617064206698102844461293285229703053568 MaxTessLevel 123412841339620568892586561045940610734 MaxTessLevel 246825682679241137785173122091881221488 MaxTessLevel 493651365358482275570346244183762442976 MaxTessLevel 98730273071696455114069248836752488552 MaxTessLevel 19746054614339291022813849767350497104 MaxTessLevel 39492109228678582045627699534700994208 MaxTessLevel 78984218457357164091255399069400988416 MaxTessLevel 15796843691471432818251079813880097632 MaxTessLevel 31593687382942865636502159627760095264 MaxTessLevel 63187374765885731273004319255520090528 MaxTessLevel 12637474953177146254600863851040091056 MaxTessLevel 25274949906354292509201727702080082112 MaxTessLevel 50549899812708585018403455404160084224 MaxTessLevel 10109979962541717023680691080832008848 MaxTessLevel 20219959925083434047361382161664001696 MaxTessLevel 40439919850166868094722764323328003392 MaxTessLevel 80879839700333736189445528646656006784 MaxTessLevel 161759679400667472378811057293312013568 MaxTessLevel 323519358801334944757622114586624027136 MaxTessLevel 647038717602669889515244229173248054272 MaxTessLevel 129407743520533977903048845834649610944 MaxTessLevel 258815487041067955806097691669299221888 MaxTessLevel 517630974082135911612195383338598443776 MaxTessLevel 103526194816427182322490676667797687552 MaxTessLevel 207052389632854364644981353335595375056 MaxTessLevel 414104779265708729289962706671190750112 MaxTessLevel 828209558531417458579925401342381500224 MaxTessLevel 1656419117062835971559850802684763004448 MaxTessLevel 3312838234125671943119701605369326008896 MaxTessLevel 6625676468251343886239403210738652017792 MaxTessLevel 13251352936502687772478864421477304035584 MaxTessLevel 26502705873005375544957728842954608071168 MaxTessLevel 53005411746010751089915457685909216142336 MaxTessLevel 10601082349202150217983091537181843228672 MaxTessLevel 21202164698404300435966183074363686457344 MaxTessLevel 42404329396808600871932366148727372914688 MaxTessLevel 84808658793617201743864732297454745829376 MaxTessLevel 16961731796834403486773466459490949165872 MaxTessLevel 33923463593668806973546932918981898331744 MaxTessLevel 67846927187337613947093865837963786663488 MaxTessLevel 13569385437467522789418773167592757332776 MaxTessLevel 27138770874935045578837546335185514665552 MaxTessLevel 54277541749870091157675092670371029331104 MaxTessLevel 10855508349774018235337518534074205862208 MaxTessLevel 2171101669954803647067503706814841172416 MaxTessLevel 4342203339909607294135007413629682344832 MaxTessLevel 8684406679819214588270014827259364689664 MaxTessLevel 1736881339623842917654002965458732937328 MaxTessLevel 3473762679247685835308005930917465874656 MaxTessLevel 6947525358495371670616001861834931743312 MaxTessLevel 1389505071698874334123203732366986348624 MaxTessLevel 2779010143397748668246407464733972692448 MaxTessLevel 5558020286795497336492814929467945384896 MaxTessLevel 1111604057390995667385628945893589076992 MaxTessLevel 2223208114781991334771257891787178153984 MaxTessLevel 4446416229563982669542515783574356307968 MaxTessLevel 8892832459127965339085031567148712615936 MaxTessLevel 1778566491825593067817063113429424523872 MaxTessLevel 3557132983651186135634126226858849047744 MaxTessLevel 7114265967302372271268252453717698095488 MaxTessLevel 1422853193404744442553650490743396190976 MaxTessLevel 2845706386809488885107300981486792381952 MaxTessLevel 5691412773618977770214601962973584763904 MaxTessLevel 1138282554723755544042920392594716932808 MaxTessLevel 2276565109447511088085840785189433864016 MaxTessLevel 4553130218895022176171681570378867728032 MaxTessLevel 9106260437780044352343363140757735456664 MaxTessLevel 18212520875560088704686766281515470913328 MaxTessLevel 3642504175112017740937353256303094082656 MaxTessLevel 72850083502240354818747065126061881653136 MaxTessLevel 14570016704480710963744130245212376330672 MaxTessLevel 29140033408961421927488260490424752661344 MaxTessLevel 58280066817922843854976520980849505322688 MaxTessLevel 11656013363845787709953254196169901065336 MaxTessLevel 23312026727691575419906508392339802130672 MaxTessLevel 46624053455383150839813016784679604261344 MaxTessLevel 93248106910766301679626033569359208482688 MaxTessLevel 18649621382153260335925206713879441685336 MaxTessLevel 37299242764306520671850413427758883370672 MaxTessLevel 74598485528613041343700826855517766741344 MaxTessLevel 14919697105722682266740153711035533482688 MaxTessLevel 29839394211445364533480307422071066965376 MaxTessLevel 59678788422890729066960614844142133931552 MaxTessLevel 11935757684578145813932122888828227863104 MaxTessLevel 23871515369156291627864245777656455772008 MaxTessLevel 47743030738312583255728491555312911540016 MaxTessLevel 95486061476625166511456983110625823080032 MaxTessLevel 190972122933252332222934766221256466160064 MaxTessLevel 381944245866504664445869532442512932320128 MaxTessLevel 763888491733009328891739064885025866640256 MaxTessLevel 1527776983466018657783478129770051733280512 MaxTessLevel 3055553966932037315566956259540103466561024 MaxTessLevel 6111107933864074631133912519080206933122048 MaxTessLevel 1222221586772814926226782503816041386244096 MaxTessLevel 2444443173545629852453565007632082772488192 MaxTessLevel 4888886347091259704907130015264165544976384 MaxTessLevel 9777772694182519409814260030528311089532768 MaxTessLevel 19555545388365238819628520061056622178565536 MaxTessLevel 39111090776730477639257040032113244437131072 MaxTessLevel 78222181553460955278514080064226488874262144 MaxTessLevel 156444363106921910557081600128453217748524288 MaxTessLevel 31288872621384382111416320025690635454704856 MaxTessLevel 6257774524276876422283264005138127090941912 MaxTessLevel 1251554904855373284566532001027645418183824 MaxTessLevel 2503109809670746569133064002055290836367648 MaxTessLevel 5006219619341493138266128004010581672735296 MaxTessLevel 10012439238682966764532456008021163345470592 MaxTessLevel </pre>		



Creating a Vulkan Data Buffer

```
VkBufferCreateInfo vbc;  
vbc.sType = VK_STRUCTURE_TYPE_BUFFER_CREATE_INFO;  
vbc.pNext = nullptr;  
vbc.flags = 0;  
vbc.size = << buffer size in bytes >>;  
vbc.usage = << buffer usage >>;  
VK_USAGE_TRANSFER_SRC_BIT  
VK_USAGE_TRANSFER_DST_BIT  
VK_USAGE_UNIFORM_TEXEL_BUFFER_BIT  
VK_USAGE_STORAGE_TEXEL_BUFFER_BIT  
VK_USAGE_UNIFORM_BUFFER_BIT  
VK_USAGE_STORAGE_BUFFER_BIT  
VK_USAGE_INDEX_BUFFER_BIT  
VK_USAGE_VERTEX_BUFFER_BIT  
VK_USAGE_INDIRECT_BUFFER_BIT  
vbc.sharingMode = << one of >>;  
VK_SHARING_MODE_EXCLUSIVE  
VK_SHARING_MODE_CONCURRENT  
vbc.queueFamilyIndexCount = 0;  
vbc.pQueueFamilyIndices = (const int32_t*)nullptr;  
  
result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &Buffer );
```



Finding the Right Type of Memory

112

```
int
FindMemoryThatIsHostVisible(
{
    VkPhysicalDeviceMemoryProperties     vpdmp;
    vkGetPhysicalDeviceMemoryProperties( PhysicalDevice, OUT &vpdmp );
    for( unsigned int i = 0; i < vpdmp.memoryTypeCount; i++ )
    {
        VkMemoryType vmt = vpdmp.memoryTypes[i];
        if( ( vmt.propertyFlags & VK_MEMORY_PROPERTY_HOST_VISIBLE_BIT ) != 0 )
        {
            return i;
        }
    }
    return -1;
}
```

Finding the Right Type of Memory

Finding the Right Type of Memory

Sidebar: The Vulkan Memory Allocator (VMA) 115

The **Vulkan Memory Allocator** is a set of functions to simplify your view of allocating buffer memory. I don't have experience using it (yet), so I'm not in a position to confidently comment on it. But, I am including its github link here and a little sample code in case you want to take a peek.

<https://github.com/GPUOpen-LibrariesAndSDKs/VulkanMemoryAllocator>

This repository includes a smattering of documentation.

mjb – July 24, 2020

Sidebar: The Vulkan Memory Allocator (VMA) 116

```
#define VMA_IMPLEMENTATION
#include "vk_mem_alloc.h"
...
VkBufferCreateInfo vbc;
...
VmaAllocationCreateInfo vaci;
vaci.physicalDevice = PhysicalDevice;
vaci.device = LogicalDevice;
vaci.usage = VMA_MEMORY_USAGE_GPU_ONLY;
...
VmaAllocator var;
vmaCreateAllocator( IN &vaci, OUT &var );
...
VkBuffer Buffer;
VmaAllocation van;
vmaCreateBuffer( IN var, IN &vbc, IN &vaci, OUT &Buffer, OUT &van, nullptr );
}

void *mappedDataAddr;
vmaMapMemory( IN var, IN van, OUT &mappedDataAddr );
memcpy( mappedDataAddr, &MyData, sizeof(MyData) );
vmaUnmapMemory( IN var, IN van );
```

mjb – July 24, 2020

Something I've Found Useful 117

I find it handy to encapsulate buffer information in a struct:

```
typedef struct MyBuffer
{
    VkDataBuffer        buffer;
    VkDeviceMemory     vdm;
    VkDeviceSize       size;
} MyBuffer;

...
MyBuffer      MyMatrixUniformBuffer;
```

It's the usual object-oriented benefit – you can pass around just one data-item and everyone can access whatever information they need.

It also makes it impossible to accidentally associate the wrong `VkDeviceMemory` and/or `VkDeviceSize` with the wrong data buffer.

mjb – July 24, 2020

Initializing a Data Buffer 118

It's the usual object-oriented benefit – you can pass around just one data-item and everyone can access whatever information they need.

```
VkResult
Init5DDataBuffer( VkDeviceSize size, VkBufferUsageFlags usage, OUT MyBuffer * pMyBuffer )
{
    ...
    vbc.size = pMyBuffer->size = size;
    ...
    result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &pMyBuffer->buffer );
    ...
    pMyBuffer->vdm = vdm;
}
```

mjb – July 24, 2020

Here's a C struct used by the Sample Code to hold some uniform variables 119

```
struct matBuf
{
    glm::mat4 uModelMatrix;
    glm::mat4 uViewMatrix;
    glm::mat4 uProjectionMatrix;
    glm::mat3 uNormalMatrix;
} Matrices;
```

Here's the associated GLSL shader code to access those uniform variables

```
layout( std140, set = 0, binding = 0 ) uniform matBuf
{
    mat4 uModelMatrix;
    mat4 uViewMatrix;
    mat4 uProjectionMatrix;
    mat4 uNormalMatrix;
} Matrices;
```

mjb – July 24, 2020

Filling those Uniform Variables 120

```
uint32_t          Height, Width;
const double FOV = glm::radians(60.); // field-of-view angle in radians

glm::vec3 eye(0.0, EYEDIST);
glm::vec3 look(0.0, 0.0);
glm::vec3 up(0.1, 0.0);

Matrices.uModelMatrix = glm::mat4( 1. ); // identity
Matrices.uViewMatrix = glm::lookAt( eye, look, up );
Matrices.uProjectionMatrix = glm::perspective( FOV, (double)Width/(double)Height, 0.1, 1000. );
Matrices.uProjectionMatrix[1][1] = -1.; // account for Vulkan's LH screen coordinate system
Matrices.uNormalMatrix = glm::inverseTranspose( glm::mat3( Matrices.uModelMatrix ) );
```

This code assumes that this line:
`#define GLM_FORCE_RADIANS`
is listed before GLM is included!

mjb – July 24, 2020

The Parade of Buffer Data 121

MyBuffer MyMatrixUniformBuffer;

The MyBuffer does not hold any actual data itself. It just information about what is in the data buffer

```
MyBuffer
VK_BUFFER_CREATE_INFO vbo;
VkDeviceSize size;
VkBufferUsageFlags usage;
OUT MyBuffer * pMyBuffer;
```

This C struct is holding the original data, written by the application.

```
struct matBuf {
    glm::mat4 matBuf; // Matrices;
    ...
}
```

Memory mapped copy operation

The Data Buffer in GPU memory is holding the copied data. It is readable by the shaders

```
uniform matBuf Matrices;
```

matBuf.vbo.size = 0; // Matrices.uModelMatrix = glm::mat4(1); // identity

Matrices.uViewMatrix = glm::lookAt(eye(0,0,EYEDIST), look(0,0,0));
 matBuf.vbo.size = 12; // Matrices.uViewMatrix = glm::lookAt(eye(0,0,EYEDIST), look(0,0,0));
 matBuf.vbo.size = 12; // Matrices.uProjectionMatrix = glm::perspective(FOV, (double)Width/(double)Height, 0.1, 1000.);
 matBuf.vbo.size = 12; // Matrices.uProjectionMatrix = glm::inverseTranspose(glm::mat3(Matrices.uModelMatrix));

mjb – July 24, 2020

Filling the Data Buffer 122

Init05UniformBuffer(sizeof(Matrices), OUT &MyMatrixUniformBuffer)
Fill05DataBuffer(MyMatrixUniformBuffer, IN (void *) &Matrices);

```
glm::vec3 eye(0,0,EYEDIST);
glm::vec3 look(0,0,0);
glm::vec3 up(0,1,0);

Matrices.uModelMatrix = glm::lookAt( eye, look, up );

Matrices.uViewMatrix = glm::lookAt( eye, look, up );

Matrices.uProjectionMatrix = glm::perspective( FOV, (double)Width/(double)Height, 0.1, 1000. );
Matrices.uProjectionMatrix[1][1] = -1.;

Matrices.uNormalMatrix = glm::inverseTranspose( glm::mat3( Matrices.uModelMatrix ) );
```

mjb – July 24, 2020

Creating and Filling the Data Buffer – the Details 123

```
VkResult
Fill05DataBuffer( IN VkDeviceSize size, VkBufferUsageFlags usage, OUT MyBuffer * pMyBuffer )
{
    VkResult result = VK_SUCCESS;
    VkBufferCreateInfo vbc;
    vbc.sType = VK_STRUCTURE_TYPE_BUFFER_CREATE_INFO;
    vbc.pNext = nullptr;
    vbc.flags = 0;
    vbc.size = pMyBuffer->size = size;
    vbc.usage = usage;
    vbc.sharingMode = VK_SHARING_MODE_EXCLUSIVE;
    vbc.queueFamilyIndexCount = 0;
    vbc.pQueueFamilyIndices = (const uint32_t *)nullptr;
    result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &pMyBuffer->buffer );

    VkMemoryRequirements vmr;
    vkGetBufferMemoryRequirements( LogicalDevice, IN pMyBuffer->buffer, OUT &vmr ); // fills vmr

    VkMemoryAllocateInfo vmai;
    vmai.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
    vmai.pNext = nullptr;
    vmai.allocationSize = vmr.size;
    vmai.memoryTypeIndex = FindMemoryThatIsHostVisible( );
    result = vkAllocateMemory( LogicalDevice, IN &vmai, PALLOCATOR, OUT &vdm );
    pMyBuffer->vdm = vdm;

    result = vkBindBufferMemory( LogicalDevice, pMyBuffer->buffer, IN vdm, OFFSET_ZERO );
    return result;
}
```

mjb – July 24, 2020

Creating and Filling the Data Buffer – the Details 124

```
VkResult
Fill05DataBuffer( IN MyBuffer myBuffer, IN void * data )
{
    // the size of the data had better match the size that was used to init the buffer!

    void * pGpuMemory;
    vkMapMemory( LogicalDevice, IN myBuffer.vdm, 0, VK_WHOLE_SIZE, 0, OUT &pGpuMemory );
    memcpy( pGpuMemory, data, (size_t)myBuffer.size );
    vkUnmapMemory( LogicalDevice, IN myBuffer.vdm );
    return VK_SUCCESS;
}
```

Remember – to Vulkan and GPU memory, these are just bits. It is up to you to handle their meaning correctly.

mjb – July 24, 2020

Creating and Filling the Data Buffer – the Details 125

```
VkResult
Fill05DataBuffer( IN MyBuffer myBuffer, IN void * data )
{
    // the size of the data had better match the size that was used to init the buffer!

    void * pGpuMemory;
    vkMapMemory( LogicalDevice, IN myBuffer.vdm, 0, VK_WHOLE_SIZE, 0, OUT &pGpuMemory );
    memcpy( pGpuMemory, data, (size_t)myBuffer.size );
    vkUnmapMemory( LogicalDevice, IN myBuffer.vdm );
    return VK_SUCCESS;
}
```

Remember – to Vulkan and GPU memory, these are just bits. It is up to you to handle their meaning correctly.

mjb – July 24, 2020

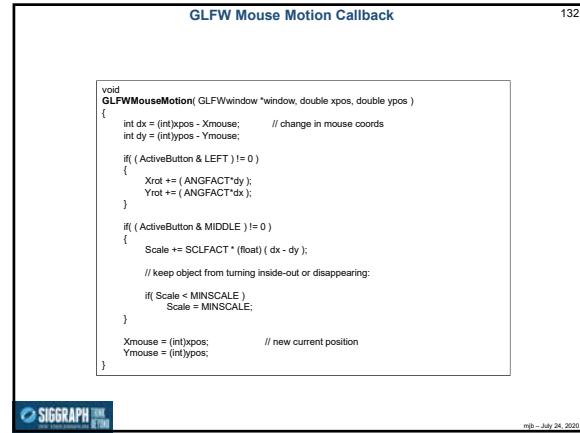
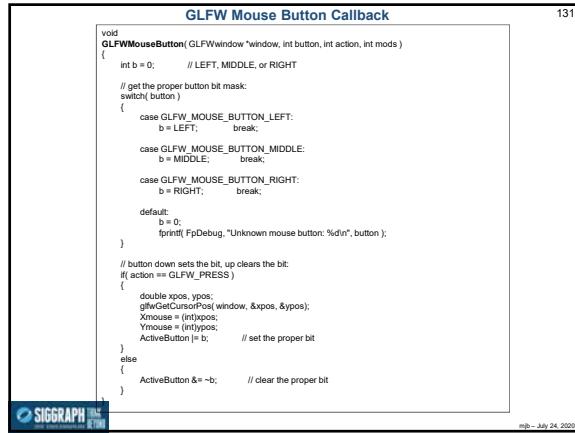
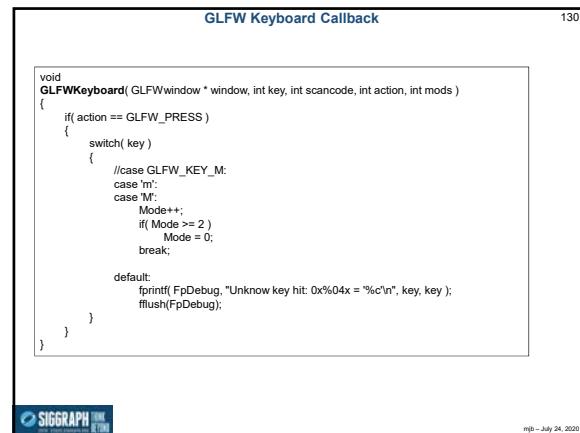
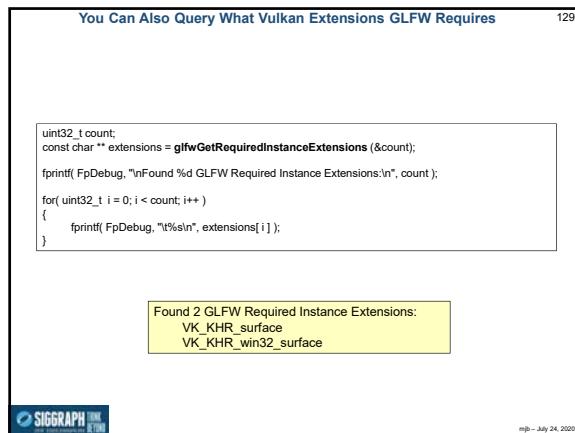
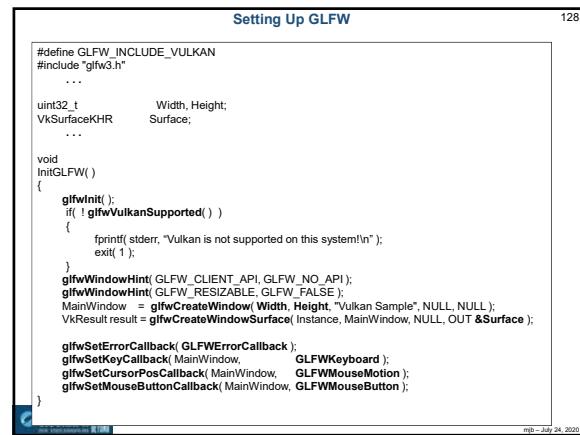
Vulkan.
GLFW

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020



Looping and Closing GLFW 133

```

while( glfwWindowShouldClose( MainWindow ) == 0 )
{
    glfwPollEvents(); // Does not block – processes any waiting events, then returns
    Time = glfwGetTime(); // elapsed time, in double-precision seconds
    UpdateScene();
    RenderScene();
}

vkQueueWaitIdle( Queue );
vkDeviceWaitIdle( LogicalDevice );
DestroyAllVulkan();
glfwDestroyWindow( MainWindow );
glfwTerminate();

```

mjb – July 24, 2020

Looping and Closing GLFW 134

If you would like to *block* waiting for events, use:

```
glfwWaitEvents();
```

You can have the blocking wake up after a timeout period with:

```
glfwWaitEventsTimeout( double secs );
```

You can wake up one of these blocks from another thread with:

```
glfwPostEmptyEvent();
```

mjb – July 24, 2020

Vulkan.

GLM

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

What is GLM? 136

GLM is a set of C++ classes and functions to fill in the programming gaps in writing the basic vector and matrix mathematics for OpenGL applications. However, even though it was written for OpenGL, it works fine with Vulkan.

Even though GLM looks like a library, it actually isn't – it is all specified in *.hpp header files so that it gets compiled in with your source code.

You can find it at:
<http://glm.g-truc.net/0.9.8.5/>

You invoke GLM like this:

```
#define GLM_FORCE_RADIANS
```

OpenGL treats all angles as given in *degrees*. This line forces GLM to treat all angles as given in *radians*. I recommend this so that *all* angles you create in *all* programming will be in radians.

If GLM is not installed in a system place, put it somewhere you can get access to. Later on, these notes will show you how to use it from there.

mjb – July 24, 2020

Why are we even talking about this? 137

All of the things that we have talked about being *deprecated* in OpenGL are *really deprecated* in Vulkan -- built-in pipeline transformations, begin-end, fixed-function, etc. So, where you might have said in OpenGL:

```
glMatrixModel( GL_MODELVIEW );
glLoadIdentity();
gluLookAt( 0., 0., 0., 0., 0., 0., 1., 0., 0. );
glRotatef( GLfloatYrot, 0., 1., 0. );
glRotatef( GLfloatXrot, 1., 0., 0. );
glScalef( GLfloatScale, (GLfloat)Scale, (GLfloat)Scale );
```

you would now say:

```
glm::mat4 modelview = glm::mat4( 1. ); // identity
glm::vec3 eye( 0., 0., 3. );
glm::vec3 look( 0., 0., 0. );
glm::vec3 up( 0., 1., 0. );
modelview = glm::lookAt( eye, look, up ); // {x,y,z} = [V]{x,y,z}
modelview = glm::rotate( modelview, D2R * Yrot, glm::vec3( 0., 1., 0. ) ); // {x,y,z} = [V]{y}[r]{x,y,z}
modelview = glm::rotate( modelview, D2R * Xrot, glm::vec3( 1., 0., 0. ) ); // {x,y,z} = [V]{y}[r][x]{x,y,z}
modelview = glm::scale( modelview, glm::vec3( Scale, Scale, Scale ) ); // {x,y,z} = [V]{y}[r][s]{x,y,z}
```

This is exactly the same concept as OpenGL, but a different expression of it. Read on for details ...

mjb – July 24, 2020

The Most Useful GLM Variables, Operations, and Functions 138

```
// constructor:
glm::mat4( 1. ); // identity matrix
glm::vec4();
glm::vec3();

// multiplications:
glm::mat4 * glm::mat4;
glm::mat4 * glm::vec4;
glm::mat4 * glm::vec4( glm::vec3, 1. ) // promote a vec3 to a vec4 via a constructor

// emulating OpenGL transformations with concatenation:
glm::mat4 glm::rotate( glm::mat4 const & m, float angle, glm::vec3 const & axis );
glm::mat4 glm::scale( glm::mat4 const & m, glm::vec3 const & factors );
glm::mat4 glm::translate( glm::mat4 const & m, glm::vec3 const & translation );
```

GLM recommends that you use the "glm::" syntax and avoid "using namespace" syntax because they have not made any effort to create unique function names

mjb – July 24, 2020

The Most Useful GLM Variables, Operations, and Functions 139

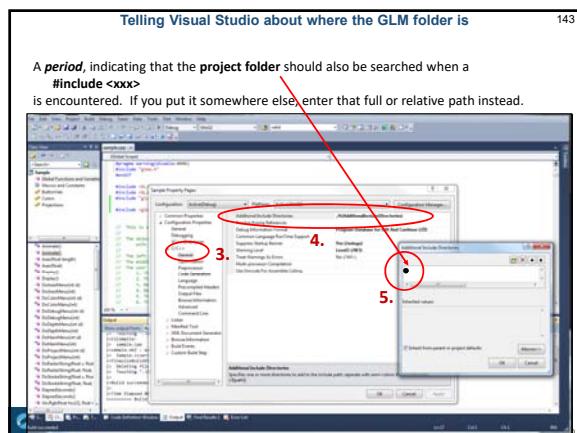
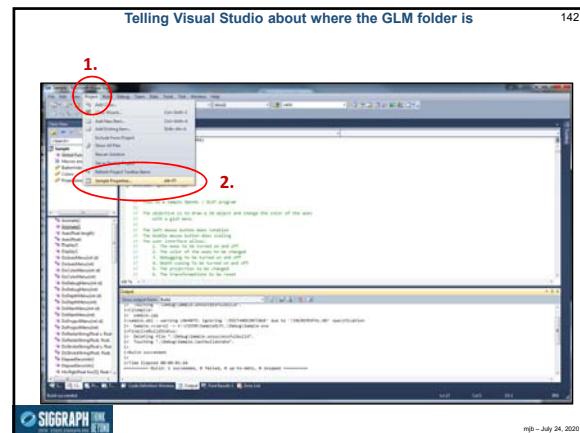
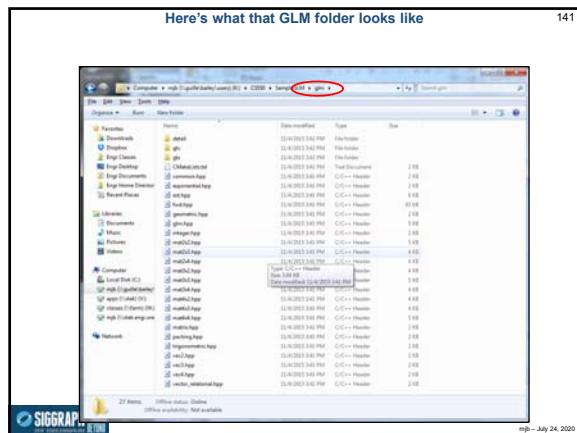
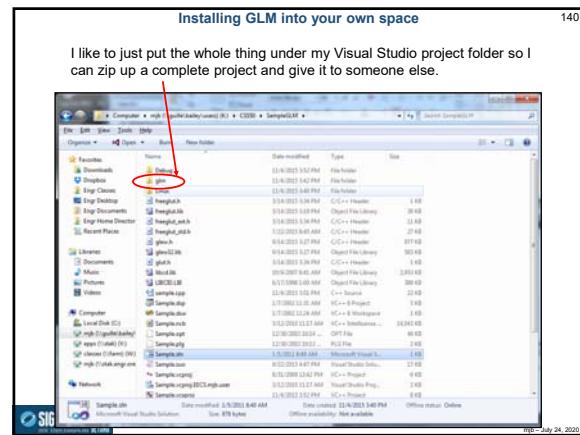
```
// viewing volume (assign, not concatenate):
glm::mat4 glm::ortho( float left, float right, float bottom, float top, float near, float far );
glm::mat4 glm::ortho( float left, float right, float bottom, float top );

glm::mat4 glm::frustum( float left, float right, float bottom, float top, float near, float far );
glm::mat4 glm::perspective( float fovy, float aspect, float near, float far);

// viewing (assign, not concatenate):
glm::mat4 glm::lookAt( glm::vec3 const & eye, glm::vec3 const & look, glm::vec3 const & up );
```



mjb – July 24, 2020



GLM in the Vulkan sample.cpp Program 144

```
#if UseMouse
{
    if( Scale < MINSCALE )
        Matrices.uModelMatrix = glm::mat4( 1. ); // identity
    Matrices.uModelMatrix = glm::rotate( Matrices.uModelMatrix, Xrot, glm::vec3( 0.1, 0.0 ) );
    Matrices.uModelMatrix = glm::rotate( Matrices.uModelMatrix, Yrot, glm::vec3( 0.0, 1.0 ) );
    Matrices.uModelMatrix = glm::scale( Matrices.uModelMatrix, glm::vec3( Scale, Scale, Scale ) );
    // done this way, the Scale is applied first, then the Xrot, then the Yrot
}
else
{
    if( !Paused )
    {
        const glm::vec3 axis = glm::vec3( 0.1, 0.0 );
        Matrices.uModelMatrix = glm::rotate( glm::mat4( 1. ), (float)glm::radians( 360.0f * Time / SECONDS_PER_CYCLE ), axis );
    }
}

glm::vec3 eye( 0.0, 0.0, 1.0 );
glm::vec3 up( 0.0, 1.0 );
Matrices.uViewMatrix = glm::lookAt( eye, look, up );

Matrices.uProjectionMatrix = glm::perspective( FOV, (double)Width/(double)Height, 0.1f, 1000.0f );
Matrices.uProjectionMatrix[1][1] = -1.0; // Vulkan's projected Y is inverted from OpenGL

Matrices.uNormalMatrix = glm::inverseTranspose( glm::mat3( Matrices.uModelMatrix ) ); // note: inverseTransform!
F10SDataBuffer( MyMatrixUniformBuffer, (void *) &Matrices );
F10SDataBuffer( MyMiscUniformBuffer, (void *) &Misc );
Misc.uTime = (float)Time;
Misc.uMode = Mode;
F10SDataBuffer( MyMiscUniformBuffer, (void *) &Misc );
```



mjb – July 24, 2020

How Does this Matrix Stuff Really Work?

$x' = Ax + By + Cz + D$ This is called a "Linear Transformation" because all of the coordinates are raised to the 1st power, that is, there are no x^2 , x^3 , etc. terms.

$y' = Ex + Fy + Gz + H$

$z' = Ix + Jy + Kz + L$

Or, in matrix form:

$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} A & B & C & D \\ E & F & G & H \\ I & J & K & L \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$

mjb – July 24, 2020

Transformation Matrices

Translation

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} 1 & 0 & 0 & T_x \\ 0 & 1 & 0 & T_y \\ 0 & 0 & 1 & T_z \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

Rotation about X

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \cos \theta & -\sin \theta & 0 \\ 0 & \sin \theta & \cos \theta & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

Scaling

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} S_x & 0 & 0 & 0 \\ 0 & S_y & 0 & 0 \\ 0 & 0 & S_z & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

Rotation about Y

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} \cos \theta & 0 & \sin \theta & 0 \\ 0 & 1 & 0 & 0 \\ -\sin \theta & 0 & \cos \theta & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

Rotation about Z

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{bmatrix} \cos \theta & -\sin \theta & 0 & 0 \\ \sin \theta & \cos \theta & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

mjb – July 24, 2020

How it Really Works :-)

$\begin{bmatrix} \cos 90^\circ & \sin 90^\circ \\ -\sin 90^\circ & \cos 90^\circ \end{bmatrix} \begin{pmatrix} a_1 \\ a_2 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \end{pmatrix}$

<http://xkcd.com>

mjb – July 24, 2020

The Rotation Matrix for an Angle (θ) about an Arbitrary Axis (A_x, A_y, A_z)

$$[M] = \begin{bmatrix} A_x A_z + \cos \theta (1 - A_x A_z) & A_x A_y - \cos \theta (A_x A_z) - \sin \theta A_z & A_x A_z - \cos \theta (A_x A_z) + \sin \theta A_z \\ A_y A_z - \cos \theta (A_y A_z) + \sin \theta A_z & A_y A_x + \cos \theta (1 - A_y A_z) & A_y A_z - \cos \theta (A_y A_z) - \sin \theta A_z \\ A_z A_x - \cos \theta (A_z A_x) - \sin \theta A_x & A_z A_y - \cos \theta (A_z A_x) + \sin \theta A_x & A_z A_z + \cos \theta (1 - A_z A_x) \end{bmatrix}$$

mjb – July 24, 2020

For this to be correct, A must be a unit vector

Compound Transformations

Q: Our rotation matrices only work around the origin? What if we want to rotate about an arbitrary point (A,B)?

A: We create more than one matrix.

Write it

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{pmatrix} 3 \\ T_{+A+B} \end{pmatrix} \cdot \begin{pmatrix} 2 \\ R_\theta \end{pmatrix} \cdot \begin{pmatrix} 1 \\ T_{-A-B} \end{pmatrix} \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

Say it

mjb – July 24, 2020

Matrix Multiplication is not Commutative

Rotate, then translate

Translate, then rotate

mjb – July 24, 2020

Matrix Multiplication is Associative

151

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \left[T_{+A,+B} \right] \cdot \left[R_\theta \right] \cdot \left[T_{-A,-B} \right] \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \underbrace{\left[T_{+A,+B} \right] \cdot \left[R_\theta \right] \cdot \left[T_{-A,-B} \right]}_{\text{One matrix -- the Current Transformation Matrix, or CTM}} \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

SIGGRAPH INK RETURNS

mjb – July 24, 2020

One Matrix to Rule Them All

152

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \left[\left[T_{+A,+B} \right] \cdot \left[R_\theta \right] \cdot \left[T_{-A,-B} \right] \right] \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$

```
glm::mat4 Model = glm::mat4( 1. );
Model = glm::translate(Model, glm::vec3(A, B, 0. ) );
Model = glm::rotate(Model, thetaRadians, glm::vec3(Ax, Ay, Az ) );
Model = glm::translate(Model, glm::vec3(-A, -B, 0. ) );

glm::vec3 eye(0.0, EYEDIST);
glm::vec3 look(0.0, 0.0, -1.0); glm::vec3 up(0,1.0,0);
glm::mat4 View = glm::lookAt( eye, look, up );

glm::mat4 Projection = glm::perspective( FOV, (double)Width/(double)Height, 0.1, 1000. );
Projection[1][1] = -1.;

...
glm::mat3 Matrix = Projection * View * Model;
glm::mat3 NormalMatrix = glm::inverseTranspose( glm::mat3(Model) );
```

SIGGRAPH INK RETURNS

mjb – July 24, 2020

Why Isn't The Normal Matrix exactly the same as the Model Matrix?

If it is, if the Model Matrix is all rotations and uniform scalings, but if it has non-uniform scalings, then it is not.
These diagrams show you why.

Original object and normal

Wrong!

Right!

glm::mat3 NormalMatrix = glm::mat3(Model);

glm::mat3 NormalMatrix = glm::inverseTranspose(glm::mat3(Model));

SIGGRAPH INK RETURNS

mjb – July 24, 2020

Vulkan.

Instancing

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a [Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License](#)

<http://cs.oregonstate.edu/~mjb/vulkan>

SIGGRAPH INK RETURNS

mjb – July 24, 2020

Instancing – What and why?

155

- Instancing is the ability to draw the same object multiple times
- It uses all the same vertices and graphics pipeline each time
- It avoids the overhead of the program asking to have the object drawn again, letting the GPU/driver handle all of that

```
vkCmdDraw(CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstVertex, firstInstance );
```

But, this will only get us multiple instances of identical objects drawn on top of each other. How can we make each instance look differently?

BTW, when not using instancing, be sure the **instanceCount** is 1, not 0 !

SIGGRAPH INK RETURNS

mjb – July 24, 2020

Making each Instance look differently -- Approach #1

156

Use the built-in vertex shader variable **gl_InstanceIndex** to define a unique display property, such as position or color.

gl_InstanceIndex starts at 0

In the vertex shader:

```
out vec3 vColor;
const int NUMINSTANCES = 16;
const float DELTA = 3.0;

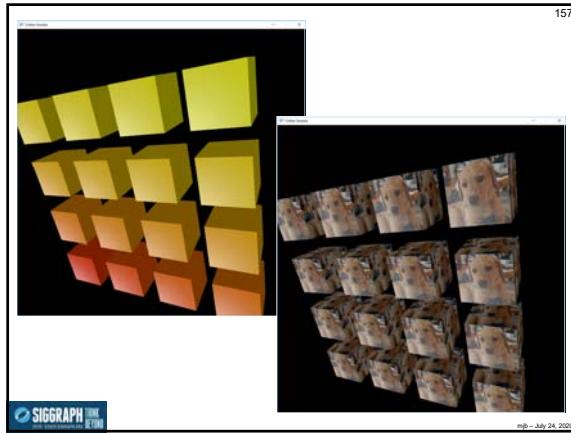
float xdelta = DELTA * sqrtf( float( gl_InstanceIndex % 4 ) );
float ydelta = DELTA * sqrtf( float( gl_InstanceIndex / 4 ) );
vColor = vec3( 1., float( (1.+gl_InstanceIndex) / float( NUMINSTANCES ), 0. );

xdelta -= DELTA * sqrtf( float( NUMINSTANCES ) ) / 2;
ydelta -= DELTA * sqrtf( float( NUMINSTANCES ) ) / 2;
vec4 vertex = vec4( aVertex.xyz + vec3( xdelta, ydelta, 0. ), 1. );

gl_Position = PVM * vertex; // [p][v][m]
```

SIGGRAPH INK RETURNS

mjb – July 24, 2020



Making each Instance look differently -- Approach #2 158

Put the unique characteristics in a uniform buffer array and reference them

Still uses `gl_InstanceIndex`

In the vertex shader:

```
layout( std140, set = 3, binding = 0 ) uniform colorBuf
{
    vec3 uColors[1024];
} Colors;

out vec3 vColor;

...

int index = gl_InstanceIndex % 1024; // or "& 1023" – gives 0 - 1023
vColor = Colors.uColors[ index ];

vec4 vertex = ...;

gl_Position = PVM * vertex; // [p][v][m]
```

SIGGRAPH THE LEARN. REACH. BELIEVE.

mjb – July 24, 2020

Vulkan.

The Graphics Pipeline Data Structure

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

SIGGRAPH THE LEARN. REACH. BELIEVE.

mjb – July 24, 2020

What is the Vulkan Graphics Pipeline? 160

Don't worry if this is too small to read – a larger version is coming up.

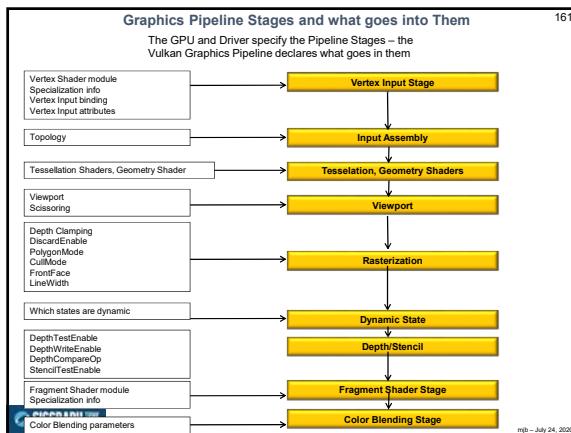
There is also a Vulkan Compute Pipeline Data Structure – we will get to that later.

Here's what you need to know:

1. The Vulkan Graphics Pipeline is like what OpenGL would call "The State", or "The Context". It is a **data structure**.
2. The Vulkan Graphics Pipeline is **not** the processes that OpenGL would call "the graphics pipeline".
3. For the most part, the Vulkan Graphics Pipeline Data Structure is **immutable** – that is, once this combination of state variables is combined into a Pipeline, that Pipeline never gets changed. To make new combinations of state variables, create a new Graphics Pipeline.
4. The shaders get compiled the rest of the way when their Graphics Pipeline gets created.

SIGGRAPH THE LEARN. REACH. BELIEVE.

mjb – July 24, 2020



The First Step: Create the Graphics Pipeline Layout 162

The Graphics Pipeline Layout is fairly static. Only the layout of the Descriptor Sets and information on the Push Constants need to be supplied.

```

VkResult
init4GraphicsPipelineLayout()
{
    VkResult result;
    VkPipelineLayoutCreateInfo
    vplci.Type = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
    vplci.pNext = nullptr;
    vplci.flags = 0;
    vplci.setLayoutCount = 4;
    vplci.pSetLayouts = &DescriptorsetLayout;
    vplci.pushConstantRangeCount = 0;
    vplci.pPushConstantRanges = (VKPushConstantRange *)nullptr;
    result = vkCreatePipelineLayout( LogicalDevice, IN &vplci, PALLOCATOR, OUT &GraphicsPipelineLayout );
    return result;
}

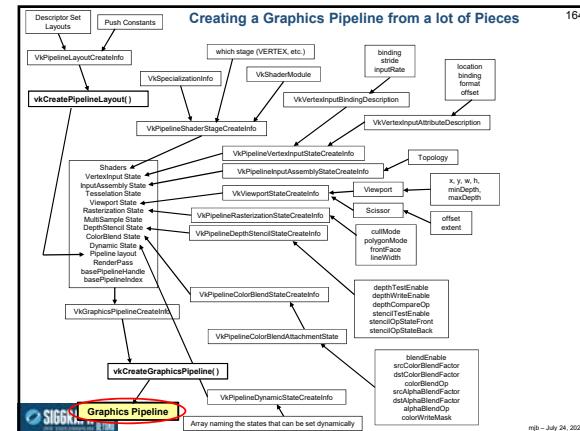
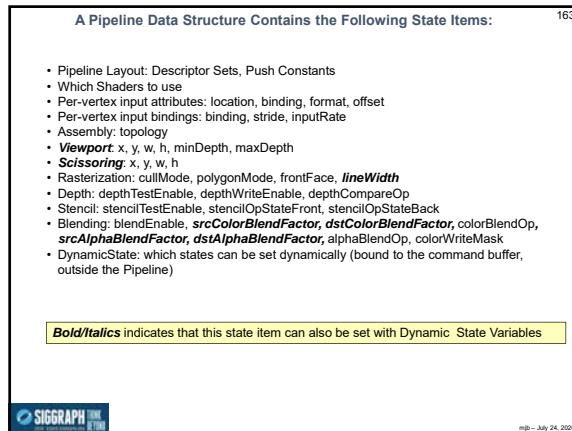
```

Let the Pipeline Layout know about the Descriptor Set and Push Constant layouts.

Why is this necessary? It is because the Descriptor Sets and Push Constants data structures have different sizes depending on how many of each you have. So, the exact structure of the Pipeline Layout depends on you telling Vulkan about the Descriptor Sets and Push Constants that you will be using.

SIGGRAPH THE LEARN. REACH. BELIEVE.

mjb – July 24, 2020



```

VkResult
Init14GraphicsVertexFragmentPipeline(VkShaderModule vertexShader, VkShaderModule fragmentShader,
VkPrimitiveTopology topology, OUT VkPipeline ^pGraphicsPipeline )
{
#ifdef ASSUMPTIONS
    vpb0.inputRate = VK_VERTEX_INPUT_RATE_VERTEX;
    vpb0.depthBias = 0.0f;
    vpb0.depthBiasScale = VK_FALSE;
    vpb0.depthBiasClamp = VK_FALSE;
    vpb0.cullMode = VK_CULL_MODE_NONE; // Don't do it because of the projectionMatrix[1][1] = -1;
    vpb0.frontFace = VK_FRONT_FACE_COUNTER_CLOCKWISE;
    vpb0.rasterizationSamples = VK_SAMPLE_COUNT_ONE_BIT;
    vpb0.blendEnable = VK_FALSE;
    vpb0.logicOpEnable = VK_FALSE;
    vpb0.colorBlendEnable = VK_TRUE;
    vpb0.depthWriteEnable = VK_TRUE;
    vpb0.depthCompareOp = VK_COMPARE_OP_LESS;
#endif
    ...
}

These settings seem pretty typical to me. Let's write a simplified Pipeline-creator that accepts Vertex and Fragment shader modules and the topology, and always uses the settings in red above.

```

The Shaders to Use

```

VkPipelineShaderStageCreateInfo
    .sType = VK_STRUCTURE_TYPE_PIPELINE_SHADER_STAGE_CREATE_INFO;
    .pNext = &vpsci[2];
    .pNext = NULL;
    .stage = VK_SHADER_STAGE_VERTEX_BIT;
    .module = &vertexShader;
    .name = "main";
    .specializationInfo = &vkSpecializationInfo *NULLptr;
#endif

VK_SHADER_STAGE_VERTEX_BIT
VK_SHADER_STAGE_COMPUTE_BIT
VK_SHADER_STAGE_FRAGMENT_CONTROL_BIT
VK_SHADER_STAGE_TESSELLATION_EVALUATION_BIT
VK_SHADER_STAGE_GEOMETRY_BIT
VK_SHADER_STAGE_FRAGMENT_BIT
VK_SHADER_STAGE_ALL_GRAPHICS
VK_SHADER_STAGE_ALL
#endif

vkSpecializationInfo.sType = VK_STRUCTURE_TYPE_PIPELINE_SHADER_STAGE_CREATE_INFO;
vkSpecializationInfo.pNext = NULLptr;
vkSpecializationInfo.flags = 0;
vkSpecializationInfo.stage = VK_SHADER_STAGE_FRAGMENT_BIT;
vkSpecializationInfo.module = &fragmentShader;
vkSpecializationInfo.name = "main";
vkSpecializationInfo.specializationInfo = &vkInputBindingDescription[0].binding;
vkSpecializationInfo.bindingCount = 1;

VkVertexInputBindingDescription
    .binding = 0; // an array containing one of these per buffer being used
    .stride = 0; // which brings us this
    .inputRate = VK_VERTEX_INPUT_RATE_VERTEX;
    .format = VK_FORMAT_R32G32B32A32_SFLOAT;
    .inputArrayStride = 0; // bytes between successive
    .arraySize = 1; // vertex

#def CHOICES
VK_VERTEX_INPUT_RATE_VERTEX
VK_VERTEX_INPUT_RATE_INSTANCE
#endif

```

Use one **vpsci** array member per shader module you are using

Use one **vkvibd** array member per vertex input array-of-structures you are using

Link in the Per-Vertex Attributes

```

VkVertexInputAttributeDescription vvid[4] // an array containing one of these per vertex attribute in all bindings
    vvid[4].binding = 0; // v is vertex, normal, color, textureCoord
    vvid[4].location = 0; // location in the layout
    vvid[4].binding = 0; // which binding description this is part of
    vvid[4].format = VK_FORMAT_VEC3; // x, y, z
    vvid[4].offset = offsetof(struct vertex, position); // 0

#ifdef EXTRAS, define AT THE TOP
// these are here for convenience and readability:
#define VK_FORMAT_VEC4 VK_FORMAT_R32G32B32A32_SFLOAT
#define VK_FORMAT_VEC3 VK_FORMAT_R32G32B32_SFLOAT
#define VK_FORMAT_VEC2 VK_FORMAT_R32G32_SFLOAT
#define VK_FORMAT_STP VK_FORMAT_R32G32B32_SFLOAT
#define VK_FORMAT_ST VK_FORMAT_R32G32_SFLOAT
#define VK_FORMAT_SR VK_FORMAT_R32G32_SFLOAT
#define VK_FORMAT_STB VK_FORMAT_R32G32B32_SFLOAT
#define VK_FORMAT_STL VK_FORMAT_R32G32B32A32_SFLOAT
#define VK_FORMAT_SRW VK_FORMAT_R32G32_SFLOAT
#define VK_FORMAT_SRW_A32VK_FORMAT_SRW_A32_SFLOAT
#define VK_FORMAT_SRW_T VK_FORMAT_R32G32_SFLOAT
#define VK_FORMAT_SRW_LT VK_FORMAT_R32G32A32_SFLOAT
#define VK_FORMAT_SRW_LT_A32VK_FORMAT_SRW_LT_A32_SFLOAT
#define VK_FORMAT_X VK_FORMAT_R32_SFLOAT
#endif

vvid[1].location = 1;
vvid[1].binding = 0;
vvid[1].format = VK_FORMAT_VEC3; // nx, ny, nz
vvid[1].offset = offsetof(struct vertex, normal); // 12

vvid[2].location = 2;
vvid[2].binding = 0;
vvid[2].format = VK_FORMAT_VEC3; // r, g, b
vvid[2].offset = offsetof(struct vertex, color); // 24

vvid[3].location = 3;
vvid[3].binding = 0;
vvid[3].format = VK_FORMAT_VEC2; // s, t
vvid[3].offset = offsetof(struct vertex, texCoord); // 36

```

Use one **vvid array member per element in the struct for the array-of-structures element you are using as vertex input**

These are defined at the top of the sample code so that you don't need to use confusing image-looking formats for positions, normals, and tex coords

```

VkPipelineVertexInputStateCreateInfo          vppisci; // used to describe the input vertex attributes
    vkpisci.sType = VK_STRUCTURE_TYPE_PIPELINE_VERTEX_INPUT_STATE_CREATE_INFO;
    vkpisci.pNext = NULLP;
    vkpisci.flags = 0;
    vkpisci.vertexBindingDescriptionsCount = 1;
    vkpisci.vertexBindingDescriptions = vvbld;
    vkpisci.vertexAttributeDescriptionsCount = 4;
    vkpisci.vertexAttributeDescriptions = vad;

VkPipelineInputAssemblyStateCreateInfo        vplasci; // Declare the binding descriptions and attribute descriptions
    vplasci.sType = VK_STRUCTURE_TYPE_PIPELINE_INPUT_ASSEMBLY_STATE_CREATE_INFO;
    vplasci.pNext = NULLP;
    vplasci.flags = 0;
    vplasci.topology = VK_PRIMITIVE_TOPOLOGY_TRIANGLE_LIST;

#endif CHONDRUS

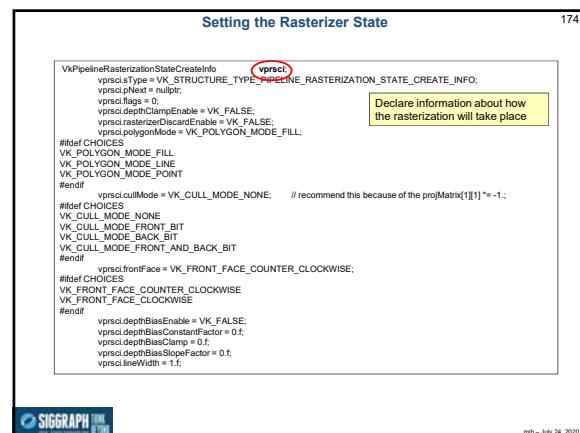
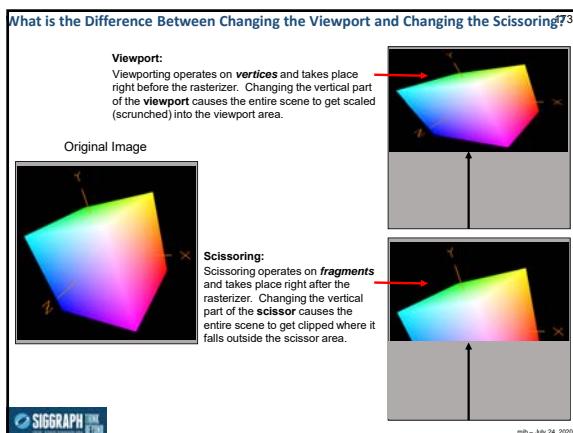
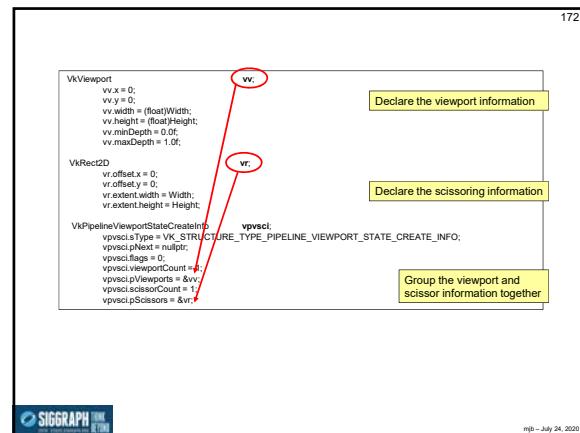
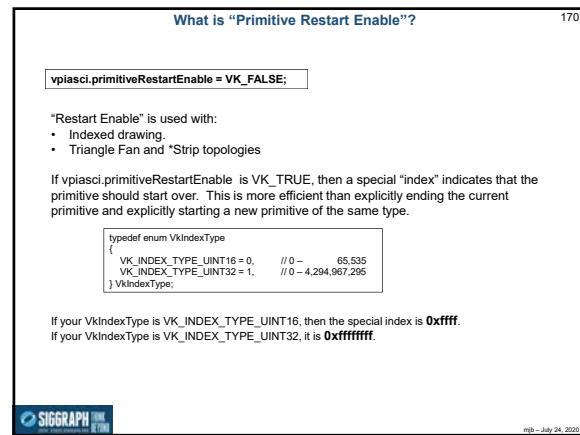
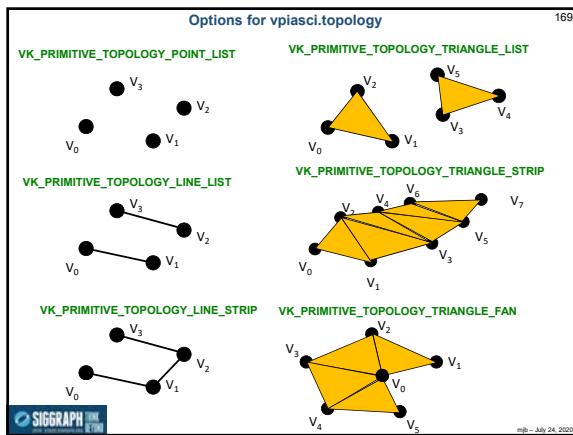
VK_PRIMITIVE_TOPOLOGY_POINT_LIST
VK_PRIMITIVE_TOPOLOGY_LINE_LIST
VK_PRIMITIVE_TOPOLOGY_TRIANGLE_LIST
VK_PRIMITIVE_TOPOLOGY_PATCH_LIST
VK_PRIMITIVE_TOPOLOGY_TRIANGLE_STRIP
VK_PRIMITIVE_TOPOLOGY_TRIANGLE_FAN
VK_PRIMITIVE_TOPOLOGY_LINE_STRIP_WITH_ADJACENCY
VK_PRIMITIVE_TOPOLOGY_PATCH_STRIP_WITH_ADJACENCY
VK_PRIMITIVE_TOPOLOGY_TRIANGLE_LIST_WITH_ADJACENCY
VK_PRIMITIVE_TOPOLOGY_TRIANGLE_STRIP_WITH_ADJACENCY
#endif

vplasci.primitiveRestartEnable = VK_FALSE;

VkPipelineTessellationStateCreateInfo          vptisci; // Tessellation Shader info
    vptisci.sType = VK_STRUCTURE_TYPE_PIPELINE_TESSELLATION_STATE_CREATE_INFO;
    vptisci.pNext = NULLP;
    vptisci.flags = 0;
    vptisci.patchControlPoints = 0; // number of patch control points

VkPipelineGeometryStateCreateInfo              vgisci; // Geometry Shader info
    vgisci.sType = VK_STRUCTURE_TYPE_PIPELINE_TESSELLATION_STATE_CREATE_INFO;
    vgisci.pNext = NULLP;
    vgisci.flags = 0;

```



What is "Depth Clamp Enable"?

```
vprsci.depthClampEnable = VK_FALSE;
```

Depth Clamp Enable causes the fragments that would normally have been discarded because they are closer to the viewer than the near clipping plane to instead get projected to the near clipping plane and displayed.

A good use for this is **Polygon Capping**:

The front of the polygon is clipped, revealing to the viewer that this is really a shell, not a solid

The gray area shows what would happen with depthClampEnable (except it would have been red).

mjb – July 24, 2020

What is "Depth Bias Enable"?

```
vprsci.depthBiasEnable = VK_FALSE;
vprsci.depthBiasConstantFactor = 0.f;
vprsci.depthBiasClamp = 0.f;
vprsci.depthBiasSlopeFactor = 0.f;
```

Depth Bias Enable allows scaling and translation of the Z-depth values as they come through the rasterizer to avoid Z-fighting.

Z-fighting

mjb – July 24, 2020

MultiSampling State

VkPipelineMultisampleStateCreateInfo
vpmsci

```
vpmsci.sType = VK_STRUCTURE_TYPE_PIPELINE_MULTISAMPLE_STATE_CREATE_INFO;
vpmsci.pNext = nullptr;
vpmsci.flags = 0;
vpmsci.rasterizationSamples = VK_SAMPLE_COUNT_1_BIT;
vpmsci.sampleShadingEnable = VK_FALSE;
vpmsci.sampleMask = 1;
vpmsci.alphaToCoverageEnable = VK_FALSE;
vpmsci.alphaToOneEnable = VK_FALSE;
```

Declare information about how the multisampling will take place

We will discuss MultiSampling in a separate noteset.

mjb – July 24, 2020

Color Blending State for each Color Attachment *

Create an array with one of these for each color buffer attachment. Each color buffer attachment can use different blending operations.

VkPipelineColorBlendAttachmentState
vpcbas

```
vpcbas.blendEnable = VK_FALSE;
vpcbas.srcColorBlendFactor = VK_BLEND_FACTOR_SRC_COLOR;
vpcbas.dstColorBlendFactor = VK_BLEND_FACTOR_ONE_MINUS_SRC_COLOR;
vpcbas.colorBlendOp = VK_BLEND_OP_ADD;
vpcbas.srcAlphaBlendFactor = VK_BLEND_FACTOR_ONE;
vpcbas.dstAlphaBlendFactor = VK_BLEND_FACTOR_ZERO;
vpcbas.alphaBlendOp = VK_BLEND_OP_ADD;
vpcbas.colorWriteMask = VK_COLOR_COMPONENT_R_BIT |
VK_COLOR_COMPONENT_G_BIT |
VK_COLOR_COMPONENT_B_BIT |
VK_COLOR_COMPONENT_A_BIT;
```

This controls blending between the output of each color attachment and its image memory.

$Color_{new} = (1-\alpha) * Color_{existing} + \alpha * Color_{incoming}$

$0 \leq \alpha \leq 1$

*A "Color Attachment" is a framebuffer to be rendered into. You can have as many of these as you want.

mjb – July 24, 2020

Raster Operations for each Color Attachment

VkPipelineColorBlendStateCreateInfo
vpcbsci

```
vpcbsci.sType = VK_STRUCTURE_TYPE_PIPELINE_COLOR_BLEND_STATE_CREATE_INFO;
vpcbsci.pNext = nullptr;
vpcbsci.flags = 0;
vpcbsci.logicOpEnable = VK_FALSE;
vpcbsci.logicOp = VK_LOGIC_OP_COPY;
#ifdef CHOICES
VK_LOGIC_OP_CLEAR
VK_LOGIC_OP_AND
VK_LOGIC_OP_AND_REVERSE
VK_LOGIC_OP_OR
VK_LOGIC_OP_OR_INVERTED
VK_LOGIC_OP_XOR
VK_LOGIC_OP_NAND
VK_LOGIC_OP_NOR
VK_LOGIC_OP_EQUIVALENT
VK_LOGIC_OP_EQUIVALENT_INVERTED
VK_LOGIC_OP_OR_INVERSE
VK_LOGIC_OP_COPY_INVERTED
VK_LOGIC_OP_OR_INVERTED
VK_LOGIC_OP_NAND
VK_LOGIC_OP_SET
#endif
vpcbsci.attachmentCount = 1;
vpcbsci.blendAttachments = &vpcbsci;
vpcbsci.blendConstants[0] = 0;
vpcbsci.blendConstants[1] = 0;
vpcbsci.blendConstants[2] = 0;
vpcbsci.blendConstants[3] = 0;
```

This controls blending between the output of the fragment shader and the input to the color attachments.

mjb – July 24, 2020

Which Pipeline Variables can be Set Dynamically

Just used as an example in the Sample Code

VkDynamicState
#ifdef CHOICES
VK_DYNAMIC_STATE_VIEWPORT
VK_DYNAMIC_STATE_LINE_WIDTH
VK_DYNAMIC_STATE_DEPTH_BIAS
VK_DYNAMIC_STATE_BLEND_CONSTANTS
VK_DYNAMIC_STATE_STENCIL_COMPARE_FUNC
VK_DYNAMIC_STATE_STENCIL_WRITE_MASK
VK_DYNAMIC_STATE_STENCIL_REFERENCE

vds[]

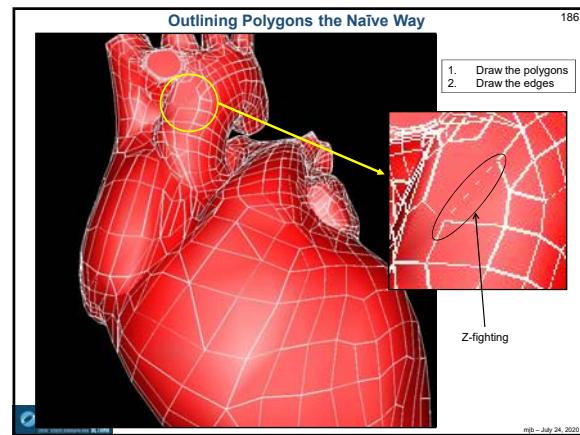
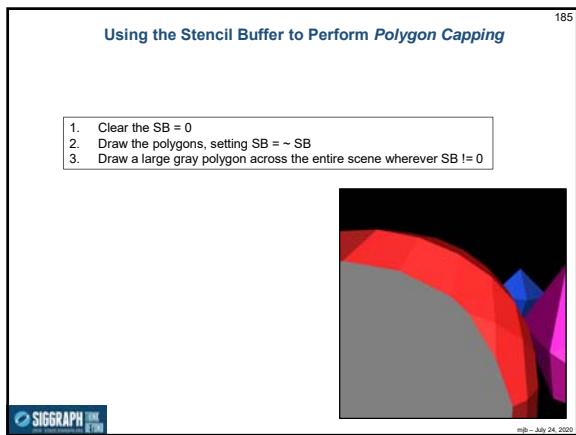
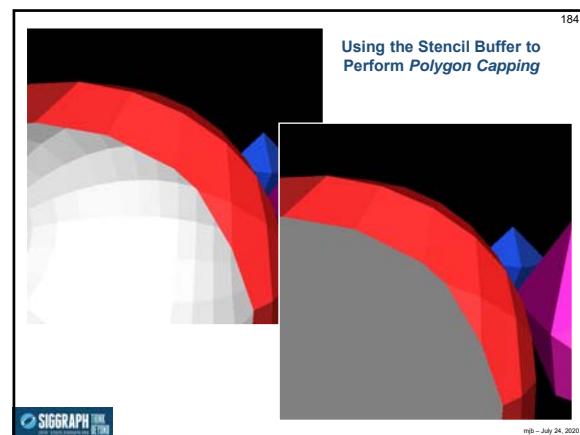
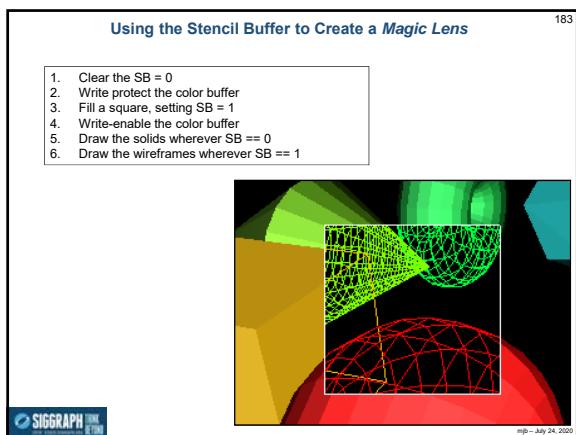
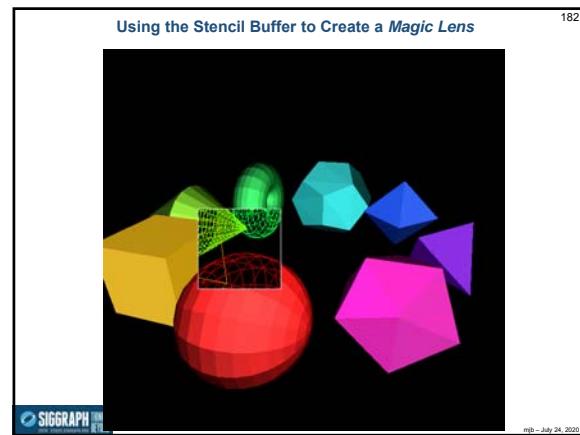
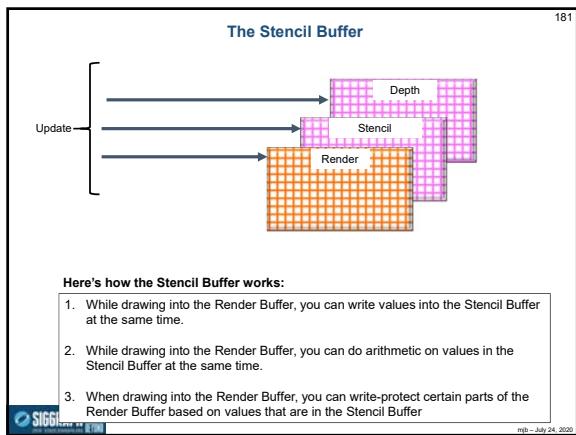
```
vds[i] = VK_DYNAMIC_STATE_VIEWPORT; // leave turned off for now
-- vkCmdSetViewport()
-- vkCmdSetLineWidth()
-- vkCmdSetDepthBias()
-- vkCmdSetBlendConstants()
-- vkCmdSetDepthBounds()
-- vkCmdSetStencilOp()
-- vkCmdSetStencilWriteMask()
-- vkCmdSetStencilReference()
```

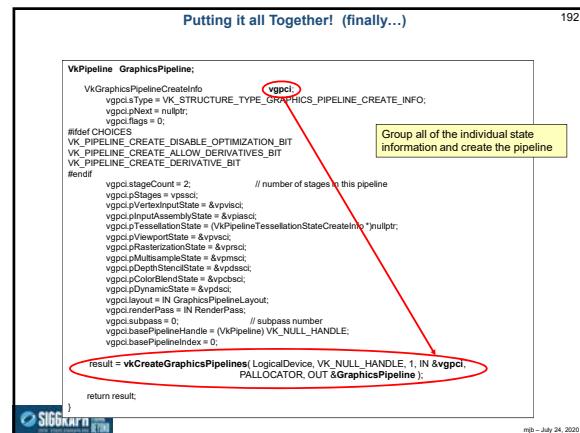
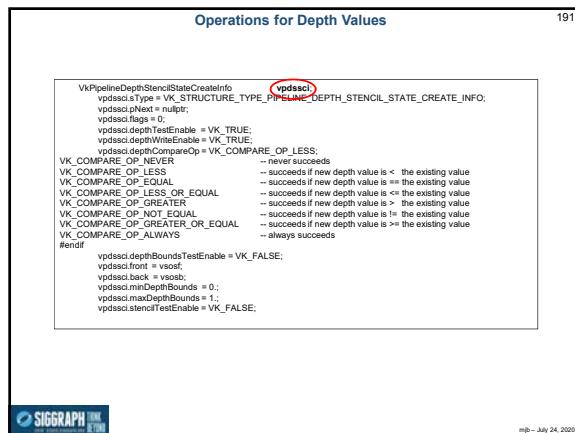
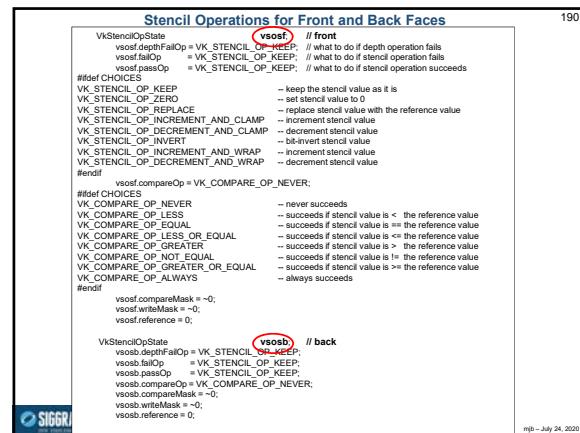
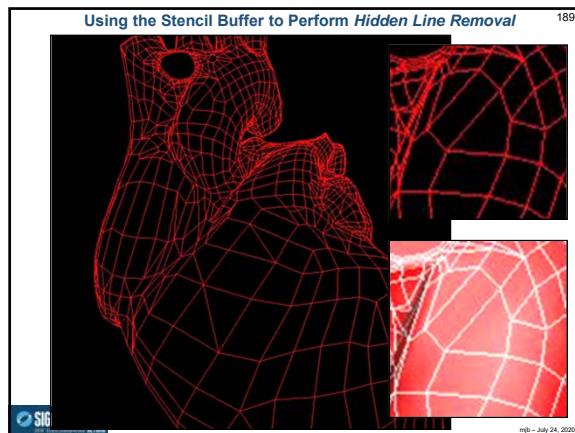
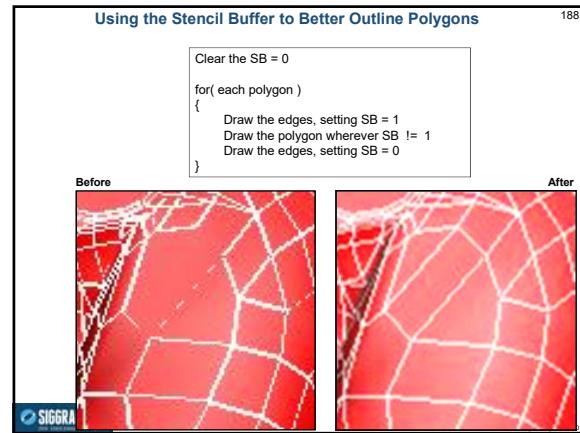
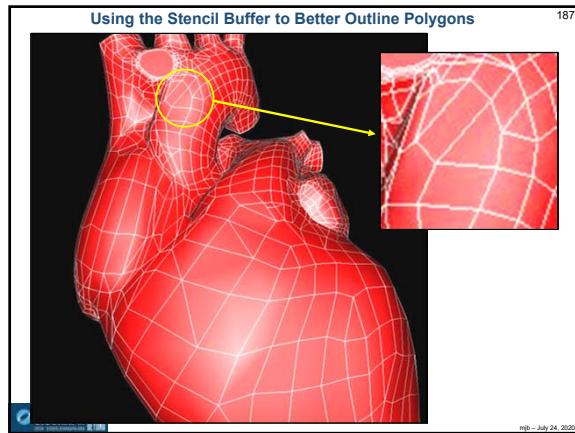
#endif

VkPipelineDynamicStateCreateInfo
vpdsci

```
vpdsci.sType = VK_STRUCTURE_TYPE_PIPELINE_DYNAMIC_STATE_CREATE_INFO;
vpdsci.pNext = nullptr;
vpdsci.flags = 0;
vpdsci.attachmentCount = 0;
vpdsci.pDynamicStates = vds;
```

mjb – July 24, 2020





Later on, we will Bind a Specific Graphics Pipeline Data Structure to the Command Buffer when Drawing 193

```
vkCmdBindPipeline( CommandBuffers[nextImageIndex],
    VK_PIPELINE_BIND_POINT_GRAPHICS, GraphicsPipeline );
```



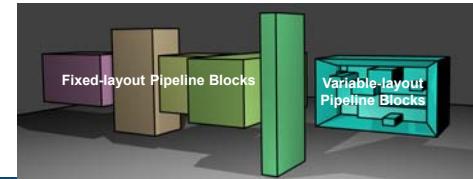
mjb – July 24, 2020

Sidebar: What is the Organization of the Pipeline Data Structure? 194

If you take a close look at the pipeline data structure creation information, you will see that almost all the pieces have a *fixed* size. For example, the viewport only needs 6 pieces of information – ever:

```
VkViewport          vv;
vv.x = 0;
vv.y = 0;
vv.width = (float)Width;
vv.height = (float)Height;
vv.minDepth = 0.0f;
vv.maxDepth = 1.0f;
```

There are two exceptions to this -- the Descriptor Sets and the Push Constants. Each of these two can be almost any size, depending on what you allocate for them. So, I think of the Pipeline Data Structure as consisting of some fixed-layout blocks and 2 variable-layout blocks, like this:




mjb – July 24, 2020

Vulkan.

Descriptor Sets

Mike Bailey
mjb@cs.oregonstate.edu



This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>



mjb – July 24, 2020

In OpenGL 196

OpenGL puts all uniform data in the same “set”, but with different binding numbers, so you can get at each one.

Each uniform variable gets updated one-at-a-time.

Wouldn’t it be nice if we could update a collection of related uniform variables all at once, without having to update the uniform variables that are not related to this collection?

```
layout( std140, binding = 0 ) uniform mat4      uModelMatrix;
layout( std140, binding = 1 ) uniform mat4      uViewMatrix;
layout( std140, binding = 2 ) uniform mat4      uProjectionMatrix;
layout( std140, binding = 3 ) uniform mat3      uNormalMatrix;
layout( std140, binding = 4 ) uniform vec4     uLightPos;
layout( std140, binding = 5 ) uniform float    uTime;
layout( std140, binding = 6 ) uniform int      uMode;
layout(                                binding = 7 ) uniform sampler2D uSampler;
```



mjb – July 24, 2020

What are Descriptor Sets? 197

Descriptor Sets are an intermediate data structure that tells shaders how to connect information held in GPU memory to groups of related uniform variables and texture sampler declarations in shaders. There are three advantages in doing things this way:

- Related uniform variables can be updated as a group, gaining efficiency.
- Descriptor Sets are activated when the Command Buffer is filled. Different values for the uniform buffer variables can be toggled by just swapping out the Descriptor Set that points to GPU memory, rather than re-writing the GPU memory.
- Values for the shaders’ uniform buffer variables can be compartmentalized into what quantities change often and what change seldom (scene-level, model-level, draw-level), so that uniform variables need to be re-written no more often than is necessary.

```
for( each scene )
{
    Bind Descriptor Set #0
    for( each object )
    {
        Bind Descriptor Set #
        for( each draw )
        {
            Bind Descriptor Set #2
            Do the drawing
        }
    }
}
```



mjb – July 24, 2020

Descriptor Sets 198

Our example will assume the following shader uniform variables:

```
// non-opaque must be in a uniform block:
layout( std140, set = 0, binding = 0 ) uniform matBuf
{
    mat4 uModelMatrix;
    mat4 uViewMatrix;
    mat4 uProjectionMatrix;
    mat3 uNormalMatrix;
} Matrices;

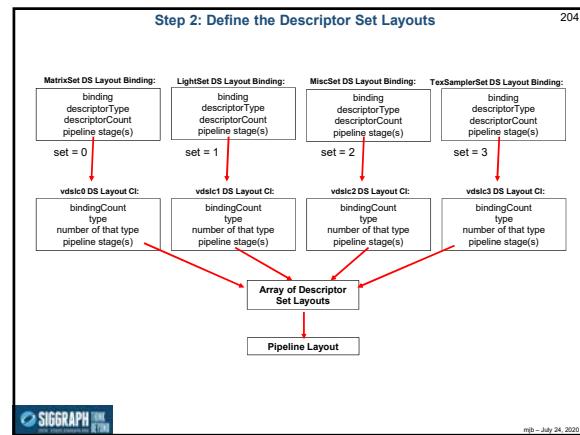
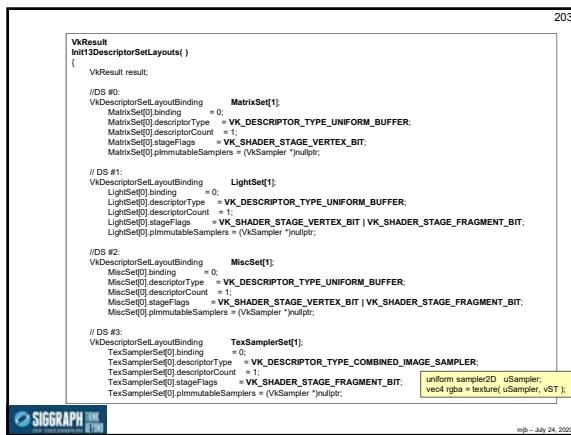
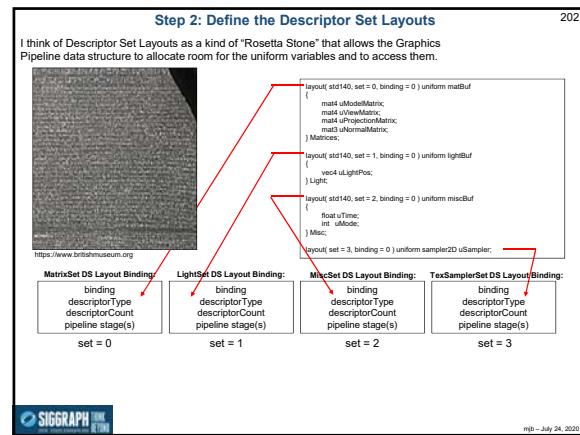
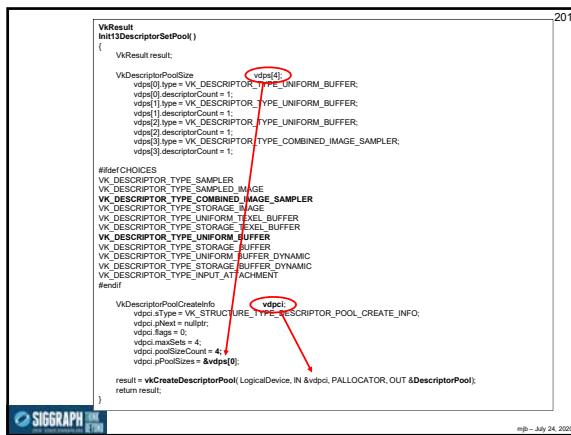
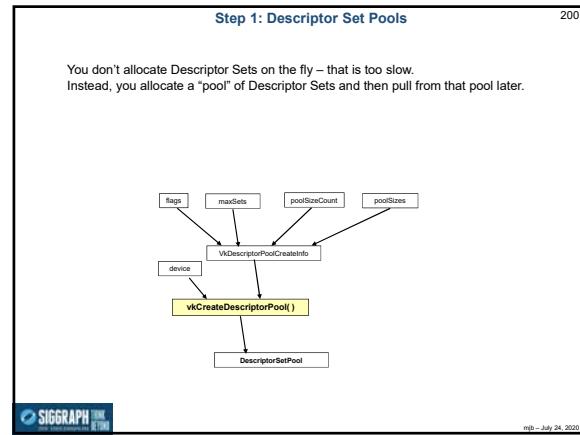
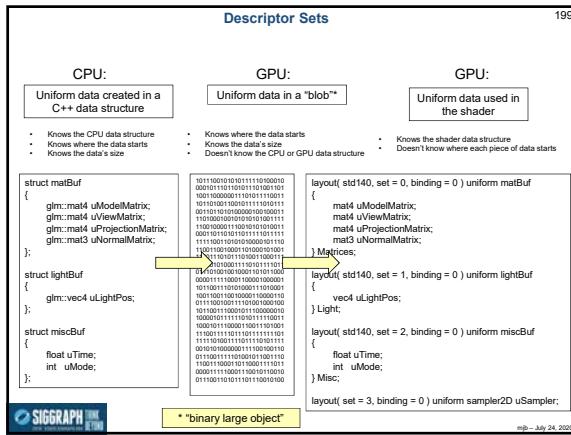
layout( std140, set = 1, binding = 0 ) uniform lightBuf
{
    vec4 uLightPos;
} Light;

layout( std140, set = 2, binding = 0 ) uniform miscBuf
{
    float uTime;
    int uMode;
} Misc;

layout( set = 3, binding = 0 ) uniform sampler2D uSampler;
```



mjb – July 24, 2020



```

205
VkDescriptorSetLayoutCreateInfo vdsi0;
vdsi0.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_LAYOUT_CREATE_INFO;
vdsi0.pNext = nullptr;
vdsi0.pNext = nullptr;
vdsi0.bindingCount = 1;
vdsi0.pBindings = &matrixSet[0];

VkDescriptorSetLayoutCreateInfo vdsi1;
vdsi1.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_LAYOUT_CREATE_INFO;
vdsi1.pNext = nullptr;
vdsi1.flags = 0;
vdsi1.bindingCount = 1;
vdsi1.pBindings = &lightSet[0];

VkDescriptorSetLayoutCreateInfo vdsi2;
vdsi2.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_LAYOUT_CREATE_INFO;
vdsi2.pNext = nullptr;
vdsi2.flags = 0;
vdsi2.bindingCount = 1;
vdsi2.pBindings = &MiscSet[0];

VkDescriptorSetLayoutCreateInfo vdsi3;
vdsi3.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_LAYOUT_CREATE_INFO;
vdsi3.pNext = nullptr;
vdsi3.flags = 0;
vdsi3.bindingCount = 1;
vdsi3.pBindings = &texSamplerSet[0];

result = vkCreateDescriptorSetLayout( LogicalDevice, IN &vdsi0, PALLOCATOR, OUT &DescriptorSetLayouts[0] );
result = vkCreateDescriptorSetLayout( LogicalDevice, IN &vdsi1, PALLOCATOR, OUT &DescriptorSetLayouts[1] );
result = vkCreateDescriptorSetLayout( LogicalDevice, IN &vdsi2, PALLOCATOR, OUT &DescriptorSetLayouts[2] );
result = vkCreateDescriptorSetLayout( LogicalDevice, IN &vdsi3, PALLOCATOR, OUT &DescriptorSetLayouts[3] );

return result;
}

```

 mjb - July 24, 2020

Step 3: Include the Descriptor Set Layouts in a Graphics Pipeline Layout

206

```

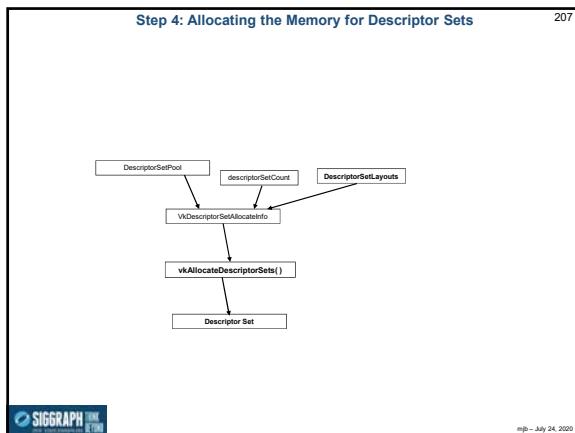
VkResult Init14GraphicsPipelineLayout()
{
    VkPipelineLayoutCreateInfo vplci;
    vplci.sType = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
    vplci.pNext = nullptr;
    vplci.flags = 0;
    vplci.setLayoutCount = 4;
    vplci.pSetLayouts = &DescriptorSetLayouts[0];
    vplci.pushConstantRangeCount = 0;
    vplci.pPushConstantRanges = (VkPushConstantRange *)nullptr;

    result = vkCreatePipelineLayout( LogicalDevice, IN &vplci, PALLOCATOR, OUT &GraphicsPipelineLayout );
    return result;
}



```

mjb - July 24, 2020



Step 4: Allocating the Memory for Descriptor Sets

208

```

VkResult Init13DescriptorSets()
{
    VkResult result;

    VkDescriptorSetAllocateInfo vdsai;
    vdsai.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_ALLOCATE_INFO;
    vdsai.pNext = nullptr;
    vdsai.descriptorPool = DescriptorPool;
    vdsai.descriptorSetCount = 4;
    vdsai.pSetLayouts = DescriptorSetLayouts;

    result = vkAllocateDescriptorSets( LogicalDevice, IN &vdsai, OUT &DescriptorSets[0] );
}



```

mjb - July 24, 2020

Step 5: Tell the Descriptor Sets where their CPU Data is

209

```

VkDescriptorBufferInfo vdbi0;
vdbi0.buffer = MyMatrixUniformBuffer.buffer;
vdbi0.offset = 0;
vdbi0.range = sizeof(Matrices);

VkDescriptorBufferInfo vdbi1;
vdbi1.buffer = MyLightUniformBuffer.buffer;
vdbi1.offset = 0;
vdbi1.range = sizeof(Light);

VkDescriptorBufferInfo vdbi2;
vdbi2.buffer = MyMiscUniformBuffer.buffer;
vdbi2.offset = 0;
vdbi2.range = sizeof(Misc);

VkDescriptorImageInfo vdi0;
vdi0.sampler = MyPuppyTexture.texSampler;
vdi0.imageView = MyPuppyTexture.textureView;
vdi0.imageLayout = VK_IMAGE_LAYOUT_SHADER_READ_ONLY_OPTIMAL;

This struct identifies what buffer it owns and how big it is
This struct identifies what buffer it owns and how big it is
This struct identifies what texture sampler and image view it owns

```

 mjb - July 24, 2020

Step 5: Tell the Descriptor Sets where their CPU Data is

210

```

VkWriteDescriptorSet vwds0;
// ds 0:
vwds0.sType = VK_STRUCTURE_TYPE_WRITE_DESCRIPTOR_SET;
vwds0.pNext = nullptr;
vwds0.dsSet = DescriptorSets[0];
vwds0.dsBinding = 0;
vwds0.dsArrayElement = 0;
vwds0.descriptorCount = 1;
vwds0.descriptorType = VK_DESCRIPTOR_TYPE_UNIFORM_BUFFER;
vwds0.pBufferInfo = IN &vdbi0;
vwds0.pImageInfo = (VkDescriptorImageInfo *)nullptr;
vwds0.pTexelBufferView = (VkbBufferView *)nullptr;

This struct links a Descriptor Set to the buffer it is pointing to

VkwDescriptorSet vwds1;
vwds1.sType = VK_STRUCTURE_TYPE_WRITE_DESCRIPTOR_SET;
vwds1.pNext = nullptr;
vwds1.dsSet = DescriptorSets[1];
vwds1.dsBinding = 0;
vwds1.dsArrayElement = 0;
vwds1.descriptorCount = 1;
vwds1.descriptorType = VK_DESCRIPTOR_TYPE_UNIFORM_BUFFER;
vwds1.pBufferInfo = IN &vdbi1;
vwds1.pImageInfo = (VkDescriptorImageInfo *)nullptr;
vwds1.pTexelBufferView = (VkbBufferView *)nullptr;

This struct links a Descriptor Set to the buffer it is pointing to



```

mjb - July 24, 2020

Step 5: Tell the Descriptor Sets where their data is

```

VkWriteDescriptorSet    wvds2;           This struct links a Descriptor Set to the buffer it is pointing to
// ds 2
wvds2.sType = VK_STRUCTURE_TYPE_WRITE_DESCRIPTOR_SET;
wvds2.pNext = nullptr;
wvds2.descriptorSet = DescriptorSets[2];
wvds2.descriptorType = VK_DESCRIPTOR_TYPE_UNIFORM_BUFFER;
wvds2.descriptorCount = 1;
wvds2.pBufferInfo = IN &vds2;
wvds2.pImageInfo = (VkDescriptorImageInfo *)nullptr;
wvds2.pTexelBufferView = (VkBufferView *)nullptr;

// ds 3
VkWriteDescriptorSet    wvds3;           This struct links a Descriptor Set to the image it is pointing to
wvds3.sType = VK_STRUCTURE_TYPE_WRITE_DESCRIPTOR_SET;
wvds3.pNext = nullptr;
wvds3.descriptorSet = DescriptorSets[3];
wvds3.descriptorType = VK_DESCRIPTOR_TYPE_COMBINED_IMAGE_SAMPLER;
wvds3.descriptorCount = 1;
wvds3.pBufferInfo = (VkDescriptorBufferInfo *)nullptr;
wvds3.pImageInfo = IN &vds3;
wvds3.pTexelBufferView = (VkBufferView *)nullptr;

uint32_t copyCount = 0;
// this could have been done with one call and an array of VkWriteDescriptorSets:
vkUpdateDescriptorSets( LogicalDevice, 1, &wvds0, IN copyCount, (VkCopyDescriptorSet *)nullptr );
vkUpdateDescriptorSets( LogicalDevice, 1, &wvds1, IN copyCount, (VkCopyDescriptorSet *)nullptr );
vkUpdateDescriptorSets( LogicalDevice, 1, &wvds2, IN copyCount, (VkCopyDescriptorSet *)nullptr );
vkUpdateDescriptorSets( LogicalDevice, 1, &wvds3, IN copyCount, (VkCopyDescriptorSet *)nullptr );

```

mjb – July 24, 2020

Step 6: Include the Descriptor Set Layout when Creating a Graphics Pipeline

```

VkGraphicsPipelineCreateInfo    vpcl;           vpcl is highlighted in red
vpcl.sType = VK_STRUCTURE_TYPE_GRAPHICS_PIPELINE_CREATE_INFO;
vpcl.pNext = nullptr;
vpcl.flags = 0;
#define CHICKEN_V_PIPELINE_CREATE_DISABLE_OPTIMIZATION_BIT
#define CHICKEN_V_PIPELINE_CREATE_ALLOW_DERIVATIVES_BIT
#define CHICKEN_V_PIPELINE_CREATE_DERIVATIVE_BIT
#endif
vpcl.stageCount = 2;                         // number of stages in this pipeline
vpcl.pStages = vpsc;
vpcl.pVertexInputState = &pvisci;
vpcl.pInputAssemblyState = &piasci;
vpcl.pViewportState = (VkPipelineTessellationStateCreateInfo *)nullptr;
vpcl.pScissorState = &pscissci;
vpcl.pRasterizationState = &prasci;
vpcl.pMultisampleState = &pmssci;
vpcl.pDepthStencilState = &pdssci;
vpcl.pColorBlendState = &pcbsci;
vpcl.pDynamicState = &pdsc;
vpcl.layout = GraphicsPipelineLayout;           GraphicsPipelineLayout is highlighted in red
vpcl.renderPass = IN RenderPass;
vpcl.subpass = 0;                            // subpass number
vpcl.basePipelineHandle = (VkPipeline)VK_NULL_HANDLE;
vpcl.basePipelineIndex = 0;
result = vkCreateGraphicsPipelines( LogicalDevice, VK_NULL_HANDLE, 1, IN &vpcl,
                                  PALLOCATOR, OUT &GraphicsPipeline );

```

mjb – July 24, 2020

Step 7: Bind Descriptor Sets into the Command Buffer when Drawing

Diagram illustrating the binding process:

```

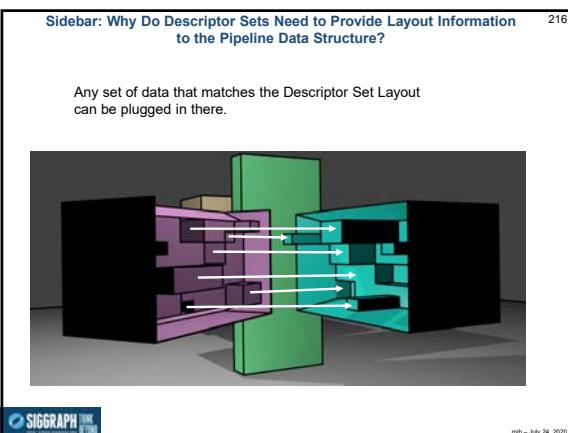
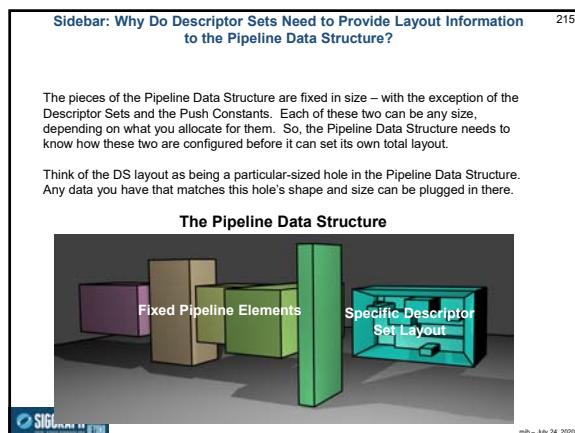
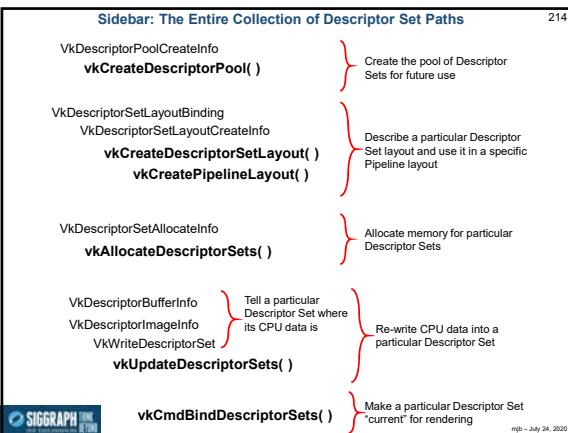
graph TD
    cmdBuffer --> vkCmdBindDescriptorSets()
    vkCmdBindDescriptorSets() --> descriptorSets
    vkCmdBindDescriptorSets() --> descriptorSetCount
    vkCmdBindDescriptorSets() --> graphicsPipelineLayout
    vkCmdBindDescriptorSets() --> pipelineBindPoint
    descriptorSets --> descriptorSet

```

`vkCmdBindDescriptorSets(CommandBuffers[nextImageIndex],
VK_PIPELINE_BIND_POINT_GRAPHICS, GraphicsPipelineLayout,
0, 4, DescriptorSets, 0, (uint32_t *)nullptr);`

So, the Pipeline Layout contains the **structure** of the Descriptor Sets.
Any collection of Descriptor Sets that match that structure can be bound into that pipeline.

mjb – July 24, 2020



Vulkan.

Textures

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

SIGGRAPH

mjb – July 24, 2020

The Basic Idea

Texture mapping is a computer graphics operation in which a separate image, referred to as the **texture**, is stretched onto a piece of 3D geometry and follows it however it is transformed. This image is also known as a **texture map**.

Also, to prevent confusion, the texture pixels are not called **pixels**. A pixel is a dot in the final screen image. A dot in the texture image is called a **texture element**, or **texel**.

Similarly, to avoid terminology confusion, a texture's width and height dimensions are not called **X** and **Y**. They are called **S** and **T**. A texture map is not generally indexed by its actual resolution coordinates. Instead, it is indexed by a coordinate system that is resolution-independent. The left side is always **S=0**, the right side is **S=1**, the bottom is **T=0**, and the top is **T=1**. Thus, you do not need to be aware of the texture's resolution when you are specifying coordinates that point into it. Think of **S** and **T** as a measure of what fraction of the way you are into the texture.

T=1.
S=0.
S=1.
T=0.

SIGGRAPH

mjb – July 24, 2020

The Basic Idea

The mapping between the geometry of the 3D object and the S and T of the texture image works like this:

(0.78, 0.67) in S and T = (199.68, 171.52) in texels

SIGGRAPH

mjb – July 24, 2020

In OpenGL terms: assigning an (s,t) to each vertex

Enable texture mapping:
`glEnable(GL_TEXTURE_2D);`

Draw your polygons, specifying s and t at each vertex:

```
glBegin(GL_POLYGON);
    glTexCoord2f(s0, t0);
    glNormal3f(nx0, ny0, nz0);
    glVertex3f(x0, y0, z0);

    glTexCoord2f(s1, t1);
    glNormal3f(nx1, ny1, nz1);
    glVertex3f(x1, y1, z1);

    ...

```

Disable texture mapping:
`glDisable(GL_TEXTURE_2D);`

SIGGRAPH

mjb – July 24, 2020

Triangles in an Array of Structures

```
struct vertex
{
    glm::vec3 position;
    glm::vec3 normal;
    glm::vec2 texCoord;
};

struct vertex VertexData[ ] =
{
    // triangle 0-2-3:
    // vertex #0:
    {
        { -1.0, -1.0, -1.0 },
        { 0.0, 0.0, -1.0 },
        { 0.0, 0.0, 0.0 },
        { 1.0, 0.0, 0.0 },
    },
    // vertex #2:
    {
        { -1.0, 1.0, -1.0 },
        { 0.0, 0.0, -1.0 },
        { 0.0, 1.0, 0.0 },
        { 1.0, 1.0, 0.0 },
    },
    // vertex #3:
    {
        { 1.0, 1.0, -1.0 },
        { 0.0, 0.0, -1.0 },
        { 1.0, 0.0, 0.0 },
        { 0.0, 1.0, 0.0 },
    },
}
```

SIGGRAPH

mjb – July 24, 2020

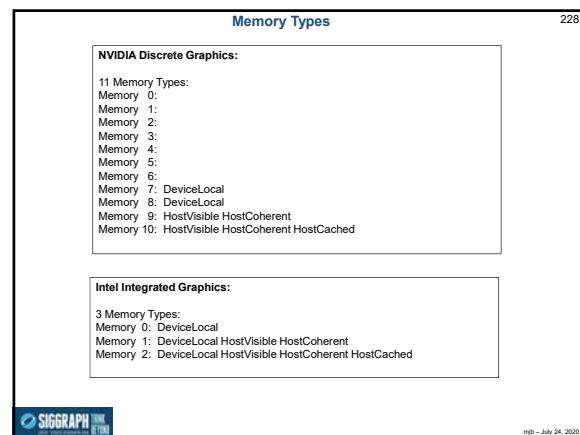
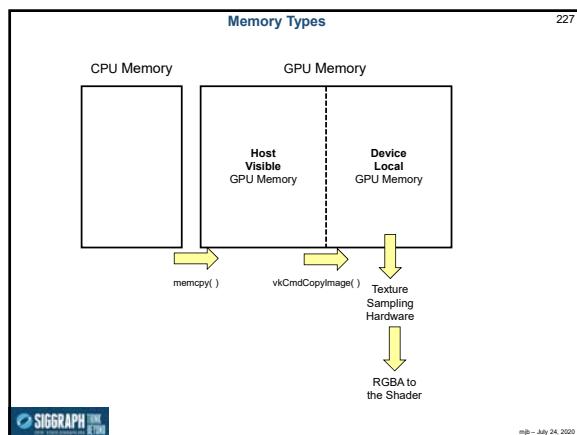
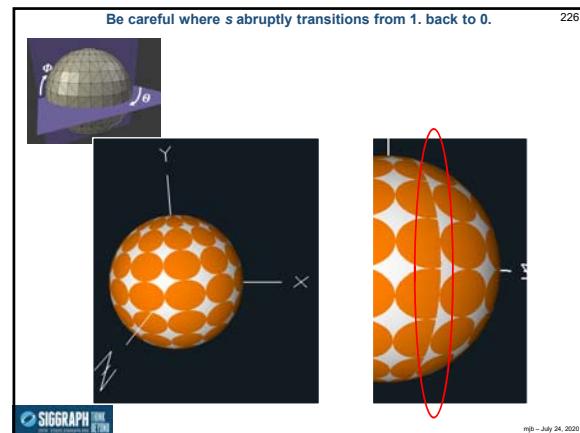
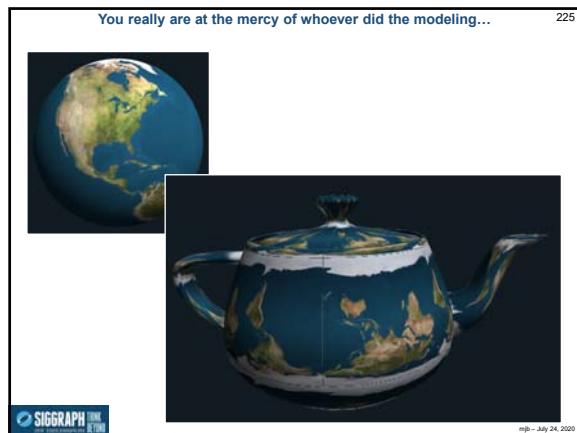
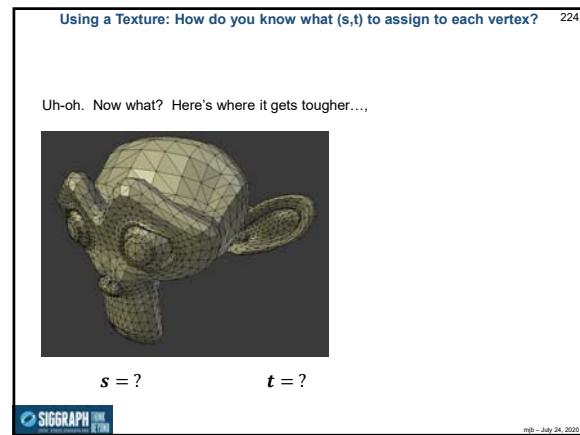
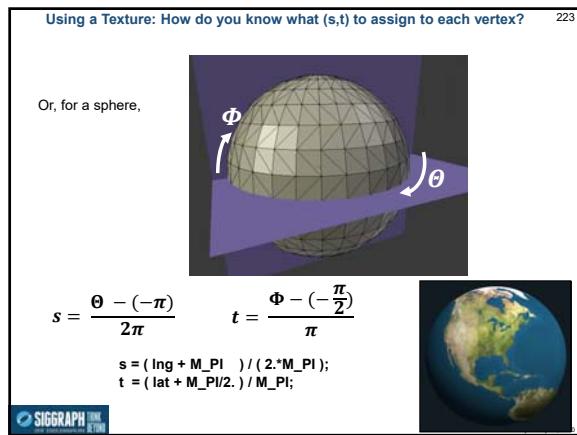
Using a Texture: How do you know what (s,t) to assign to each vertex?

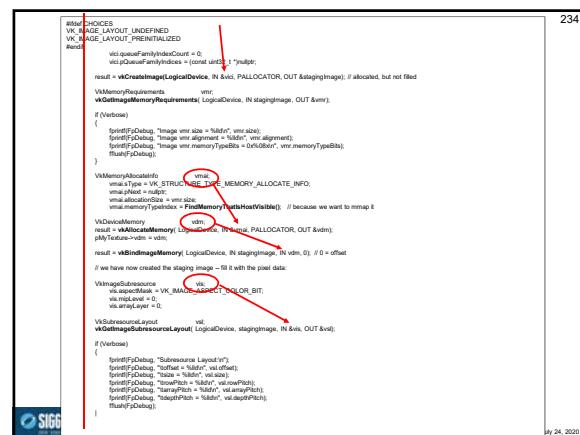
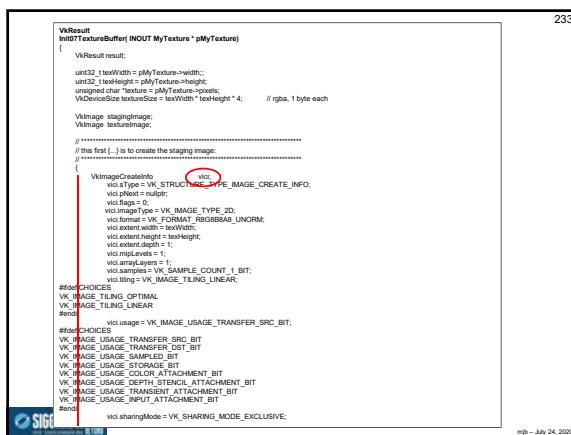
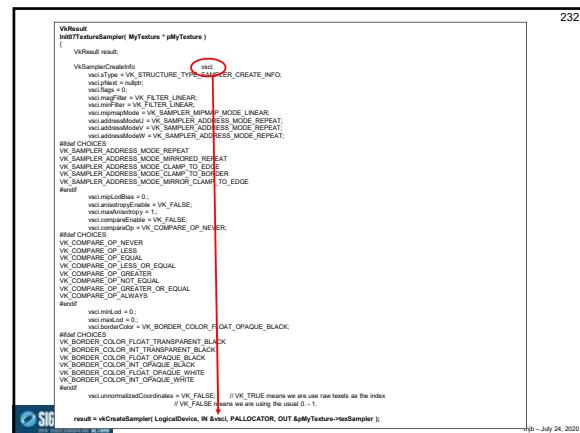
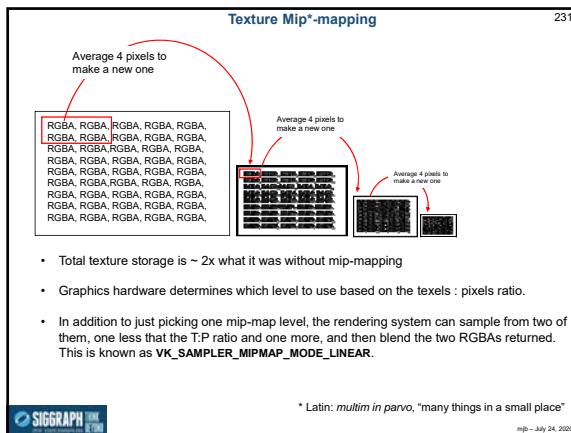
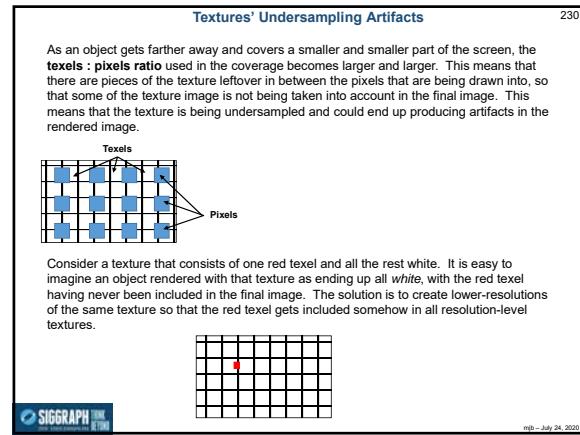
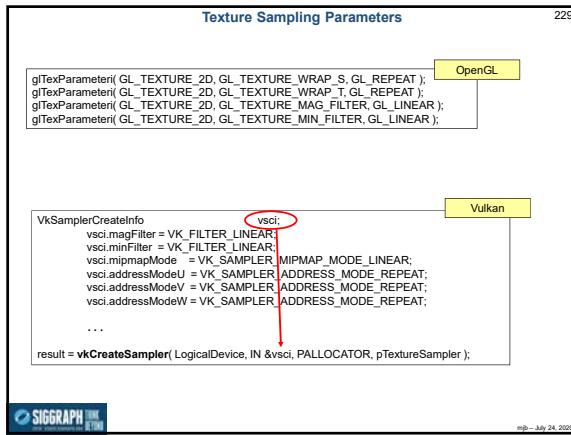
The easiest way to figure out what s and t are at a particular vertex is to figure out what fraction across the object the vertex is living at. For a plane,

$$s = \frac{x - X_{min}}{X_{max} - X_{min}} \quad t = \frac{y - Y_{min}}{Y_{max} - Y_{min}}$$

SIGGRAPH

mjb – July 24, 2020





```

void * gpuMemory;
vkMapMemory LogicalDevice, vdm, 0, VK_WHOLE_SIZE, 0, OUT &gpuMemory;
// I/O and 0 = offset and memory map flags

if (vd.rowPitch == 4 * texWidth)
{
    memcpy(gpuMemory, (void *)Texture, (size_t)textureSize);
}
else
{
    unsigned char * gpuBytes = (unsigned char *)gpuMemory;
    for (unsigned int y = 0; y < texHeight; y++)
    {
        memcpy(gpuBytes + y * vd.rowPitch), &texture[4 * y * texWidth], (size_t)(4 * texWidth));
    }
}

vkUnmapMemory LogicalDevice, vdm;

```

mjb – July 24, 2020

```

// this second (...) is to create the actual texture image:
// -----
// VImageCreateInfo
// vci.sType = VK_STRUCTURE_TYPE_IMAGE_CREATE_INFO;
// vci.pNext = nullptr;
// vci.imageType = VK_IMAGE_TYPE_2D;
// vci.format = VK_FORMAT_B8G8R8_UNORM;
// vci.extent.width = texWidth;
// vci.extent.height = texHeight;
// vci.extent.depth = 1;
// vci.mipLevels = 1;
// vci.arrayLayers = 1;
// vci.samples = VK_SAMPLE_COUNT_1_BIT;
// vci.tiling = VK_IMAGE_TILING_OPTIMAL;
// vci.usage = VK_IMAGE_USAGE_TRANSFER_DST_BIT | VK_IMAGE_USAGE_SAMPLED_BIT;
// because we are using it as an external sample from it
// vci.initialLayout = VK_IMAGE_LAYOUT_UNDEFINED;
// vci.flags = VK_IMAGE_CREATE_EXTERNAL_SAMPLED_BIT;
// vci.queueFamilyIndices = const uint8_t *pulptr;

result = vkCreateImageLogicalDevice, IN &vci, PALLOCATOR, OUT &textureImage); // allocated, but not filled

VMemoryRequirements vmr;
vkGetImageMemoryRequirements LogicalDevice, IN textureImage, OUT &vmr;

if (Verbose)
{
    fprintf(FpDebug, "Texture memory info:\n");
    fprintf(FpDebug, "  texture vci memoryTypeBits = 0x%08X, vm memoryTypeBits: %d\n");
    fprintf(FpDebug, "  texture vci alignment = %d, vm alignment : %d\n");
    fprintf(FpDebug, "  texture vci tiling = %d, vm tiling : %d\n");
    fprintf(FpDebug, "  texture vci usage = %d, vm usage : %d\n");
    fprintf(FpDebug, "  texture vci format = %d, vm format : %d\n");
    fprintf(FpDebug, "  texture vci samples = %d, vm samples : %d\n");
    fprintf(FpDebug, "  texture vci mipLevels = %d, vm mipLevels : %d\n");
    fprintf(FpDebug, "  texture vci arrayLayers = %d, vm arrayLayers : %d\n");
    fprintf(FpDebug, "  texture vci depthStencilFormat = %d, vm depthStencilFormat : %d\n");
    fprintf(FpDebug, "  texture vci queueFamilyIndex = %d, vm queueFamilyIndex : %d\n");
    fprintf(FpDebug, "  texture vci allocationSize = %d, vm allocationSize : %d\n");
}

VMemoryAllocateInfo vma;
vma.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
vma.pNext = nullptr;
vma.allocationSize = pulptr;
vma.memoryTypeIndex = FindMemoryAllocDeviceLocal; // if because we want to sample from it
VDeviceMemory vmem;
result = vkAllocateMemory LogicalDevice, IN &vmr, PALLOCATOR, OUT &vmem;
result = vkBindImageMemory LogicalDevice, IN textureImage, IN vdm, 0); // 0 = offset


```

mjb – July 24, 2020

```

// copy pixels from the staging image to the texture:
VCommandBufferBeginInfo vcbi;
vcbi.sType = VK_STRUCTURE_TYPE_COMMAND_BUFFER_BEGIN_INFO;
vcbi.pNext = nullptr;
vcbi.flags = VK_COMMAND_BUFFER_USAGE_ONE_TIME_SUBMIT_BIT;
vcbi.pInheritanceInfo = &(VCommandBufferInheritance);
vcbi.renderPass = nullptr;
result = vkBeginCommandBuffer TextureCommandBuffer, IN &vcbi;

// transition the staging buffer layout:
// -----
VImageSubresourceRange vir;
vir.aspectMask = VK_IMAGE_ASPECT_COLOR_BIT;
vir.baseMipLevel = 0;
vir.levelCount = 1;
vir.baseArrayLayer = 0;
vir.arrayLayerCount = 1;

VImageMemoryBarrier vimb;
vimb.sType = VK_STRUCTURE_TYPE_IMAGE_MEMORY_BARRIER;
vimb.pNext = nullptr;
vimb.oldLayout = VK_IMAGE_LAYOUT_PREINITIALIZED;
vimb.newLayout = VK_IMAGE_LAYOUT_TRANSFER_SRC_OPTIMAL;
vimb.srcQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.dstQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.image = stagingImage;
vimb.subresourceRange = vir;
vimb.accessFlags = VK_ACCESS_HOST_WRITE_BIT;
vimb.subresourceRange = vir;

vkCmdPipelineBarrier TextureCommandBuffer,
VK_PIPELINE_STAGE_HOST_BIT, VK_PIPELINE_STAGE_HOST_BIT,
0, (VMemoryBarrier *pulptr,
0, (VImageMemoryBarrier *pulptr,
1, IN &vimb);


```

mjb – July 24, 2020

```

// transition the texture buffer layout:
// -----
VImageSubresourceRange vir;
vir.aspectMask = VK_IMAGE_ASPECT_COLOR_BIT;
vir.baseMipLevel = 0;
vir.levelCount = 1;
vir.baseArrayLayer = 0;
vir.arrayLayerCount = 1;

VImageMemoryBarrier vimb;
vimb.sType = VK_STRUCTURE_TYPE_IMAGE_MEMORY_BARRIER;
vimb.pNext = nullptr;
vimb.oldLayout = VK_IMAGE_LAYOUT_TRANSFER_DST_OPTIMAL;
vimb.newLayout = VK_IMAGE_LAYOUT_TRANSFER_DST_OPTIMAL;
vimb.srcQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.dstQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.image = textureImage;
vimb.subresourceRange = vir;
vimb.accessFlags = VK_ACCESS_TRANSFER_WRITE_BIT;

vkCmdPipelineBarrier TextureCommandBuffer,
VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT, VK_PIPELINE_STAGE_TRANSFER_BIT, 0,
0, (VMemoryBarrier *pulptr,
0, (VImageMemoryBarrier *pulptr,
1, IN &vimb);

// now do the final image transfer:
VImageSubresourceLayers vis;
vis.sType = VK_STRUCTURE_TYPE_IMAGE_ASPECT_COLOR_BIT;
vis.pNext = nullptr;
vis.baseMipLevel = 0;
vis.levelCount = 1;
vis.baseArrayLayer = 0;
vis.arrayLayerCount = 1;

VOffset3D vo3;
vo3.x = 0;
vo3.y = 0;
vo3.z = 0;

VEvent3D ve3;
ve3.width = texWidth;
ve3.height = texHeight;
ve3.depth = 1;

VImageCopy vic;
vic.sType = VK_STRUCTURE_TYPE_IMAGE_COPY;
vic.pNext = nullptr;
vic.srcOffset = vo3;
vic.dstOffset = vis;
vic.extent = ve3;

vkCmdCopyImage(TextureCommandBuffer,
stagingImage, VK_PIPELINE_STAGE_TRANSFER_SRC_OPTIMAL,
textureImage, VK_IMAGE_LAYOUT_TRANSFER_DST_OPTIMAL, 1, IN &vic);


```

mjb – July 24, 2020

```

VImageCopy vic;
vic.sType = VK_STRUCTURE_TYPE_IMAGE_COPY;
vic.pNext = nullptr;
vic.srcOffset = vo3;
vic.dstOffset = vis;
vic.extent = ve3;
vkCmdCopyImage(TextureCommandBuffer,
stagingImage, VK_PIPELINE_STAGE_TRANSFER_SRC_OPTIMAL,
textureImage, VK_IMAGE_LAYOUT_TRANSFER_DST_OPTIMAL, 1, IN &vic);

```

mjb – July 24, 2020

```

// transition the texture buffer layout a second time:
// -----
VImageSubresourceRange vir;
vir.aspectMask = VK_IMAGE_ASPECT_COLOR_BIT;
vir.baseMipLevel = 0;
vir.levelCount = 1;
vir.baseArrayLayer = 0;
vir.arrayLayerCount = 1;

VImageMemoryBarrier vimb;
vimb.sType = VK_STRUCTURE_TYPE_IMAGE_MEMORY_BARRIER;
vimb.pNext = nullptr;
vimb.oldLayout = VK_IMAGE_LAYOUT_TRANSFER_DST_OPTIMAL;
vimb.newLayout = VK_IMAGE_LAYOUT_FRAGMENT_SHADER_READ_ONLY_OPTIMAL;
vimb.srcQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.dstQueueFamilyIndex = VK_QUEUE_FAMILY_IGNORED;
vimb.image = textureImage;
vimb.subresourceRange = vir;
vimb.accessFlags = 0;
vimb.subresourceRange = vir;

vkCmdPipelineBarrier TextureCommandBuffer,
VK_PIPELINE_STAGE_TRANSFER_BIT, VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT, 0,
0, (VMemoryBarrier *pulptr,
0, (VImageMemoryBarrier *pulptr,
1, IN &vimb);

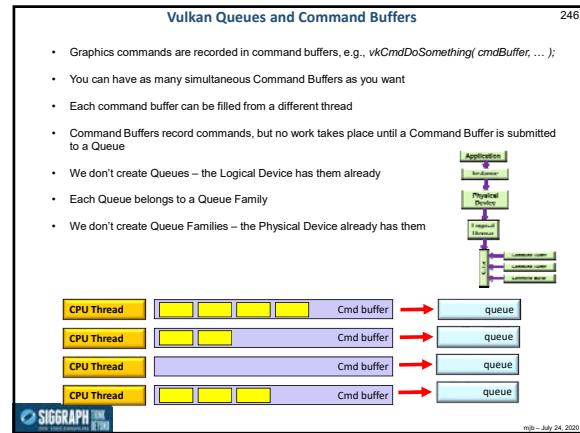
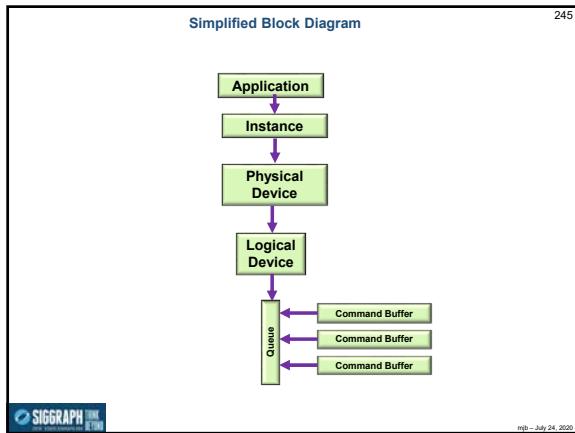
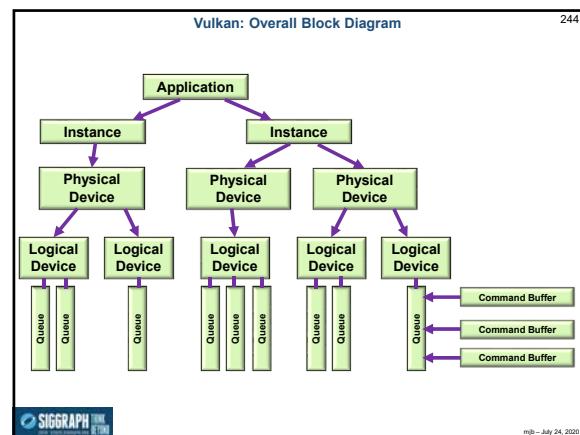
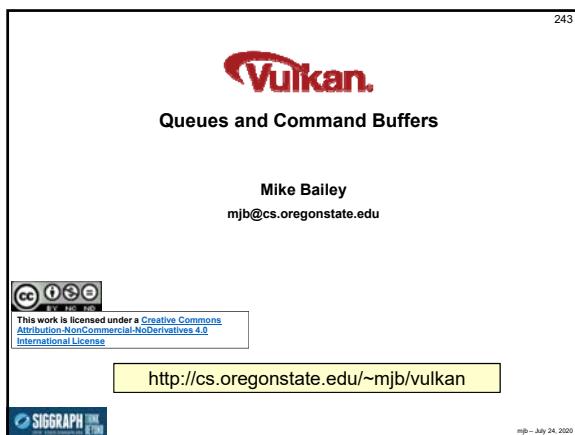
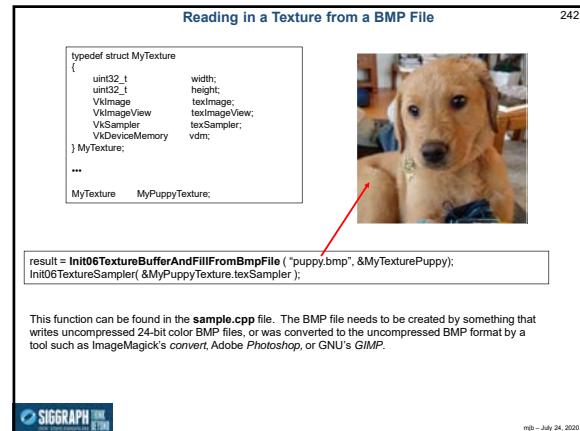
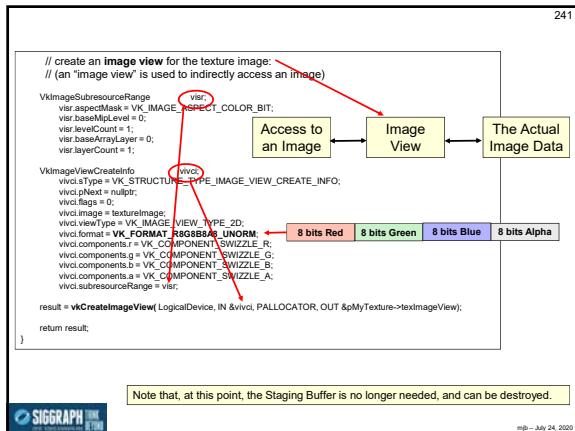
result = vkEndCommandBuffer TexureCommandBuffer;

VSubmitInfo vis;
vis.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vis.pNext = nullptr;
vis.commandBufferCount = 1;
vis.pCommandBuffers = &TextureCommandBuffer;
vis.pWaitSemaphoreCount = 1;
vis.pWaitSemaphores = &(VSemaphore *pulptr);
vis.signalSemaphoreCount = 0;
vis.pSignalSemaphores = nullptr;
vis.pWaitDstStageMask = (VkPipelineStageFlags *pulptr);

result = vkQueueSubmit Queue, 1, IN &vis, VK_NULL_HANDLE);
result = vkQueueWaitIdle Queue;


```

mjb – July 24, 2020



Querying what Queue Families are Available 247

```

uint32_t count;
vkGetPhysicalDeviceQueueFamilyProperties( IN PhysicalDevice, &count, OUT (VkQueueFamilyProperties *) nullptr );

VkQueueFamilyProperties *vqfp = new VkQueueFamilyProperties[ count ];
vkGetPhysicalDeviceQueueFamilyProperties( PhysicalDevice, &count, OUT &vqfp );

for( unsigned int i = 0; i < count; ++i )
{
    printf( FpDebug, "Queue Family Count = %d : ", vqfp[i].queueCount );
    if( ( vqfp[i].queueFlags & VK_QUEUE_GRAPHICS_BIT ) != 0 ) printf( FpDebug, "Graphics" );
    if( ( vqfp[i].queueFlags & VK_QUEUE_COMPUTE_BIT ) != 0 ) printf( FpDebug, "Compute" );
    if( ( vqfp[i].queueFlags & VK_QUEUE_TRANSFER_BIT ) != 0 ) printf( FpDebug, "Transfer" );
    printf( FpDebug, "\n" );
}

Found 3 Queue Families:
0: Queue Family Count = 16 : Graphics Compute Transfer
1: Queue Family Count = 1 : Transfer
2: Queue Family Count = 8 : Compute

```

mjb - July 24, 2020

Similarly, we Can Write a Function that Finds the Proper Queue Family 248

```

int FindQueueFamilyThatDoesGraphics()
{
    uint32_t count = -1;
    vkGetPhysicalDeviceQueueFamilyProperties( IN PhysicalDevice, OUT &count, OUT (VkQueueFamilyProperties *) nullptr );

    VkQueueFamilyProperties *vqfp = new VkQueueFamilyProperties[ count ];
    vkGetPhysicalDeviceQueueFamilyProperties( IN PhysicalDevice, IN &count, OUT vqfp );

    for( unsigned int i = 0; i < count; ++i )
    {
        if( ( vqfp[i].queueFlags & VK_QUEUE_GRAPHICS_BIT ) != 0 )
            return i;
    }
    return -1;
}

```

mjb - July 24, 2020

Creating a Logical Device Needs to Know Queue Family Information 249

```

float queuePriorities[ ] =
{
    1.0f, // one entry per queueCount
};

VkDeviceQueueCreateInfo vdcii[ ];
vdcii[0].sType = VK_STRUCTURE_TYPE_QUEUE_CREATE_INFO;
vdcii[0].pNext = nullptr;
vdcii[0].flags = 0;
vdcii[0].queueFamilyIndex = FindQueueFamilyThatDoesGraphics();
vdcii[0].queueCount = 1;
vdcii[0].queuePriorities = (float *) queuePriorities;

VkDeviceCreateInfo vdi;
vdi.sType = VK_STRUCTURE_TYPE_DEVICE_CREATE_INFO;
vdi.pNext = nullptr;
vdi.flags = 0;
vdi.enabledCreateInfoCount = 1; // # of device queues wanted
vdi.pCreateInfos = &vdcii[0]; // array of VkDeviceQueueCreateInfo's
vdi.enabledLayerCount = sizeof(myDeviceLayers) / sizeof(char *);
vdi.enabledExtensionCount = sizeof(myDeviceExtensions) / sizeof(char *);
vdi.ppEnabledExtensionNames = myDeviceExtensions;
vdi.pEnabledFeatures = IN &physicalDeviceFeatures; // already created

result = vkCreateLogicalDevice( PhysicalDevice, IN &vdi, PALLOCATOR, OUT &LogicalDevice );

VkQueue Queue;
uint32_t queueFamilyIndex = FindQueueFamilyThatDoesGraphics();
uint32_t queueIndex = 0;

result = vkGetDeviceQueue( LogicalDevice, queueFamilyIndex, queueIndex, OUT &Queue );

```

mjb - July 24, 2020

Creating the Command Pool as part of the Logical Device 250

```

VkResult Init06CommandPool()
{
    VkResult result;

    VkCommandPoolCreateInfo vcpci;
    vcpci.sType = VK_STRUCTURE_TYPE_COMMAND_POOL_CREATE_INFO;
    vcpci.pNext = nullptr;
    vcpci.flags = 0;
    vcpci.queueFamilyIndex = FindQueueFamilyThatDoesGraphics();

    #ifdef CHOICES
    VK_COMMAND_POOL_CREATE_TRANSIENT_BIT
    VK_COMMAND_POOL_CREATE_RESET_COMMAND_BUFFER_BIT
    #endif
    vcpci.flags |= VK_COMMAND_POOL_CREATE_RESET_COMMAND_BUFFER_BIT;

    result = vkCreateCommandPool( LogicalDevice, IN &vcpci, PALLOCATOR, OUT &CommandPool );
    return result;
}

```

mjb - July 24, 2020

Creating the Command Buffers 251

```

VkResult Init06CommandBuffers()
{
    VkResult result;

    // allocate 2 command buffers for the double-buffered rendering:
    {
        VkCommandBufferAllocateInfo vcbai;
        vcbai.sType = VK_STRUCTURE_TYPE_COMMAND_BUFFER_ALLOCATE_INFO;
        vcbai.pNext = nullptr;
        vcbai.commandPool = CommandPool;
        vcbai.level = VK_COMMAND_BUFFER_LEVEL_PRIMARY;
        vcbai.commandBufferCount = 2; // 2, because of double-buffering
        result = vkAllocateCommandBuffers( LogicalDevice, IN &vcbai, OUT &CommandBuffers[0] );
    }

    // allocate 1 command buffer for the transferring pixels from a staging buffer to a texture buffer:
    {
        VkCommandBufferAllocateInfo vcbai;
        vcbai.sType = VK_STRUCTURE_TYPE_COMMAND_BUFFER_ALLOCATE_INFO;
        vcbai.pNext = nullptr;
        vcbai.commandPool = CommandPool;
        vcbai.level = VK_COMMAND_BUFFER_LEVEL_PRIMARY;
        vcbai.commandBufferCount = 1;
        result = vkAllocateCommandBuffers( LogicalDevice, IN &vcbai, OUT &TextureCommandBuffer );
    }

    return result;
}

```

mjb - July 24, 2020

Beginning a Command Buffer – One per Image 252

```

VkSemaphoreCreateInfo vscl;
vscl.sType = VK_STRUCTURE_TYPE_SEMAPHORE_CREATE_INFO;
vscl.pNext = nullptr;
vscl.flags = 0;

VkSemaphore imageReadySemaphore;
imageReadySemaphore = vkCreateSemaphore( LogicalDevice, IN &vscl, PALLOCATOR, OUT &ImageReadySemaphore );

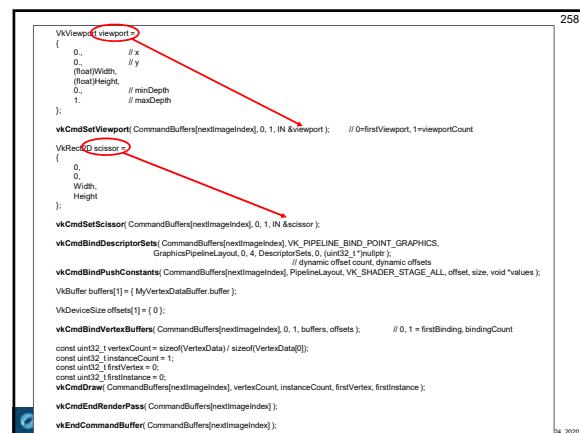
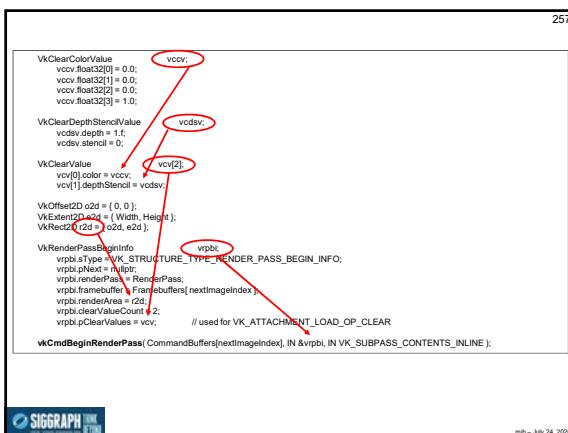
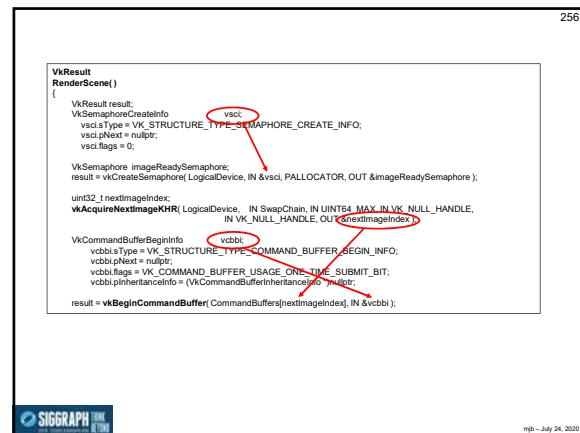
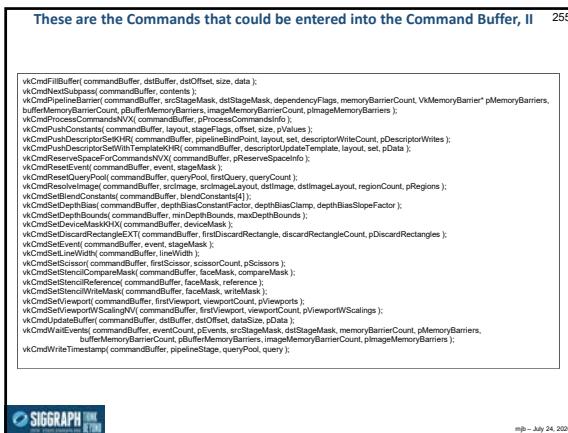
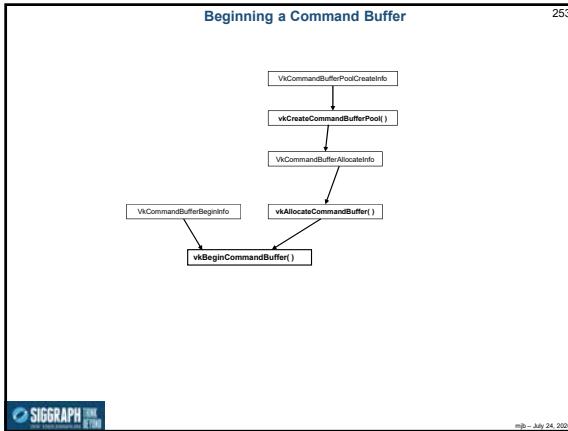
uint32_t nextImageIndex;
vkAcquireNextImageKHR( LogicalDevice, IN SwapChain, IN UINT64 MAX,
                      IN ImageReadySemaphore, IN VK_NULL_HANDLE, OUT &nextImageIndex );

VkCommandBufferBeginInfo vcbbi;
vcbbi.sType = VK_STRUCTURE_TYPE_COMMAND_BUFFER_BEGIN_INFO;
vcbbi.pNext = nullptr;
vcbbi.flags = VK_COMMAND_BUFFER_USAGE_ONE_TIME_SUBMIT_BIT;
vcbbi.inheritanceInfo = (VkCommandBufferInheritanceInfo *) nullptr;

result = vkBeginCommandBuffer( CommandBuffers[nextImageIndex], IN &vcbbi );
    ...
vkEndCommandBuffer( CommandBuffers[nextImageIndex] );

```

mjb - July 24, 2020



Submitting a Command Buffer to a Queue for Execution 259

```

VkSubmitInfo           vsi;
vsi.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vsi.pNext = nullptr;
vsi.commandBufferCount = 1;
vsi.pCommandBuffers = &CommandBuffer;
vsi.waitSemaphoreCount = 1;
vsi.pWaitSemaphores = imageReadySemaphore;
vsi.signalSemaphoreCount = 0;
vsi.pSignalSemaphoreValue = (VksSemaphore *)nullptr;
vsi.pWaitDstStageMask = (VkPipelineStageFlags *)nullptr;

```

SIGGRAPH 2019 RECAP

mjb – July 24, 2020

The Entire Submission / Wait / Display Process 260

```

vkFenceCreate(vci);
vci.sType = VK_STRUCTURE_TYPE_FENCE_CREATE_INFO;
vci.pNext = nullptr;
vci.flags = 0;

vkCreateFence(LogicalDevice, IN &vci, PALLOCATOR, OUT &renderFence);
result = VK_SUCCESS;

vkPipelineStageFlags waitAllBottom = VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT;
VKQueue presentQueue;
vkGetDeviceQueue(LogicalDevice, FindQueueFamilyThatDoesGraphics(), 0, OUT &presentQueue);
// 0 = queueIndex

vkSubmitInfo vsi;
vsi.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vsi.pNext = nullptr;
vsi.commandBufferCount = 1;
vsi.pCommandBuffers = &CommandBuffer;
vsi.waitSemaphoreCount = 1;
vsi.pWaitSemaphores = &imageReadySemaphore;
vsi.signalSemaphoreCount = 0;
vsi.pSignalSemaphoreValue = (VksSemaphore *)nullptr;
vsi.pWaitDstStageMask = (VkPipelineStageFlags *)nullptr;
vsi.signalSemaphoreCount = 0;
vsi.pSignalSemaphoreValue = &SemaphoreRenderFinish;

vkQueueSubmit(presentQueue, 1, IN &vsi, IN renderFence); // 1 = submitCount
result = vkWaitForFences(LogicalDevice, 1, IN &renderFence, VK_TRUE, UINT64_MAX); // waitAll, timeout

vkDestroyFence(LogicalDevice, renderFence, PALLOCATOR);

VKPresentInfoKHR vpi;
vpi.sType = VK_STRUCTURE_TYPE_PRESENT_INFO_KHR;
vpi.pNext = nullptr;
vpi.waitSemaphoreCount = 0;
vpi.pWaitSemaphores = (VksSemaphore *)nullptr;
vpi.swapchainCount = 1;
vpi.pSwapchains = &SwapChain;
vpi.imageIndex = SwapChain.currentImageIndex;
vpi.pImageIndices = &SwapChain.currentImageIndex;
vpi.pResults = &vpiResult; // nullptr;
vpi.timeout = 0;

result = vkQueuePresentKHR(presentQueue, IN &vpi);

```

SIGGRAPH 2019 RECAP

mjb – July 24, 2020

What Happens After a Queue has Been Submitted? 261

As the Vulkan 1.1 Specification says:

"Command buffer submissions to a single queue respect submission order and other implicit ordering guarantees, but otherwise may overlap or execute out of order. Other types of batches and queue submissions against a single queue (e.g. sparse memory binding) have no implicit ordering constraints with any other queue submission or batch. Additional explicit ordering constraints between queue submissions and individual batches can be expressed with semaphores and fences."

In other words, the Vulkan driver on your system will execute the commands in a single buffer in the order in which they were put there.

But, between different command buffers submitted to different queues, the driver is allowed to execute commands between buffers in-order or out-of-order or overlapped-order, depending on what it thinks it can get away with.

The message here is, I think, always consider using some sort of Vulkan synchronization when one command depends on a previous command reaching a certain state first.

SIGGRAPH 2019 RECAP

mjb – July 24, 2020

vulkan.
The Swap Chain

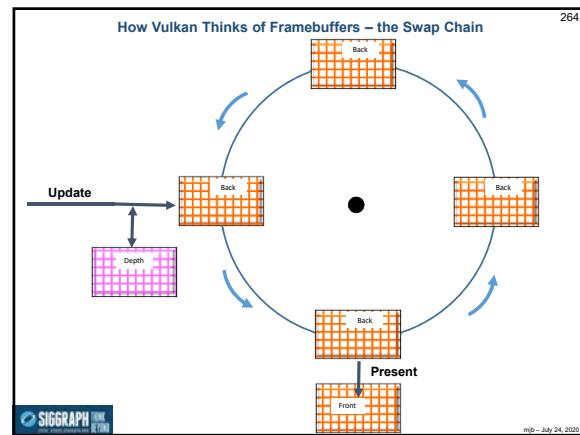
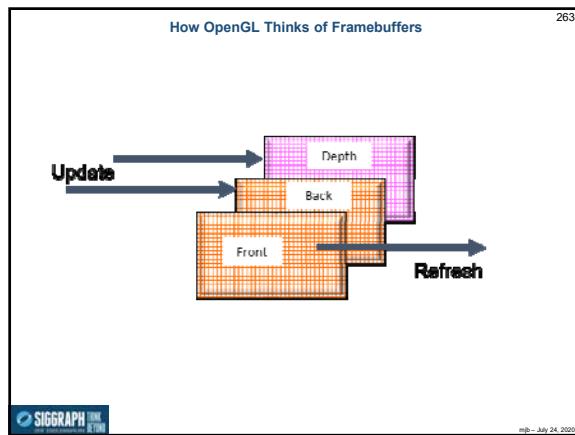
Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

SIGGRAPH 2019 RECAP

mjb – July 24, 2020



What is a Swap Chain? 265

Vulkan does not use the idea of a "back buffer". So, we need a place to render into before moving an image into place for viewing. This is called the **Swap Chain**.

In essence, the Swap Chain manages one or more image objects that form a sequence of images that can be drawn into and then given to the Surface to be presented to the user for viewing.

Swap Chains are arranged as a ring buffer → 

Swap Chains are tightly coupled to the window system.

After creating the Swap Chain in the first place, the process for using the Swap Chain is:

1. Ask the Swap Chain for an image
2. Render into it via the Command Buffer and a Queue
3. Return the image to the Swap Chain for presentation
4. Present the image to the viewer (copy to "front buffer")



mjb – July 24, 2020

We Need to Find Out What our Display Capabilities Are 266

```

VksurfacecapabilitiesKHR vsc;
vkGetPhysicalDeviceSurfaceCapabilitiesKHR(PhysicalDevice, Surface, OUT &vsc);
VKExtent2D surfaceRes = vsc.currentExtent;
fprintf(FpDebug, "vkGetPhysicalDeviceSurfaceCapabilitiesKHR:\n");
...
```

```

Vdpc002 supported:
result = vkGetPhysicalDeviceSurfaceSupportKHR(PhysicalDevice, Surface, &formatCount, (VkSurfaceFormatKHR *)nullptr);
if( supported == VK_TRUE )
    printf(FpDebug, "*** This Surface is supported by the Graphics Queue\n");
...
```

```

uint32_t formatCount;
vkGetPhysicalDeviceSurfaceFormatsKHR(PhysicalDevice, Surface, &formatCount, (VkSurfaceFormatKHR *)nullptr);
VksurfaceformatKHR *surfaceFormats = new VksurfaceformatKHR[formatCount];
vkGetPhysicalDeviceSurfaceFormatsKHR(PhysicalDevice, Surface, &formatCount, surfaceFormats);
fprintf(FpDebug, "inFound %d Surface Formats:\n", formatCount);
...
```

```

uint32_t presentModeCount;
vkGetPhysicalDeviceSurfacePresentModesKHR(PhysicalDevice, Surface, &presentModeCount, (VkPresentModeKHR *)nullptr);
VkpresentmodeKHR *presentModes = new VkpresentmodeKHR[presentModeCount];
vkGetPhysicalDeviceSurfacePresentModesKHR(PhysicalDevice, Surface, &presentModeCount, presentModes);
fprintf(FpDebug, "inFound %d Present Modes:\n", presentModeCount);
...

```



mjb – July 24, 2020

We Need to Find Out What our Display Capabilities Are 267

VulkanDebug.txt output:

```

vkGetPhysicalDeviceSurfaceCapabilitiesKHR:
minImageCount = 2; maxImageCount = 8
currentExtent = 1024 x 1024
minImageExtent = 1024 x 1024
maxImageExtent = 1024 x 1024
maxImageArrayLayers = 1
supportedTransforms = 0x0001
currentTransform = 0x0001
supportedCompositeAlpha = 0x0001
supportedUsageFlags = 0x00f
** This Surface is supported by the Graphics Queue **
```

Found 2 Surface Formats:

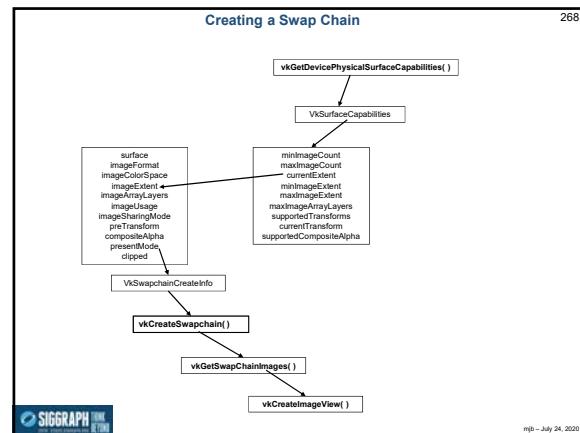
0: 44 0	(VK_FORMAT_B8G8R8A8_UNORM, VK_COLOR_SPACE_SRGB_NONLINEAR_KHR)
1: 50 0	(VK_FORMAT_B8G8R8A8_SRGB, VK_COLOR_SPACE_SRGB_NONLINEAR_KHR)

Found 3 Present Modes:

0: 2	(VK_PRESENT_MODE_FIFO_KHR)
1: 3	(VK_PRESENT_MODE_FIFO_RELAXED_KHR)
2: 1	(VK_PRESENT_MODE_MAILBOX_KHR)



mjb – July 24, 2020



Creating a Swap Chain 269

```

VksurfacecapabilitiesKHR vsc;
vkGetPhysicalDeviceSurfaceCapabilitiesKHR(PhysicalDevice, Surface, OUT &vsc);
VKExtent2D surfaceRes = vsc.currentExtent;
...
```

```

VkswapchainCreateInfoKHR vscci;
vscci.type = VK_STRUCTURE_TYPE_SWAPCHAIN_CREATE_INFO_KHR;
vscci.pNext = nullptr;
vscci.flags = 0;
vscci.surface = Surface;
vscci.imageCount = 2; // double buffering
vscci.imageFormat = VK_FORMAT_B8G8R8A8_UNORM;
vscci.imageColorSpace = VK_COLOR_SPACE_SRGB_NONLINEAR_KHR;
vscci.imageExtent.width = surfaceRes.width;
vscci.imageExtent.height = surfaceRes.height;
vscci.imageUsage = VK_IMAGE_USAGE_COLOR_ATTACHMENT_BIT;
vscci.preTransform = VK_SURFACE_TRANSFORM_IDENTITY_BIT_KHR;
vscci.compositeAlpha = VK_COMPOSITE_ALPHA_OPAQUE_BIT_KHR;
vscci.imageArrayLayers = 1;
vscci.imageSharingMode = VK_SHARING_MODE_EXCLUSIVE;
vscci.queueFamilyIndexCount = 0;
vscci.queueFamilyIndices = (const uint32_t *)nullptr;
vscci.presentMode = VK_PRESENT_MODE_MAILBOX_KHR;
vscci.oldSwapchain = VK_NULL_HANDLE;
vscci.clipped = VK_TRUE;

```

```

result = vkCreateSwapchainKHR(LogicalDevice, IN &vscci, PALLOCATOR, OUT &SwapChain);

```



mjb – July 24, 2020

Creating the Swap Chain Images and Image Views 270

```

uint32_t imageCount; // # of display buffers - 2?
result = vkGetSwapchainImagesKHR(LogicalDevice, IN SwapChain, OUT &imageCount, (VklImage *)nullptr);
PresentImages = new VklImage[imageCount];
result = vkGetSwapchainImagesKHR(LogicalDevice, SwapChain, OUT &imageCount, PresentImages);
// present views for the double-buffering:
PresentImageViews = new VklImageView[imageCount];
for( unsigned int i = 0; i < imageCount; ++ )
{
    VklImageViewCreateInfo vivci;
    vivci.type = VK_STRUCTURE_TYPE_IMAGE_VIEW_CREATE_INFO;
    vivci.pNext = nullptr;
    vivci.flags = 0;
    vivci.viewType = VK_IMAGE_VIEW_TYPE_2D;
    vivci.format = VK_FORMAT_B8G8R8A8_UNORM;
    vivci.components.r = VK_COMPONENT_SWIZZLE_R;
    vivci.components.g = VK_COMPONENT_SWIZZLE_G;
    vivci.components.b = VK_COMPONENT_SWIZZLE_B;
    vivci.components.a = VK_COMPONENT_SWIZZLE_A;
    vivci.subresourceRange.baseMipLevel = 0;
    vivci.subresourceRange.levelCount = 1;
    vivci.subresourceRange.baseArrayLayer = 0;
    vivci.subresourceRange.layerCount = 1;
    vivci.image = PresentImages[i];
}
result = vkCreateImageView(LogicalDevice, IN &vivci, PALLOCATOR, OUT &PresentImageViews[ i ]);

```



mjb – July 24, 2020

Rendering into the Swap Chain, I

```

VkSemaphoreCreateInfo vsci;
vsci.sType = VK_STRUCTURE_TYPE_SEMAPHORE_CREATE_INFO;
vsci.pNext = nullptr;
vsci.flags = 0;

VkSemaphore imageReadySemaphore;
result = vkCreateSemaphore( LogicalDevice, IN &vsci, PALLOCATOR, OUT &imageReadySemaphore );

uint32_t nextImageIndex;
uint64_t timeout = UINT64_MAX;
vkAcquireNextImageKHR( LogicalDevice, IN SwapChain, IN timeout, IN imageReadySemaphore,
                      IN VK_NULL_HANDLE, OUT &nextImageIndex );
...
result = vkBeginCommandBuffer( CommandBuffers[ nextImageIndex ], IN &vcbi );
...
vkCmdBeginRenderPass( CommandBuffers[ nextImageIndex ], IN &vrbi,
                      IN VK_SUBPASS_CONTENTS_INLINE );
vkCmdBindPipeline( CommandBuffers[ nextImageIndex ], VK_PIPELINE_BIND_POINT_GRAPHICS, GraphicsPipeline );
...
vkCmdEndRenderPass( CommandBuffers[ nextImageIndex ] );
vkEndCommandBuffer( CommandBuffers[ nextImageIndex ] );

```

mjb - July 24, 2020

Rendering into the Swap Chain, II

```

VkFenceCreateInfo vfc;
vfc.sType = VK_STRUCTURE_TYPE_FENCE_CREATE_INFO;
vfc.pNext = nullptr;
vfc.flags = 0;

VkFence renderFence;
vkCreateFence( LogicalDevice, &vfc, PALLOCATOR, OUT &renderFence );

VkQueue presentQueue;
vkGetDeviceQueue( LogicalDevice, FindQueueFamilyThatDoesGraphics( ), 0,
                  OUT &presentQueue );
...
VkSubmitInfo vsi;
vsi.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vsi.pNext = nullptr;
vsi.waitSemaphoreCount = 1;
vsi.pWaitSemaphores = &imageReadySemaphore;
vsi.waitDstStageMask = &VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT;
vsi.commandBufferCount = 1;
vsi.pCommandBuffers = &CommandBuffers[ nextImageIndex ];
vsi.signalSemaphoreCount = 0;
vsi.pSignalSemaphores = &SemaphorePreRenderFinished;

result = vkQueueSubmit( presentQueue, 1, IN &vsi, IN renderFence ); // 1 = submitCount

```

mjb - July 24, 2020

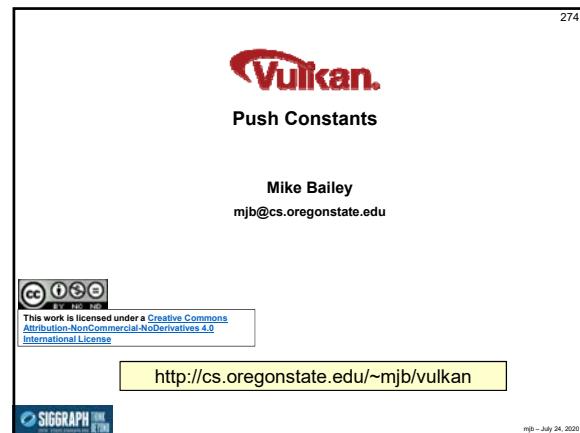
Rendering into the Swap Chain, III

```

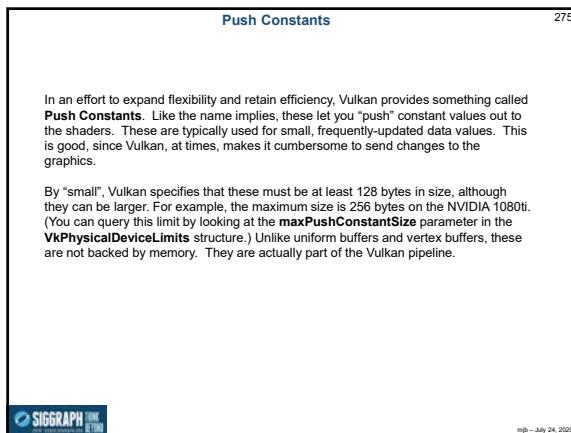
result = vkWaitForFences( LogicalDevice, 1, IN &renderFence, VK_TRUE, UINT64_MAX );
VkPresentInfoKHR vpi;
vpi.sType = VK_STRUCTURE_TYPE_PRESENT_INFO_KHR;
vpi.pNext = nullptr;
vpi.waitSemaphoreCount = 0;
vpi.pWaitSemaphores = (VkSemaphore *)nullptr;
vpi.swapchainCount = 1;
vpi.pSwapchains = &SwapChain;
vpi.pImageIndices = &nextImageIndex;
vpi.pResults = (VkResult *)nullptr;
result = vkQueuePresentKHR( presentQueue, IN &vpi );

```

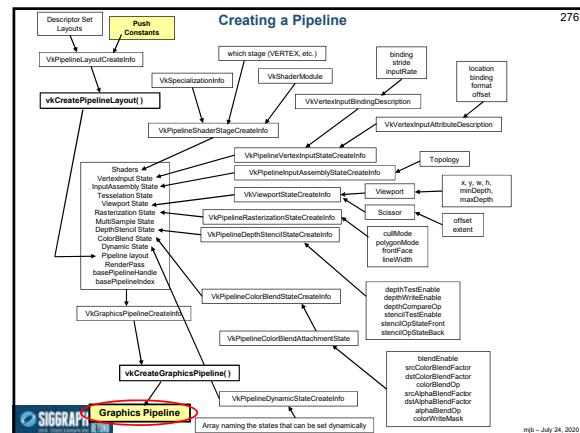
mjb - July 24, 2020



mjb - July 24, 2020



mjb - July 24, 2020



mjb - July 24, 2020

Push Constants 277

On the shader side, if, for example, you are sending a 4x4 matrix, the use of push constants in the shader looks like this:

```
layout(push_constant) uniform matrix
{
    mat4 modelMatrix;
} Matrix;
```

On the application side, push constants are pushed at the shaders by binding them to the Vulkan Command Buffer:

```
vkCmdPushConstants( CommandBuffer, PipelineLayout, stageFlags,
    offset, size, pValues );
```

where:
`stageFlags` are or'ed bits of `VK_PIPELINE_STAGE_VERTEX_SHADER_BIT`, `VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT`, etc.
`size` is in bytes
`pValues` is a void * pointer to the data, which, in this 4x4 matrix example, would be of type `glm::mat4`.

 mjb - July 24, 2020

Setting up the Push Constants for the Pipeline Structure 278

Prior to that, however, the pipeline layout needs to be told about the Push Constants:

```
VkPushConstantRange vpcr[1];
vpcr[0].stageFlags =
    VK_PIPELINE_STAGE_VERTEX_SHADER_BIT
    | VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT;
vpcr[0].offset = 0;
vpcr[0].size = sizeof( glm::mat4 );
vpcr[1].stageFlags =
    VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT;
vpcr[1].offset = 16;
vpcr[1].size = sizeof( glm::mat4 );

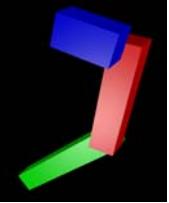
VkPipelineLayoutCreateInfo vplci;
vplci.sType = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
vplci.pNext = nullptr;
vplci.flags = 0;
vplci.setLayoutCount = 4;
vplci.pSetLayouts = DescriptorSetLayouts;
vplci.pushConstantRangeCount = 1;
vplci.pPushConstantRanges = vpcr;

result = vkCreatePipelineLayout( LogicalDevice, IN &vplci, PALLOCATOR,
    OUT &GraphicsPipelineLayout );
```

 mjb - July 24, 2020

An Robotic Example using Push Constants 279

A robotic animation (i.e., a hierarchical transformation system)

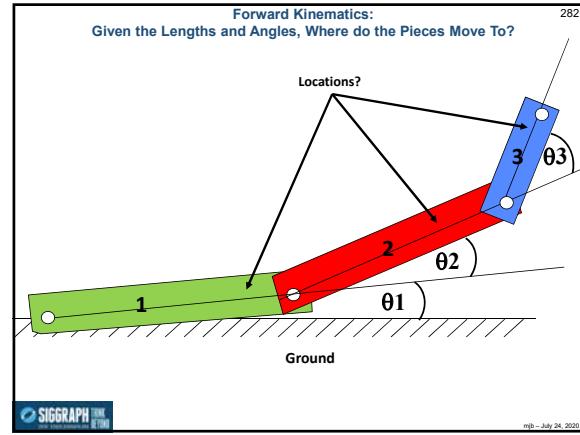
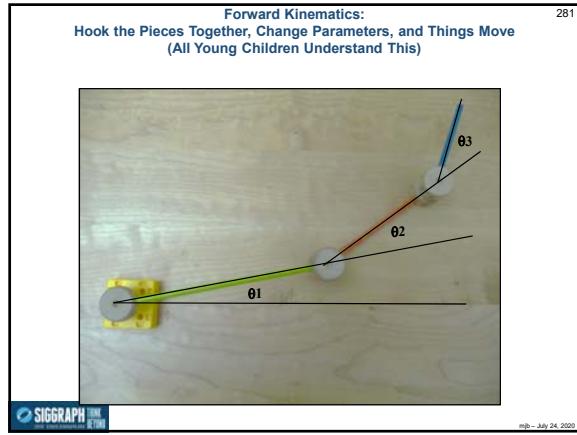
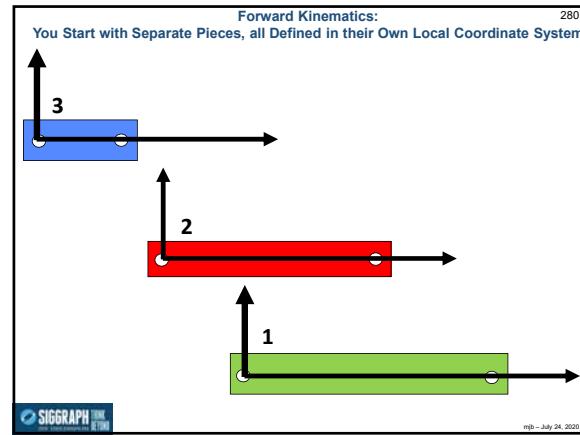


Where each arm is represented by:

```
struct arm
{
    glm::mat4 armMatrix;
    glm::vec3 armColor;
    float   armScale; // scale factor in x
};

struct armArm1;
struct armArm2;
struct armArm3;
```

 mjb - July 24, 2020



Positioning Part #1 With Respect to Ground 283

1. Rotate by Θ_1
2. Translate by $T_{1/G}$

Write it $\rightarrow [M_{1/G}] = [T_{1/G}] * [R_{\theta_1}]$

Say it $\leftarrow [M_{1/G}] = [T_{1/G}] * [R_{\theta_1}]$

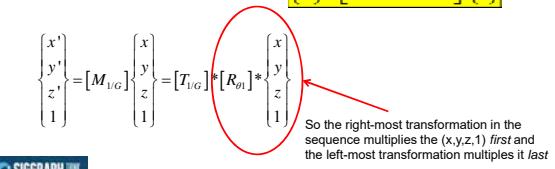
 mjb - July 24, 2020

Why Do We Say it Right-to-Left? 284

$[M_{1/G}] = [T_{1/G}] * [R_{\theta_1}]$

We adopt the convention that the coordinates are multiplied on the right side of the matrix:

$$\begin{pmatrix} x' \\ y' \\ z' \\ 1 \end{pmatrix} = \begin{pmatrix} A & B & C & D \\ E & F & G & H \\ I & J & K & L \\ 0 & 0 & 0 & 1 \end{pmatrix} \cdot \begin{pmatrix} x \\ y \\ z \\ 1 \end{pmatrix}$$



So the right-most transformation in the sequence multiplies the (x,y,z,1) *first* and the left-most transformation multiplies it *last*.

 mjb - July 24, 2020

Positioning Part #2 With Respect to Ground 285

1. Rotate by Θ_2
2. Translate the length of part 1
3. Rotate by Θ_1
4. Translate by $T_{1/G}$

Write it $\rightarrow [M_{2/G}] = [T_{1/G}] * [R_{\theta_1}] * [T_{2/1}] * [R_{\theta_2}]$

Say it $\leftarrow [M_{2/G}] = [M_{1/G}] * [M_{2/1}]$

 mjb - July 24, 2020

Positioning Part #3 With Respect to Ground 286

1. Rotate by Θ_3
2. Translate the length of part 2
3. Rotate by Θ_2
4. Translate the length of part 1
5. Rotate by Θ_1
6. Translate by $T_{1/G}$

Write it $\rightarrow [M_{3/G}] = [T_{1/G}] * [R_{\theta_1}] * [T_{2/1}] * [R_{\theta_2}] * [T_{3/2}] * [R_{\theta_3}]$

Say it $\leftarrow [M_{3/G}] = [M_{1/G}] * [M_{2/1}] * [M_{3/2}]$

 mjb - July 24, 2020

In the Reset Function 287

```

struct arm      Arm1;
struct arm      Arm2;
struct arm      Arm3;

...
Arm1.armMatrix = glm::mat4( 1. );
Arm1.armColor  = glm::vec3( 0.1, 1.0, 0.1 );
Arm1.armScale   = 6.0f;

Arm2.armMatrix = glm::mat4( 1. );
Arm2.armColor  = glm::vec3( 1.0, 0.1, 0.1 );
Arm2.armScale   = 4.0f;

Arm3.armMatrix = glm::mat4( 1. );
Arm3.armColor  = glm::vec3( 0.1, 0.1, 1.0 );
Arm3.armScale   = 2.0f;
  
```

The constructor `glm::mat4(1.)` produces an identity matrix. The actual transformation matrices will be set in `UpdateScene()`.

 mjb - July 24, 2020

Setup the Push Constant for the Pipeline Structure 288

```

VkPushConstantRange
vpcr[0].stageFlags =
    VK_PIPELINE_STAGE_VERTEX_SHADER_BIT
    | VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT;
vpcr[0].offset = 0;
vpcr[0].size = sizeof( struct arm );

VkPipelineLayoutCreateInfo
vpclci.sType = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
vpclci.pNext = nullptr;
vpclci.flags = 0;
vpclci.setLayoutCount = 4;
vpclci.pSetLayouts = DescriptorSetLayouts;
vpclci.pushConstantRangeCount = 1;
vpclci.pushConstantRanges = vpcr;
result = vkCreatePipelineLayout( LogicalDevice, IN &vpclci, PALLOCATOR,
                                OUT &GraphicsPipelineLayout );
  
```

 mjb - July 24, 2020

In the *UpdateScene* Function 289

```

float rot1 = (float)Time;
float rot2 = 2.0f * rot1;
float rot3 = 2.0f * rot2;

glm::vec3 zaxis = glm::vec3(0.0, 0.0, 1.0);

glm::mat4 m1g = glm::mat4(1.0); // identity
m1g = glm::translate(m1g, glm::vec3(0.0, 0.0, 0.0));
m1g = glm::rotate(m1g, rot1, zaxis); // [T][R]

glm::mat4 m21 = glm::mat4(1.0); // identity
m21 = glm::translate(m21, glm::vec3(2.0 * Arm1.armScale, 0.0, 0.0));
m21 = glm::rotate(m21, rot2, zaxis); // [T][R]
m21 = glm::translate(m21, glm::vec3(0.0, 0.0, 2.0)); // z-offset from previous arm

glm::mat4 m32 = glm::mat4(1.0); // identity
m32 = glm::translate(m32, glm::vec3(2.0 * Arm2.armScale, 0.0, 0.0));
m32 = glm::rotate(m32, rot3, zaxis); // [T][R]
m32 = glm::translate(m32, glm::vec3(0.0, 0.0, 2.0)); // z-offset from previous arm

Arm1.armMatrix = m1g; // m1g
Arm2.armMatrix = m1g * m21; // m2g
Arm3.armMatrix = m1g * m21 * m32; // m3g

```

 SIGGRAPH 2019

mjb - July 24, 2020

In the *RenderScene* Function 290

```

VkBuffer buffers[1] = { MyVertexDataBuffer.buffer };

vkCmdBindVertexBuffers(CommandBuffers[nextImageIndex], 0, 1, buffers, offsets);

vkCmdPushConstants(CommandBuffers[nextImageIndex], GraphicsPipelineLayout,
VK_SHADER_STAGE_ALL, 0, sizeof(struct arm), (void *)&Arm1);

vkCmdDraw(CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstVertex, firstInstance);

vkCmdPushConstants(CommandBuffers[nextImageIndex], GraphicsPipelineLayout,
VK_SHADER_STAGE_ALL, 0, sizeof(struct arm), (void *)&Arm2);

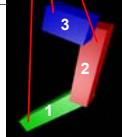
vkCmdDraw(CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstVertex, firstInstance);

vkCmdPushConstants(CommandBuffers[nextImageIndex], GraphicsPipelineLayout,
VK_SHADER_STAGE_ALL, 0, sizeof(struct arm), (void *)&Arm3);

vkCmdDraw(CommandBuffers[nextImageIndex], vertexCount, instanceCount, firstVertex, firstInstance);

```

The strategy is to draw each link using the same vertex buffer, but modified with a unique color, length, and matrix transformation



 SIGGRAPH 2019

mjb - July 24, 2020

In the Vertex Shader 291

```

layout(push_constant) uniform arm
{
    mat4 armMatrix;
    vec3 armColor;
    float armScale; // scale factor in X
} RobotArm;

layout(location = 0) in vec3 aVertex;
...

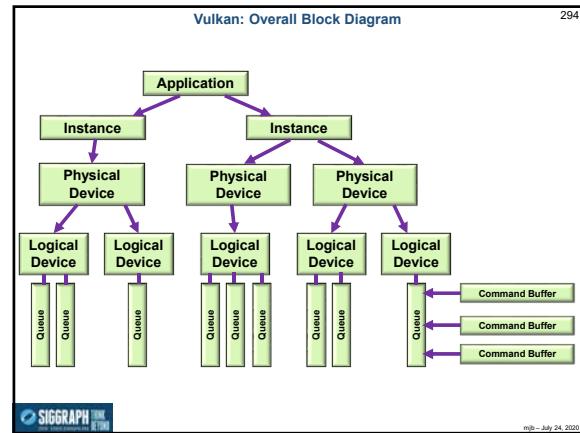
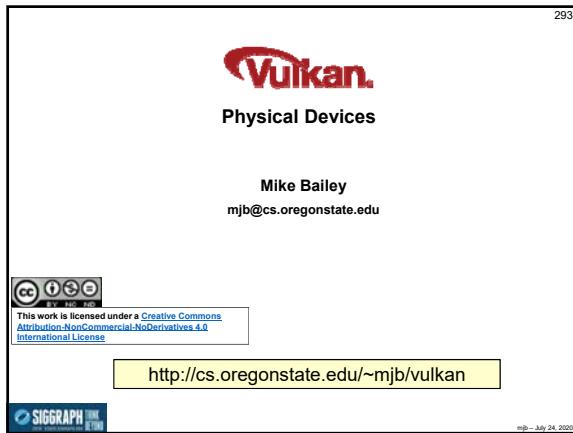
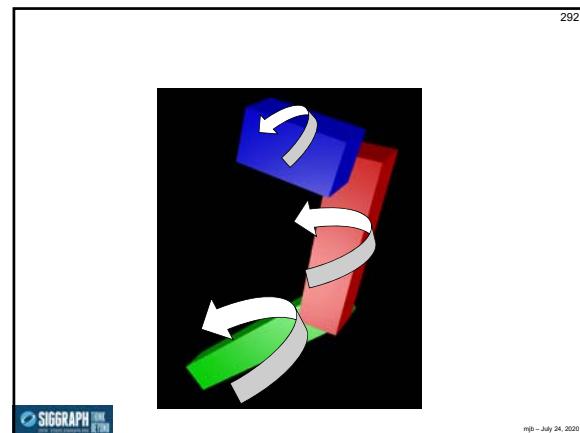
vec3 bVertex = aVertex; // arm coordinate system is [-1, 1] in X
bVertex.x += 1; // now is [0, 2]
bVertex.x /= 2; // now is [0, 1]
bVertex.x *= (RobotArm.armScale); // now is [0, RobotArm.armScale]
bVertex = vec3(RobotArm.armMatrix * vec4(bVertex, 1.0));

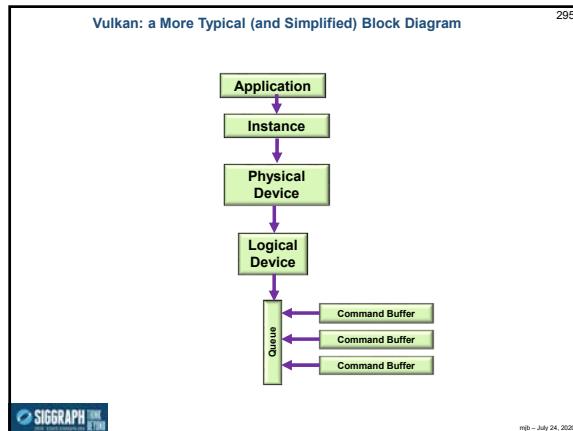
...
gl_Position = PVM * vec4(bVertex, 1.0); // Projection * Viewing * Modeling matrices

```

 SIGGRAPH 2019

mjb - July 24, 2020





Querying the Number of Physical Devices 296

```
uint32_t count;
result = vkEnumeratePhysicalDevices( Instance, OUT &count, OUT (VkPhysicalDevice *)nullptr );
```

VkPhysicalDevice * physicalDevices = new VkPhysicalDevice[count];
result = vkEnumeratePhysicalDevices(Instance, OUT &count, OUT physicalDevices);

This way of querying information is a recurring OpenCL and Vulkan pattern (get used to it):

How many total there are	Where to put them
result = vkEnumeratePhysicalDevices(Instance, &count, nullptr);	result = vkEnumeratePhysicalDevices(Instance, &count, physicalDevices);

mjb – July 24, 2020

Vulkan: Identifying the Physical Devices 297

```
VkResult result = VK_SUCCESS;
result = vkEnumeratePhysicalDevices( Instance, OUT &PhysicalDeviceCount, (VkPhysicalDevice *)nullptr );
if( result != VK_SUCCESS || PhysicalDeviceCount <= 0 )
{
    fprintf( FpDebug, "Could not count the physical devices!\n" );
    return VK_SHOULD_EXIT;
}

fprintf( FpDebug, "%d physical devices found.\n", PhysicalDeviceCount );

VkPhysicalDevice * physicalDevices = new VkPhysicalDevice[ PhysicalDeviceCount ];
result = vkEnumeratePhysicalDevices( Instance, OUT &PhysicalDeviceCount, OUT physicalDevices );
if( result != VK_SUCCESS )
{
    fprintf( FpDebug, "Could not enumerate the %d physical devices!", PhysicalDeviceCount );
    return VK_SHOULD_EXIT;
}
```

mjb – July 24, 2020

Which Physical Device to Use, I 298

```
int discreteSelect = -1;
int integratedSelect = -1;
for( unsigned int i = 0; i < PhysicalDeviceCount; i++ )
{
    VkPhysicalDeviceProperties vpdp;
    vkGetPhysicalDeviceProperties( physicalDevices[i], OUT &vpdp );
    if( result != VK_SUCCESS )
    {
        fprintf( FpDebug, "Could not get the physical device properties of device %d\n", i );
        return VK_SHOULD_EXIT;
    }

    fprintf( FpDebug, "\nIn Device %d:\n", i );
    fprintf( FpDebug, "IAPI version: %d\n", vpdp.apiVersion );
    fprintf( FpDebug, "IDriver version: %d\n", vpdp.driverVersion );
    fprintf( FpDebug, "Vulkan version: %d\n", vpdp.vulkanVersion );
    fprintf( FpDebug, "IDevice ID: 0x%04X\n", vpdp.deviceID );
    fprintf( FpDebug, "IPhysical Device Type: %d\n", vpdp.deviceType );
    if( vpdp.deviceType == VK_PHYSICAL_DEVICE_TYPE_DISCRETE_GPU ) printf( FpDebug, "(Discrete GPU)\n" );
    if( vpdp.deviceType == VK_PHYSICAL_DEVICE_TYPE_INTEGRATED_GPU ) printf( FpDebug, "(Integrated GPU)\n" );
    if( vpdp.deviceType == VK_PHYSICAL_DEVICE_TYPE_VIRTUAL_GPU ) printf( FpDebug, "(Virtual GPU)\n" );
    fprintf( FpDebug, "IDevice Name: %s\n", vpdp.deviceName );
    fprintf( FpDebug, "IPipeline Cache Size: %d\n", vpdp.pipelineCacheUUID[0] ).
```

mjb – July 24, 2020

Which Physical Device to Use, II 299

```
// need some logical here to decide which physical device to select:
if( vpdp.deviceType == VK_PHYSICAL_DEVICE_TYPE_DISCRETE_GPU )
    discreteSelect = i;

if( vpdp.deviceType == VK_PHYSICAL_DEVICE_TYPE_INTEGRATED_GPU )
    integratedSelect = i;

int which = -1;
if( discreteSelect >= 0 )
{
    which = discreteSelect;
    PhysicalDevice = physicalDevices[which];
}
else if( integratedSelect >= 0 )
{
    which = integratedSelect;
    PhysicalDevice = physicalDevices[which];
}
else
{
    fprintf( FpDebug, "Could not select a Physical Device!\n" );
    return VK_SHOULD_EXIT;
}
```

mjb – July 24, 2020

Asking About the Physical Device's Features 300

```
VkPhysicalDeviceProperties PhysicalDeviceFeatures;
vkGetPhysicalDeviceFeatures( IN PhysicalDevice, OUT &PhysicalDeviceFeatures );

fprintf( FpDebug, "\nPhysical Device Features:\n" );
fprintf( FpDebug, "geometryShader = %d\n", PhysicalDeviceFeatures.geometryShader );
fprintf( FpDebug, "tessellationShader = %d\n", PhysicalDeviceFeatures.tessellationShader );
fprintf( FpDebug, "multiDrawIndirect = %d\n", PhysicalDeviceFeatures.multiDrawIndirect );
fprintf( FpDebug, "wideLines = %d\n", PhysicalDeviceFeatures.wideLines );
fprintf( FpDebug, "largePoints = %d\n", PhysicalDeviceFeatures.largePoints );
fprintf( FpDebug, "multiViewport = %d\n", PhysicalDeviceFeatures.multiViewport );
fprintf( FpDebug, "occlusionQueryPrecise = %d\n", PhysicalDeviceFeatures.occlusionQueryPrecise );
fprintf( FpDebug, "pipelineStatisticsQuery = %d\n", PhysicalDeviceFeatures.pipelineStatisticsQuery );
fprintf( FpDebug, "storageFloat64 = %d\n", PhysicalDeviceFeatures.shaderInt64 );
fprintf( FpDebug, "storageInt16 = %d\n", PhysicalDeviceFeatures.shaderInt16 );
```

mjb – July 24, 2020

Here's What the NVIDIA RTX 2080 Ti Produced 301

```

vkEnumeratePhysicalDevices:
Device 0:
API version: 4198499
Driver version: 4198499
Vendor ID: 0x10de
Device ID: 0xe04
Physical Device Type: 2 = (Discrete GPU)
Device Name: RTX 2080 Ti
Pipeline Cache Size: 206
Device #0 selected ('RTX 2080 Ti')

Physical Device Features:
geometryShader = 1
tessellationShader = 1
multiDrawIndirect = 1
wsiLines = 1
largePoints = 1
multiViewport = 1
occlusionQueryPrecise = 1
pipelineStatisticsQuery = 1
shaderFloat64 = 1
shaderInt64 = 1
shaderInt16 = 1

```

 mjb - July 24, 2020

Here's What the Intel HD Graphics 520 Produced 302

```

vkEnumeratePhysicalDevices:
Device 0:
API version: 4194360
Driver version: 4194360
Vendor ID: 0x0086
Device ID: 0x1916
Physical Device Type: 1 = (Integrated GPU)
Device Name: Intel(R) HD Graphics 520
Pipeline Cache Size: 213
Device #0 selected ('Intel(R) HD Graphics 520')

Physical Device Features:
geometryShader = 1
tessellationShader = 1
multiDrawIndirect = 1
wsiLines = 1
largePoints = 1
multiViewport = 1
occlusionQueryPrecise = 1
pipelineStatisticsQuery = 1
shaderFloat64 = 1
shaderInt64 = 1
shaderInt16 = 1

```

 mjb - July 24, 2020

Asking About the Physical Device's Different Memories 303

```

VkPhysicalDeviceMemoryProperties vdpmp;
vkGetPhysicalDeviceMemoryProperties(PhysicalDevice, OUT &vdpmp);

fprintf(FpDebug, "%hd Memory Types\n", vdpmp.memoryTypeCount);
for(unsigned int i = 0; i < vdpmp.memoryTypeCount; i++)
{
    VkMemoryType vmt = vdpmp.memoryTypes[i];
    if(vmt.propertyFlags & VK_MEMORY_PROPERTY_DEVICE_LOCAL_BIT) != 0) fprintf(FpDebug, "DeviceLocal");
    if(vmt.propertyFlags & VK_MEMORY_PROPERTY_HOST_VISIBLE_BIT) != 0) fprintf(FpDebug, "HostVisible");
    if(vmt.propertyFlags & VK_MEMORY_PROPERTY_HOST_COHERENT_BIT) != 0) fprintf(FpDebug, "HostCoherent");
    if(vmt.propertyFlags & VK_MEMORY_PROPERTY_HOST_CACHED_BIT) != 0) fprintf(FpDebug, "HostCached");
    if(vmt.propertyFlags & VK_MEMORY_PROPERTY_LAZILY_ALLOCATED_BIT) != 0) fprintf(FpDebug, "LazilyAllocated");
    fprintf(FpDebug, "\n");
}

fprintf(FpDebug, "%hd Memory Heaps\n", vdpmp.memoryHeapCount);
for(unsigned int i = 0; i < vdpmp.memoryHeapCount; i++)
{
    fprintf(FpDebug, "Heap %d:\n", i);
    VmMemoryHeap vmh = vdpmp.memoryHeaps[i];
    fprintf(FpDebug, "size = 0x%08x, (unsigned long int)vmh.size");
    if((vmh.flags & VK_MEMORY_HEAP_DEVICE_LOCAL_BIT) != 0) fprintf(FpDebug, " DeviceLocal"); // only one in use
    fprintf(FpDebug, "\n");
}

```

 mjb - July 24, 2020

Here's What I Got 304

```

11 Memory Types:
Memory 0:
Memory 1:
Memory 2:
Memory 3:
Memory 4:
Memory 5:
Memory 6:
Memory 7: DeviceLocal
Memory 8: DeviceLocal
Memory 9: HostVisible HostCoherent
Memory 10: HostVisible HostCoherent HostCached

2 Memory Heaps:
Heap 0: size = 0xb7c00000 DeviceLocal
Heap 1: size = 0xfac00000

```

 mjb - July 24, 2020

Asking About the Physical Device's Queue Families 305

```

uint32_t count = -1;
vkGetPhysicalDeviceQueueFamilyProperties(IN PhysicalDevice, &count, OUT (VkQueueFamilyProperties *)nulptr);
fprintf(FpDebug, "%Found %d Queue Families:\n", count);

VkQueueFamilyProperties *vqfp = new VkQueueFamilyProperties[ count ];
vkGetPhysicalDeviceQueueFamilyProperties(IN PhysicalDevice, &count, OUT vqfp);
for(unsigned int i = 0; i < count; i++)
{
    fprintf(FpDebug, "%d: queueCount = %d, : %d vqfp[i].queueCount\n";
    if(vqfp[i].queueFlags & VK_QUEUE_GRAPHICS_BIT) != 0) fprintf(FpDebug, " Graphics");
    if(vqfp[i].queueFlags & VK_QUEUE_COMPUTE_BIT) != 0) fprintf(FpDebug, " Compute");
    if(vqfp[i].queueFlags & VK_QUEUE_TRANSFER_BIT) != 0) fprintf(FpDebug, " Transfer");
    fprintf(FpDebug, "\n");
}


```

 mjb - July 24, 2020

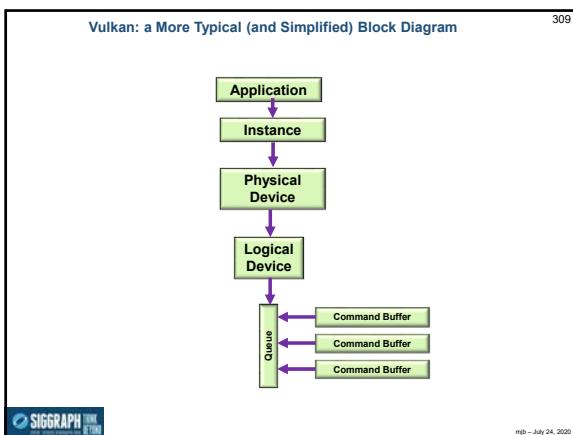
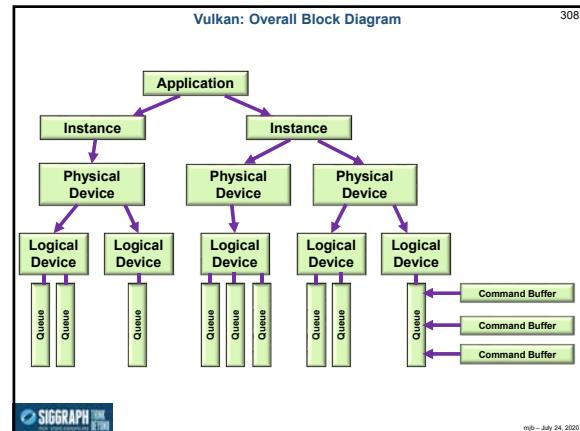
Here's What I Got 306

```

Found 3 Queue Families:
0: queueCount = 16 : Graphics Compute Transfer
1: queueCount = 2 : Transfer
2: queueCount = 8 : Compute

```

 mjb - July 24, 2020



```
Looking to See What Device Layers are Available
```

```
const char * myDeviceLayers[] =
{
    // "VK_LAYER_LUNARG_api_dump",
    // "VK_LAYER_LUNARG_core_validation",
    // "VK_LAYER_LUNARG_image",
    // "VK_LAYER_LUNARG_object_tracker",
    // "VK_LAYER_LUNARG_parameter_validation",
    // "VK_LAYER_NV_optimus"
};

const char * myDeviceExtensions[] =
{
    "VK_KHR_surface",
    "VK_KHR_win32_surface",
    "VK_EXT_debug_report",
    // "VK_KHR_swapchains"
};

// see what device layers are available:

uint32_t layerCount;
vkEnumerateDeviceLayerProperties(PhysicalDevice, &layerCount, (VkLayerProperties *)nullptr);

VkLayerProperties * deviceLayers = new VkLayerProperties[layerCount];

result = vkEnumerateDeviceLayerProperties( PhysicalDevice, &layerCount, deviceLayers);
```

Looking to See What Device Extensions are Available

311

```
// see what device extensions are available;

uint32_t extensionCount;
vkEnumerateDeviceExtensionProperties(PhysicalDevice, deviceLayers[i].layerName,
                                    &extensionCount, (VkExtensionProperties *)nulptr);

VkExtensionProperties * deviceExtensions = new VkExtensionProperties[extensionCount];

result = vkEnumerateDeviceExtensionProperties(PhysicalDevice, deviceLayers[i].layerName,
                                             &extensionCount, deviceExtensions);
```

What Device Layers and Extensions are Available	
4 physical device layers enumerated:	
0x00401063 1 'VK_LAYER_NV_optimus' 'NVIDIA Optimus layer'	0 device extensions enumerated for 'VK_LAYER_NV_optimus'.
0x00401072 1 'VK_LAYER_LUNARG_core_validation' 'LunarG Validation Layer'	2 device extensions enumerated for 'VK_LAYER_LUNARG_core_validation': 0x00000001 'VK_EXT_validation_cache' 0x00000004 'VK_EXT_debug_marker'
0x00401072 1 'VK_LAYER_LUNARG_object_tracker' 'LunarG Validation Layer'	2 device extensions enumerated for 'VK_LAYER_LUNARG_object_tracker': 0x00000001 'VK_EXT_validation_cache' 0x00000004 'VK_EXT_debug_marker'
0x00401072 1 'VK_LAYER_LUNARG_parameter_validation' 'LunarG Validation Layer'	2 device extensions enumerated for 'VK_LAYER_LUNARG_parameter_validation': 0x00000001 'VK_EXT_validation_cache' 0x00000004 'VK_EXT_debug_marker'

Vulkan: Creating a Logical Device 313

```

float queuePriorities[1] = {
    1.
};

VkDeviceQueueCreateInfo vdqci;
vdqci.sType = VK_STRUCTURE_TYPE_DEVICE_QUEUE_CREATE_INFO;
vdqci.pNext = nullptr;
vdqci.flags = 0;
vdqci.queueFamilyIndex = 0;
vdqci.queueCount = 1;
vdqci.pQueueProperties = queuePriorities;

VkDeviceCreateInfo vdcii;
vdcii.sType = VK_STRUCTURE_TYPE_DEVICE_CREATE_INFO;
vdcii.pNext = nullptr;
vdcii.flags = 0;
vdcii.queueCreateInfos = &vdqci; // # of device queues
vdcii.pQueueCreateInfos = &vdqci;
vdcii.enabledLayerCount = sizeof(myDeviceLayers) / sizeof(char);
vdcii.enabledExtensionCount = 0;
vdcii.ppEnabledLayerNames = myDeviceLayers;
vdcii.enabledLayerNames = myDeviceLayers;
vdcii.ppEnabledExtensionNames = (const char**)nullptr; // no extensions
vdcii.enabledExtensionCount = sizeof(myDeviceExtensions) / sizeof(char);
vdcii.ppEnabledExtensionNames = myDeviceExtensions;
vdcii.pEnabledFeatures = IN &PhysicalDeviceFeatures;

```

result = vkCreateLogicalDevice(PhysicalDevice, IN &vdcii, PALLOCATOR, OUT &LogicalDevice);

mjb – July 24, 2020

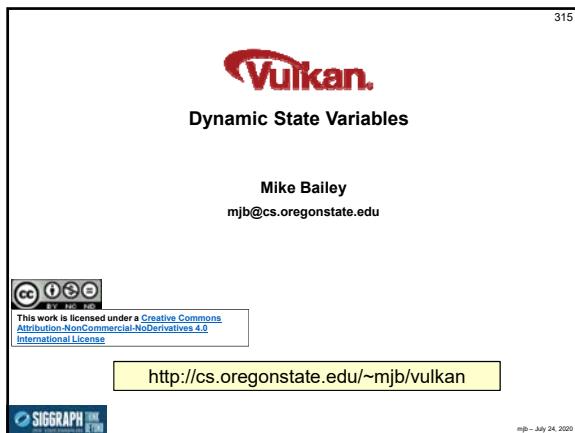
Vulkan: Creating the Logical Device's Queue 314

```

// get the queue for this logical device:
vkGetDeviceQueue( LogicalDevice, 0, 0, OUT &Queue ); // 0, 0 = queueFamilyIndex, queueIndex

```

mjb – July 24, 2020



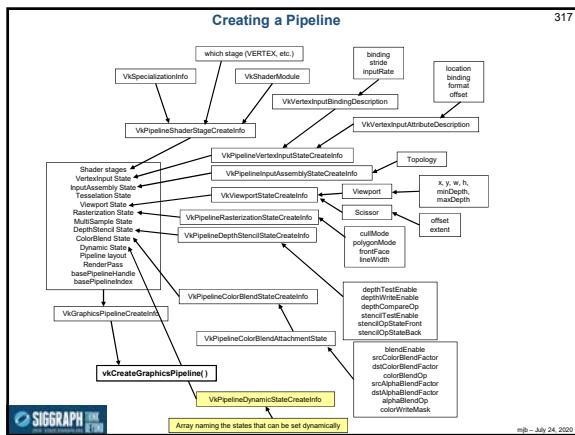
Creating a Pipeline with Dynamically Changeable State Variables 316

The graphics pipeline data structure is full of state information, and, as previously-discussed, is largely immutable, that is, the information contained inside it is fixed, and can only be changed by creating a new graphics pipeline data structure with new information.

That isn't quite true. To a certain extent, Vulkan allows you to declare parts of the pipeline state changeable. This allows you to alter pipeline state information on the fly.

This is useful for managing state information that needs to change frequently. This also creates possible optimization opportunities for the Vulkan driver.

mjb – July 24, 2020



Which Pipeline State Variables can be Changed Dynamically 318

The possible dynamic variables are shown in the `VkDynamicState` enum:

- VK_DYNAMIC_STATE_VIEWPORT
- VK_DYNAMIC_STATE_SCISSOR
- VK_DYNAMIC_STATE_LINE_WIDTH
- VK_DYNAMIC_STATE_DEPTH_BIAS
- VK_DYNAMIC_STATE_BLEND_CONSTANTS
- VK_DYNAMIC_STATE_DEPTH_BOUNDS
- VK_DYNAMIC_STATE_STENCIL_COMPARE_MASK
- VK_DYNAMIC_STATE_STENCIL_WRITE_MASK
- VK_DYNAMIC_STATE_STENCIL_REFERENCE

mjb – July 24, 2020

Creating a Pipeline 319

```

VkDynamicState
{
    VK_DYNAMIC_STATE_VIEWPORT,
    VK_DYNAMIC_STATE_LINE_WIDTH
};

VkPipelineDynamicStateCreateInfo
vpdsi.sType = VK_STRUCTURE_TYPE_PIPELINE_DYNAMIC_STATE_CREATE_INFO;
vpdsi.pNext = nullptr;
vpdsi.flags = 0;
vpdsi.dynamicStateCount = sizeof(vds) / sizeof(VkDynamicState);           // i.e., 2
vpdsi.pDynamicStates = &vds;

VkGraphicsPipelineCreateInfo
...
vgci.pDynamicState = &vpdsi;
...

vkCreateGraphicsPipelines( LogicalDevice, pipelineCache, 1, &vgci, PALLOCATOR, &GraphicsPipeline );

```

If you declare certain state variables to be dynamic like this, then you **must** fill them in the command buffer! Otherwise, they are *undefined*.

mjb – July 24, 2020

Filling the Dynamic State Variables in the Command Buffer 320

First call:
`vkCmdBindPipeline(...);`

Then, the command buffer-bound function calls to set these dynamic states are:

```

vkCmdSetViewport( commandBuffer, firstViewport, viewportCount, pViewports );
vkCmdSetScissor( commandBuffer, firstScissor, scissorCount, pScissors );
vkCmdSetLineWidth( commandBuffer, linewidth );
vkCmdSetDepthBias( commandBuffer, depthBiasConstantFactor, depthBiasClamp, depthBiasSlopeFactor );
vkCmdSetDepthBounds( commandBuffer, minDepthBounds, maxDepthBounds );
vkCmdSetStencilCompareMask( commandBuffer, faceMask, compareMask );
vkCmdSetStencilWriteMask( commandBuffer, faceMask, writeMask );
vkCmdSetStencilReference( commandBuffer, faceMask, reference );

```

mjb – July 24, 2020

Vulkan.

Getting Information Back from the Graphics System

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

Setting up Query Pools 321

- There are 3 types of Queries: Occlusion, Pipeline Statistics, and Timestamp
- Vulkan requires you to first setup "Query Pools", one for each specific type
- This indicates that Vulkan thinks that Queries are time-consuming (relatively) to setup, and thus better to set them up in program-setup than in program-runtime

mjb – July 24, 2020

Setting up Query Pools 323

```

VkQueryPoolCreateInfo
vpci.sType = VK_STRUCTURE_TYPE_QUERY_POOL_CREATE_INFO;
vpci.pNext = nullptr;
vpci.flags = 0;
vpci.queryType = << one of >>;
    VK_QUERY_TYPE_OCCLUSION
    VK_QUERY_TYPE_PIPELINE_STATISTICS
    VK_QUERY_TYPE_TIMESTAMP
vpci.queryCount = 1;
vpci.pipelineStatistics = 0;           // bitmask of what stats you are querying for if you are doing a pipeline statistics query
VK_QUERY_PIPELINE_STATISTIC_INPUT_ASSEMBLY_PRIMITIVES_BIT
VK_QUERY_PIPELINE_STATISTIC_VERTEX_SHADER_INVOCATIONS_BIT
VK_QUERY_PIPELINE_STATISTIC_GEOMETRY_SHADER_INVOCATIONS_BIT
VK_QUERY_PIPELINE_STATISTIC_FRAGMENT_SHADER_PRIMITIVES_BIT
VK_QUERY_PIPELINE_STATISTIC_CLIPPING_PRIMITIVES_BIT
VK_QUERY_PIPELINE_STATISTIC_CONTROL_SHADER_PATCHES_BIT
VK_QUERY_PIPELINE_STATISTIC_TESSELLATION_CONTROL_SHADER_INVOCATIONS_BIT
VK_QUERY_PIPELINE_STATISTIC_COMPUTE_SHADER_INVOCATIONS_BIT

VkQueryPool      occlusionQueryPool;
result = vkCreateQueryPool( LogicalDevice, IN &vpci, PALLOCATOR, OUT &occlusionQueryPool );

VkQueryPool      statisticsQueryPool;
result = vkCreateQueryPool( LogicalDevice, IN &vpci, PALLOCATOR, OUT &statisticsQueryPool );

VkQueryPool      timestampQueryPool;
result = vkCreateQueryPool( LogicalDevice, IN &vpci, PALLOCATOR, OUT &timestampQueryPool );

```

mjb – July 24, 2020

Resetting, Filling, and Examining a Query Pool 324

```

vkCmdResetQueryPool( CommandBuffer, occlusionQueryPool, 0, 1 );
vkCmdBeginQuery( CommandBuffer, occlusionQueryPool, 0, VK_QUERY_CONTROL_PRECISE_BIT );
...
vkCmdEndQuery( CommandBuffer, occlusionQueryPool, 0 );           // query index number

#define DATASIZE 128
uint32_t data[DATASIZE];

result = vkGetQueryPoolResults( LogicalDevice, occlusionQueryPool, 0, 1, DATASIZE*sizeof(uint32_t), data, stride, flags );
// or'd combinations of:
// VK_QUERY_RESULT_64_BIT
// VK_QUERY_RESULT_WAIT_BIT
// VK_QUERY_RESULT_WITH_AVAILABILITY_BIT
// VK_QUERY_RESULT_PARTIAL_BIT
// stride is # of bytes in between each result

```

mjb – July 24, 2020

Occlusion Query 325

Occlusion Queries count the number of fragments drawn between the `vkCmdBeginQuery` and the `vkCmdEndQuery` that pass both the Depth and Stencil tests

This is commonly used to see what level-of-detail should be used when drawing a complicated object

Some hints:

- Don't draw the whole scene – just draw the object(s) you are interested in
- Don't draw the whole object – just draw a simple bounding volume at least as big as the object(s)
- Don't draw the whole bounding volume – cull away the back faces (two reasons: time and correctness)
- Don't draw the colors – just draw the depths (especially if the fragment shader is time-consuming)

```
uint32_t fragmentCount;
result = vkGetQueryPoolResults( LogicalDevice, occlusionQueryPool, 0, 1,
                               sizeof(uint32_t), &fragmentCount, 0, VK_QUERY_RESULT_WAIT_BIT );
```



mjb – July 24, 2020

Pipeline Statistics Query 326

Pipeline Statistics Queries count how many of various things get done between the `vkCmdBeginQuery` and the `vkCmdEndQuery`

```
uint32_t counts[NUM_STATS];
result = vkGetQueryPoolResults( LogicalDevice, statisticsQueryPool, 0, 1,
                               NUM_STATS*sizeof(uint32_t), counts, 0, VK_QUERY_RESULT_WAIT_BIT );

// vpcpi.pipelineStatistics = or'd bits of:
// VK_QUERY_PIPELINE_STATISTIC_INPUT_ASSEMBLY_VERTICES_BIT
// VK_QUERY_PIPELINE_STATISTIC_INPUT_ASSEMBLY_PRIMITIVES_BIT
// VK_QUERY_PIPELINE_STATISTIC_VERTEX_SHADER_INVOCATIONS_BIT
// VK_QUERY_PIPELINE_STATISTIC_GEOMETRY_SHADER_INVOCATIONS_BIT
// VK_QUERY_PIPELINE_STATISTIC_GEOMETRY_SHADER_PRIMITIVES_BIT
// VK_QUERY_PIPELINE_STATISTIC_CLIPPING_INVOCATIONS_BIT
// VK_QUERY_PIPELINE_STATISTIC_CLIPPING_PRIMITIVES_BIT
// VK_QUERY_PIPELINE_STATISTIC_FRAGMENT_SHADER_INVOCATIONS_BIT
// VK_QUERY_PIPELINE_STATISTIC_TESSELLATION_CONTROL_SHADER_PATCHES_BIT
// VK_QUERY_PIPELINE_STATISTIC_TESSELLATION_EVALUATION_SHADER_INVOCATIONS_BIT
// VK_QUERY_PIPELINE_STATISTIC_COMPUTE_SHADER_INVOCATIONS_BIT
```



mjb – July 24, 2020

Timestamp Query 327

Timestamp Queries count how many nanoseconds of time elapsed between the `vkCmdBeginQuery` and the `vkCmdEndQuery`.

```
uint64_t nanosecondsCount;
result = vkGetQueryPoolResults( LogicalDevice, timestampQueryPool, 0, 1,
                               sizeof(uint64_t), &nanosecondsCount, 0,
                               VK_QUERY_RESULT_64_BIT | VK_QUERY_RESULT_WAIT_BIT );
```



mjb – July 24, 2020

Timestamp Query 328

The `vkCmdWriteTimeStamp()` function produces the time between when this function is called and when the first thing reaches the specified pipeline stage.

Even though the stages are "bits", you are supposed to only specify one of them, not "or" multiple ones together

```
vkCmdWriteTimeStamp( CommandBuffer, pipelineStages, timestampQueryPool, 0 );

// VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT
// VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT
// VK_PIPELINE_STAGE_VERTEX_INPUT_BIT
// VK_PIPELINE_STAGE_VERTEX_SHADER_BIT
// VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT
// VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT
// VK_PIPELINE_STAGE_GEOMETRY_SHADER_BIT
// VK_PIPELINE_STAGE_FRAGMENT_TESTS_BIT
// VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT
// VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT
// VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT
// VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT
// VK_PIPELINE_STAGE_TRANSFER_BIT
// VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT
// VK_PIPELINE_STAGE_HOST_BIT
```



mjb – July 24, 2020


Compute Shaders

Mike Bailey
mjb@cs.oregonstate.edu



This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>



mjb – July 24, 2020

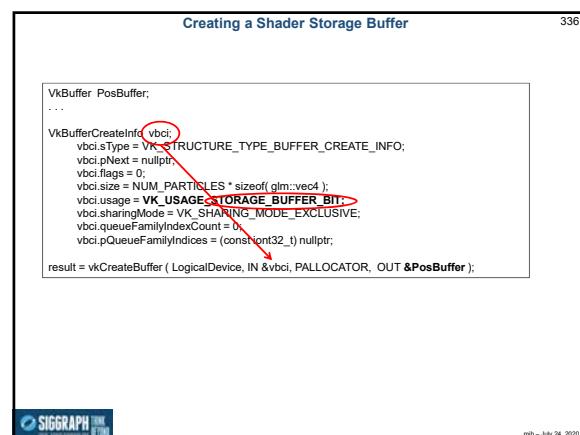
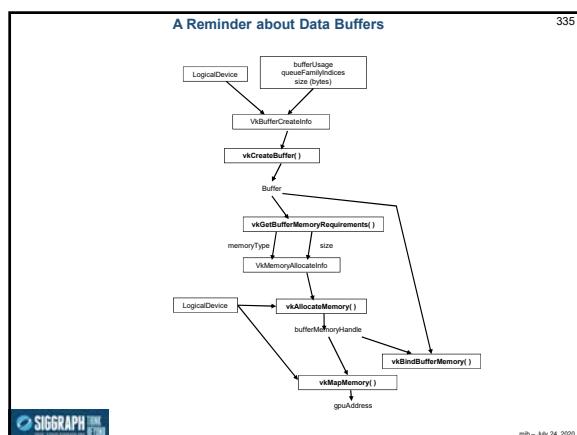
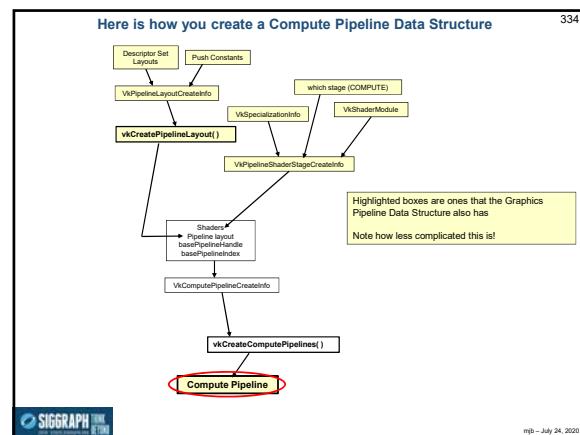
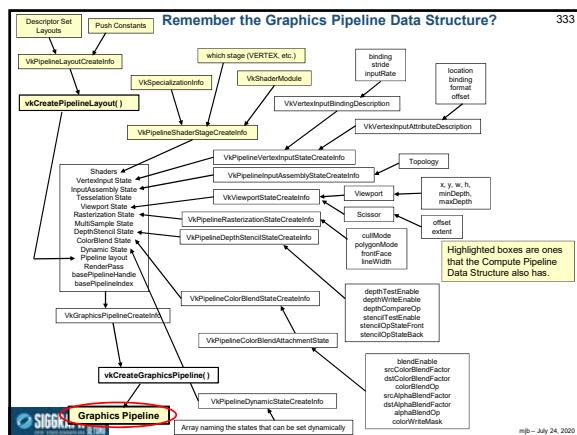
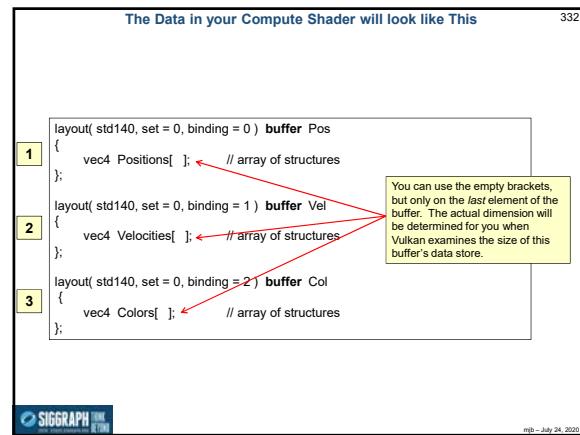
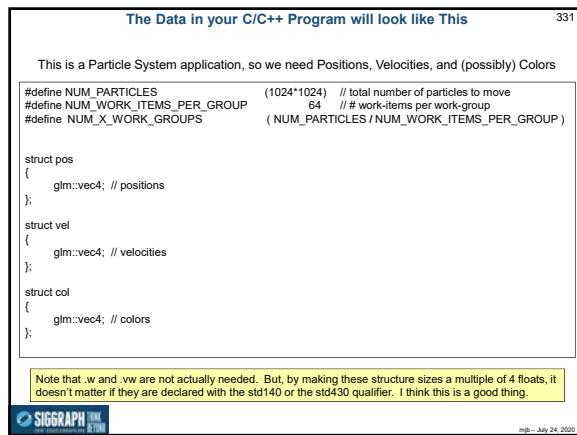
The Example We Are Going to Use Here is a Particle System 330

The Compute Shader Moves the Particles by Recomputing the Position and Velocity Buffers

The Rendering Draws the Particles by Reading the Position and Color Buffers



mjb – July 24, 2020



Allocating Memory for a Buffer, Binding a Buffer to Memory, and Filling the Buffer 337

```

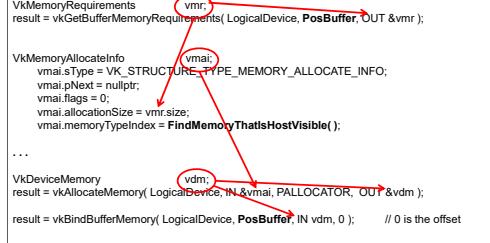
VkMemoryRequirements vmr;
result = vkGetBufferMemoryRequirements( LogicalDevice, PosBuffer, OUT &vmr );

VkMemoryAllocateInfo vmai;
vmai.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
vmai.pNext = nullptr;
vmai.flags = 0;
vmai.allocationSize = vmr.size;
vmai.memoryTypeIndex = FindMemoryThatIsHostVisible();

...

VkDeviceMemory vdm;
result = vkAllocateMemory( LogicalDevice, IN &vmai, PALLOCATOR, OUT &vdm );
result = vkBindBufferMemory( LogicalDevice, PosBuffer, IN vdm, 0 ); // 0 is the offset

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Create the Compute Pipeline Layout 338

```

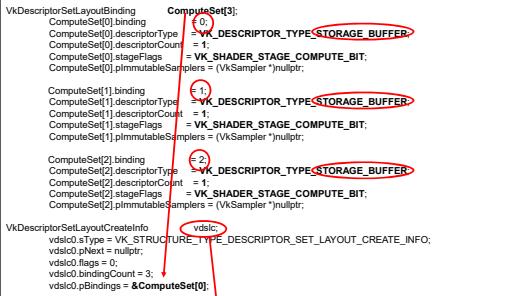
VkDescriptorSetLayoutBinding ComputeSet[3];
ComputeSet[0].binding = 0;
ComputeSet[0].descriptorType = VK_DESCRIPTOR_TYPE_STORAGE_BUFFER;
ComputeSet[0].descriptorCount = 1;
ComputeSet[0].stageFlags = VK_SHADER_STAGE_COMPUTE_BIT;
ComputeSet[0].pImmutableSamplers = (VkSampler *)nullptr;

ComputeSet[1].binding = 1;
ComputeSet[1].descriptorType = VK_DESCRIPTOR_TYPE_STORAGE_BUFFER;
ComputeSet[1].descriptorCount = 1;
ComputeSet[1].stageFlags = VK_SHADER_STAGE_COMPUTE_BIT;
ComputeSet[1].pImmutableSamplers = (VkSampler *)nullptr;

ComputeSet[2].binding = 2;
ComputeSet[2].descriptorType = VK_DESCRIPTOR_TYPE_STORAGE_BUFFER;
ComputeSet[2].descriptorCount = 1;
ComputeSet[2].stageFlags = VK_SHADER_STAGE_COMPUTE_BIT;
ComputeSet[2].pImmutableSamplers = (VkSampler *)nullptr;

VkDescriptorSetLayoutCreateInfo vdsic;
vdsic.sType = VK_STRUCTURE_TYPE_DESCRIPTOR_SET_LAYOUT_CREATE_INFO;
vdsic.pNext = nullptr;
vdsic.flags = 0;
vdsic.bindingCount = 3;
vdsic.pBindings = &ComputeSet[0];

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Create the Compute Pipeline Layout 339

```

VkPipelineLayout ComputePipelineLayout;
VkDescriptorSetLayout ComputeSetLayout;
...
```

```

result = vkCreateDescriptorSetLayout( LogicalDevice, IN &vdsic, PALLOCATOR, OUT &ComputeSet.layout );

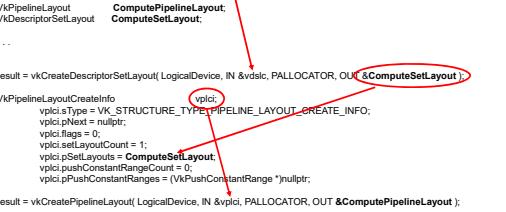
VkPipelineLayoutCreateInfo vplci;
vplci.sType = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
vplci.pNext = nullptr;
vplci.flags = 0;
vplci.setLayoutCount = 1;
vplci.setLayouts = ComputeSetLayout;
vplci.pushConstantRangeCount = 0;
vplci.pushConstantRanges = (VkPushConstantRange *)nullptr;

```

```

result = vkCreatePipelineLayout( LogicalDevice, IN &vplci, PALLOCATOR, OUT &ComputePipelineLayout );

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Create the Compute Pipeline 340

```

VkPipeline ComputePipeline;
...
```

```

VkPipelineShaderStageCreateInfo vpssci;
vpssci.sType = VK_STRUCTURE_TYPE_PIPELINE_SHADER_STAGE_CREATE_INFO;
vpssci.pNext = nullptr;
vpssci.flags = 0;
vpssci.stage = VK_SHADER_STAGE_COMPUTE_BIT;
vpssci.module = computeShader;
vpssci.pName = "main";
vpssci.pSpecializationInfo = (VkSpecializationInfo *)nullptr;

```

```

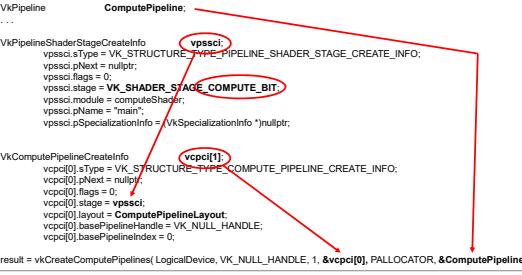
VkComputePipelineCreateInfo vpcpi[1];
vpcpi[0].sType = VK_STRUCTURE_TYPE_COMPUTE_PIPELINE_CREATE_INFO;
vpcpi[0].pNext = nullptr;
vpcpi[0].flags = 0;
vpcpi[0].stage = vpssci;
vpcpi[0].layout = ComputePipelineLayout;
vpcpi[0].basePipelineHandle = VK_NULL_HANDLE;
vpcpi[0].basePipelineIndex = 0;

```

```

result = vkCreateComputePipelines( LogicalDevice, VK_NULL_HANDLE, 1, &vpcpi[0], PALLOCATOR, &ComputePipeline );

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Creating a Vulkan Data Buffer 341

```

VkBuffer Buffer;

```

```

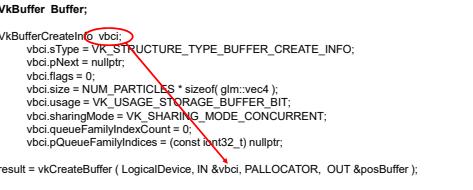
VkBufferCreateInfo vbc;
vbc.sType = VK_STRUCTURE_TYPE_BUFFER_CREATE_INFO;
vbc.pNext = nullptr;
vbc.flags = 0;
vbc.size = NUM_PARTICLES * sizeof(glm::vec4);
vbc.usage = VK_USAGE_STORAGE_BUFFER_BIT;
vbc.sharingMode = VK_SHARING_MODE_CONCURRENT;
vbc.queueFamilyIndexCount = 0;
vbc.queueFamilyIndices = (const int32_t *)nullptr;

```

```

result = vkCreateBuffer( LogicalDevice, IN &vbc, PALLOCATOR, OUT &posBuffer );

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Allocating Memory and Binding the Buffer 342

```

VkMemoryRequirements vmr;
result = vkGetBufferMemoryRequirements( LogicalDevice, posBuffer, OUT &vmr );

```

```

VkMemoryAllocateInfo vmai;
vmai.sType = VK_STRUCTURE_TYPE_MEMORY_ALLOCATE_INFO;
vmai.pNext = nullptr;
vmai.flags = 0;
vmai.allocationSize = vmr.size;
vmai.memoryTypeIndex = FindMemoryThatIsHostVisible();

```

```

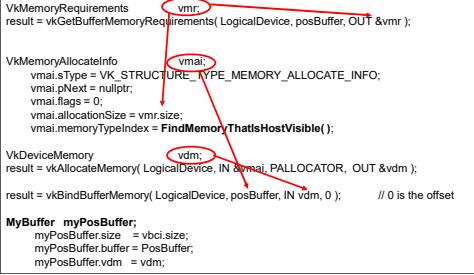
VkDeviceMemory vdm;
result = vkAllocateMemory( LogicalDevice, IN &vmai, PALLOCATOR, OUT &vdm );
result = vkBindBufferMemory( LogicalDevice, posBuffer, IN vdm, 0 ); // 0 is the offset

```

```

MyBuffer myPosBuffer;
myPosBuffer.size = vbc.size;
myPosBuffer.buffer = PosBuffer;
myPosBuffer.vdm = vdm;

```



SIGGRAPH 2019 REPLAY

mjb - July 24, 2020

Fill the Buffers 343

```

struct pos * positions;
vkMapMemory( LogicalDevice, IN myPosBuffer.vdm, 0, VK_WHOLE_SIZE, 0, OUT (void *) &positions );
for( int i = 0; i < NUM_PARTICLES; i++ )
{
    positions[i].x = Ranf( XMIN, XMAX );
    positions[i].y = Ranf( YMIN, YMAX );
    positions[i].z = Ranf( ZMIN, ZMAX );
    positions[i].w = 1.0;
}
vkUnmapMemory( LogicalDevice, IN myPosBuffer.vdm );

struct vel * velocities;
vkMapMemory( LogicalDevice, IN myVelBuffer.vdm, 0, VK_WHOLE_SIZE, 0, OUT (void *) &velocities );
for( int i = 0; i < NUM_PARTICLES; i++ )
{
    velocities[i].x = Ranf( Vxmin, Vxmax );
    velocities[i].y = Ranf( V ymin, Vymax );
    velocities[i].z = Ranf( Vzmin, Vzmax );
    velocities[i].w = 0.0;
}
vkUnmapMemory( LogicalDevice, IN myVelBuffer.vdm );

struct col * colors;
vkMapMemory( LogicalDevice, IN myColBuffer.vdm, 0, VK_WHOLE_SIZE, 0, OUT (void *) &colors );
for( int i = 0; i < NUM_PARTICLES; i++ )
{
    colors[i].r = Ranf( 3f, 1.0 );
    colors[i].g = Ranf( 3f, 1.0 );
    colors[i].b = Ranf( 3f, 1.0 );
    colors[i].a = 1.0;
}
vkUnmapMemory( LogicalDevice, IN myColBuffer.vdm );

```

mjb - July 24, 2020

Fill the Buffers 344

```

#include <stdlib.h>
#define TOP 2147483647.0 // 2^31 - 1

float
Ranf( float low, float high )
{
    long random(); // returns integer 0 - TOP
    float r = (float)random();
    return low + r * (high - low) / (float)RAND_MAX;
}

```

 mjb - July 24, 2020

The Particle System Compute Shader 345

```

layout( std140, set = 0, binding = 0 ) buffer Pos
{
    vec4 Positions[ ];
};

layout( std140, set = 0, binding = 1 ) buffer Vel
{
    vec4 Velocities[ ];
};

layout( std140, set = 0, binding = 2 ) buffer Col
{
    vec4 Colors[ ];
};

layout( local_size_x = 64, local_size_y = 1, local_size_z = 1 ) in;

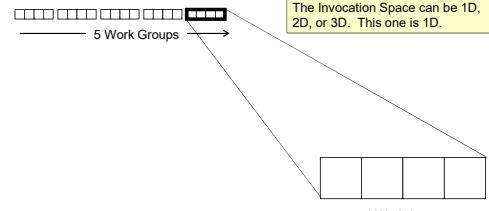
```

This is the number of work-items per work-group, set in the compute shader.
The number of work-groups is set in the `vkCmdDispatch(commandBuffer, workGroupCountX, workGroupCountY, workGroupCountZ);` function call in the application program.

 mjb - July 24, 2020

The Data gets Divided into Large Quantities call Work-Groups, each of which is further Divided into Smaller Units Called Work-Items 346

20 total items to compute:



The Invocation Space can be 1D, 2D, or 3D. This one is 1D.

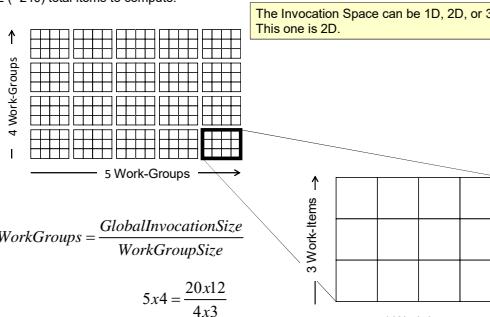
$$\#WorkGroups = \frac{GlobalInvocationSize}{WorkGroupSize}$$

$$5 \times 4 = \frac{20}{4}$$

 mjb - July 24, 2020

The Data Needs to be Divided into Large Quantities call Work-Groups, each of which is further Divided into Smaller Units Called Work-Items 347

20x12 (=240) total items to compute:

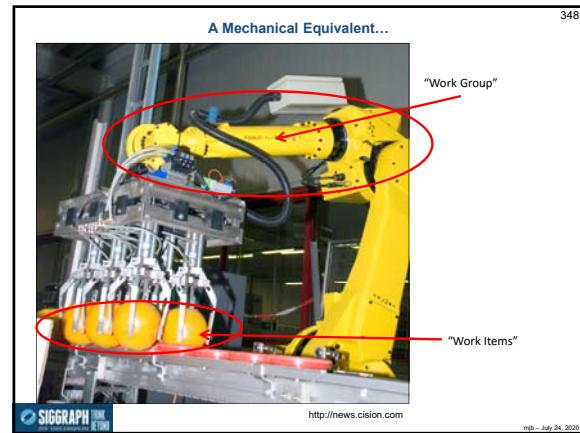


The Invocation Space can be 1D, 2D, or 3D. This one is 2D.

$$\#WorkGroups = \frac{GlobalInvocationSize}{WorkGroupSize}$$

$$5 \times 4 = \frac{20 \times 12}{4 \times 3}$$

 mjb - July 24, 2020



The Particle System Compute Shader – The Physics 349

```
#define POINT vec3
#define VELOCITY vec3
#define VECTOR vec3
#define SPHERE vec4 // xc, yc, zc, r
#define PLANE vec4 // a, b, c, d

const VECTOR G = VECTOR(0., -9.8, 0.);
const float DT = 0.1;

const SPHERE Sphere = vec4( -100., -800., 0., 600. ); // x, y, z, r

...
uint gid = gl_GlobalInvocationID.x; // where I am in the global dataset (6 in this example)
// (as a 1d problem, the y and z are both 1)

POINT p = Positions[ gid ].xyz;
VELOCITY v = Velocities[ gid ].xyz;
POINT pp = p + v * DT + .5 * DT * DT * G;
VELOCITY vp = v + G * DT;

Positions[ gid ].xyz = pp;
Velocities[ gid ].xyz = vp;
```

Diagram: A grid of 12x12 squares representing a global dataset. The 7th column from the left is highlighted in red, indicating the current global invocation ID.

Equation:

$$p' = p + v \cdot t + \frac{1}{2} G \cdot t^2$$

$$v' = v + G \cdot t$$

The Particle System Compute Shader – How About Introducing a Bounce? 350

```
VELOCITY
Bounce( VELOCITY vin, VECTOR n )
{
    VELOCITY vout = reflect( vin, n );
    return vout;
}

// plane equation: Ax + By + Cz + D = 0
// it turns out that (A,B,C) is the normal

VELOCITY
BouncePlane( POINT p, VELOCITY v, PLANE pl )
{
    VECTOR n = normalize( VECTOR( pl.x, pl.y, pl.z ) );
    return Bounce( v, n );
}

bool
IsUnderPlane( POINT p, PLANE pl )
{
    float r = pl.x * p.x + pl.y * p.y + pl.z * p.z + pl.w;
    return ( r < 0. );
}
```

Diagram: A diagram showing a yellow plane with a normal vector n pointing upwards. An arrow labeled "in" enters the plane from the left, and an arrow labeled "out" exits from the right.

Note: A surface in the x-z plane has the equation: $0x + 1y + 0z + 0 = 0$ and thus its normal vector is $(0, 1, 0)$.

The Particle System Compute Shader – How About Introducing a Bounce? 351

```
VELOCITY
BounceSphere( POINT p, VELOCITY v, SPHERE s )
{
    VECTOR n = normalize( p - s.xyz );
    return Bounce( v, n );
}

bool
IsInsideSphere( POINT p, SPHERE s )
{
    float r = length( p - s.xyz );
    return ( r < s.w );
}
```

Diagram: A yellow sphere with a normal vector n pointing upwards. An arrow labeled "in" enters the sphere from the left, and an arrow labeled "out" exits from the right.

The Particle System Compute Shader – How About Introducing a Bounce? 352

```
uint gid = gl_GlobalInvocationID.x; // the .y and .z are both 1 in this case
POINT p = Positions[ gid ].xyz;
VELOCITY v = Velocities[ gid ].xyz;
POINT pp = p + v * DT + .5 * DT * DT * G;
VELOCITY vp = v + G * DT;

if( IsInsideSphere( p, Sphere ) )
{
    vp = BounceSphere( p, Sphere );
    pp = p + vp * DT + .5 * DT * DT * G;
}

Positions[ gid ].xyz = pp;
Velocities[ gid ].xyz = vp;
```

Graphics Trick Alert: Making the bounce happen from the surface of the sphere is time-consuming. Instead, bounce from the previous position in space. If DT is small enough (and it is), nobody will ever know...

Dispatching the Compute Shader from the Command Buffer 353

```
#define NUM_PARTICLES (1024 * 1024)
#define NUM_WORK_ITEMS_PER_GROUP 64
#define NUM_X_WORK_GROUPS (NUM_PARTICLES / NUM_WORK_ITEMS_PER_GROUP)
...

vkCmdBindPipeline( CommandBuffer, VK_PIPELINE_BIND_POINT_COMPUTE, ComputePipeline );
vkCmdDispatch( CommandBuffer, NUM_X_WORK_GROUPS, 1, 1 );

This is the number of work-groups, set in the application program.
The number of work-items per work-group is set in the layout in the compute shader:
layout( local_size_x = 64, local_size_y = 1, local_size_z = 1 ) in;
```

Displaying the Particles 354

```
VkVertexInputBindingDescription vvibd[3]; // one of these per buffer data buffer
vvibd[0].binding = 0; // which binding # this is
vvibd[0].stride = sizeof( struct pos ); // bytes between successive structs
vvibd[0].inputRate = VK_VERTEX_INPUT_RATE_VERTEX;

vvibd[1].binding = 1;
vvibd[1].stride = sizeof( struct vel );
vvibd[1].inputRate = VK_VERTEX_INPUT_RATE_VERTEX;

vvibd[2].binding = 2;
vvibd[2].stride = sizeof( struct col );
vvibd[2].inputRate = VK_VERTEX_INPUT_RATE_VERTEX;
```

```
layout( location = 0 ) in vec4 aPosition;
layout( location = 1 ) in vec4 aVelocity;
layout( location = 2 ) in vec4 aColor;
```

Displaying the Particles 355

```

VkVertexInputAttributeDescription vviad[3]; // array per vertex input attribute
    /3 = position, velocity, color
    vviad[0].location = 0; // location in the layout decoration
    vviad[0].binding = 0; // which binding description this is part of
    vviad[0].format = VK_FORMAT_VEC4; // x, y, z, w
    vviad[0].offset = offsetof( struct pos, pos ); // 0

    vviad[1].location = 1;
    vviad[1].binding = 0;
    vviad[1].format = VK_FORMAT_VEC4; // nx, ny, nz
    vviad[1].offset = offsetof( struct vel, vel ); // 0

    vviad[2].location = 2;
    vviad[2].binding = 0;
    vviad[2].format = VK_FORMAT_VEC4; // r, g, b, a
    vviad[2].offset = offsetof( struct col, col ); // 0

```

mjb – July 24, 2020

Telling the Pipeline about its Input 356

```

VkPipelineVertexInputStateCreateInfo vpvisci; // used to describe the input vertex attributes
vpvisci.sType = VK_STRUCTURE_TYPE_PIPELINE_VERTEX_INPUT_STATE_CREATE_INFO;
vpvisci.pNext = nullptr;
vpvisci.flags = 0;
vpvisci.vertexBindingDescriptionCount = 3;
vpvisci.pVertexBindingDescriptions = vvlbd;
vpvisci.vertexAttributeDescriptionCount = 3;
vpvisci.pVertexAttributeDescriptions = vvlad;

VkPipelineInputAssemblyStateCreateInfo vpisci;
vpisci.sType = VK_STRUCTURE_TYPE_PIPELINE_INPUT_ASSEMBLY_STATE_CREATE_INFO;
vpisci.pNext = nullptr;
vpisci.flags = 0;
vpisci.topology = VK_PRIMITIVE_TOPOLOGY_POINT_LIST;

```

mjb – July 24, 2020

Telling the Pipeline about its Input 357

We will come to the Pipeline later, but for now, know that a Vulkan Pipeline is essentially a very large data structure that holds (what OpenGL would call) the state, including how to parse its vertex input.

```

VkGraphicsPipelineCreateInfo vgpci;
vgpci.sType = VK_STRUCTURE_TYPE_GRAPHICS_PIPELINE_CREATE_INFO;
vgpci.pNext = nullptr;
vgpci.flags = 0;
vgpci.stageCount = 2; // number of shader stages in this pipeline
vgpci.pStages = vpssci;
vgpci.pVertexInputState = &vpvisci;
vgpci.pInputAssemblyState = &vpisci;
vgpci.pTessellationState = (VkPipelineTessellationStateCreateInfo *)nullptr; // &vtscsi
vgpci.pViewportState = &vpsci;
vgpci.pRasterizationState = &vprsc;
vgpci.pMultisampleState = &vpmcs;
vgpci.pDepthStencilState = &vpdscsi;
vgpci.pColorBlendState = &vpccbsci;
vgpci.pDynamicState = &vpdsc;
vgpci.layout = IN Graphics::pipelineLayout;
vgpci.renderPass = IN RenderPass;
vgpci.subpass = 0; // subpass number
vgpci.basePipelineHandle = (VkPipeline)VK_NULL_HANDLE;
vgpci.basePipelineIndex = 0;

result = vkCreateGraphicsPipelines( LogicalDevice, VK_NULL_HANDLE, 1, IN &vgpci,
PALLOCATION, OUT &GraphicsPipeline );

```

mjb – July 24, 2020

Setting a Pipeline Barrier so the Drawing Waits for the Compute 358

```

VkBufferMemoryBarrier vbmb;
vbmb.sType = VK_STRUCTURE_TYPE_BUFFER_MEMORY_BARRIER;
vbmb.pNext = nullptr;
vbmb.srcAccessFlags = VK_ACCESS_SHADER_WRITE_BIT;
vbmb.dstAccessFlags = VK_ACCESS_VERTEX_ATTRIBUTE_READ_BIT;
vbmb.srcQueueFamilyIndex = 0;
vbmb.dstQueueFamilyIndex = 0;
vbmb.buffer =
vbmb.offset = 0;
vbmb.size = NUM_PARTICLES * sizeof( glm::vec4 );

const uint32_t bufferMemoryBarrierCount = 1;
vkCmdPipelineBarrier(
(
    commandBuffer,
    VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT, VK_PIPELINE_STAGE_VERTEX_INPUT_BIT,
    VK_DEPENDENCY_BY_REGION_BIT, 0, nullptr, bufferMemoryBarrierCount
    IN &vbmb, 0, nullptr
);

```

mjb – July 24, 2020

Drawing 359

```

VkBuffer buffers[ ] = MyPosBuffer.buffer, MyVelBuffer.buffer, MyColBuffer.buffer;
size_t offsets[ ] = { 0, 0, 0 };

vkCmdBindVertexBuffers( CommandBuffers[nextImageIndex], 0, 3, buffers, offsets );

const uint32_t vertexCount = NUM_PARTICLES;
const uint32_t instanceCount = 1;
const uint32_t firstVertex = 0;
const uint32_t firstInstance = 0;

vkCmdDraw( CommandBuffers[nextImageIndex], NUM_PARTICLES, 1, 0, 0 );
// vertexCount, instanceCount, firstVertex, firstInstance

```

mjb – July 24, 2020

Setting a Pipeline Barrier so the Compute Waits for the Drawing 360

```

VkBufferMemoryBarrier vbmb;
vbmb.sType = VK_STRUCTURE_TYPE_BUFFER_MEMORY_BARRIER;
vbmb.pNext = nullptr;
vbmb.srcAccessFlags = 0;
vbmb.dstAccessFlags = VK_ACCESS_UNIFORM_READ_BIT;
vbmb.srcQueueFamilyIndex = 0;
vbmb.dstQueueFamilyIndex = 0;
vbmb.buffer =
vbmb.offset = 0;
vbmb.size = ???

const uint32_t bufferMemoryBarrierCount = 1;
vkCmdPipelineBarrier(
(
    commandBuffer,
    VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT, VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT,
    VK_DEPENDENCY_BY_REGION_BIT, 0, nullptr, bufferMemoryBarrierCount
    IN &vbmb, 0, nullptr
);

```

mjb – July 24, 2020

Vulkan.

Specialization Constants

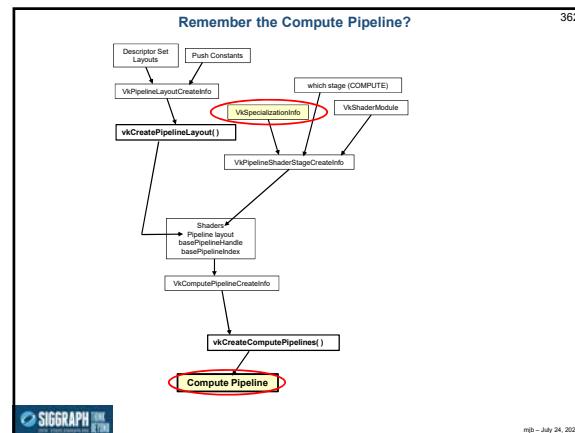
Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

SIGGRAPH 2019

mjb – July 24, 2020



What Are Specialization Constants?

In Vulkan, all shaders get halfway-compiled into SPIR-V and then the rest-of-the-way compiled by the Vulkan driver.

Normally, the half-way compile finalizes all constant values and compiles the code that uses them.

But, it would be nice every so often to have your Vulkan program sneak into the halfway-compiled binary and manipulate some constants at runtime. This is what Specialization Constants are for. A Specialization Constant is a way of injecting an integer, Boolean, uint, float, or double constant into a *halfway-compiled* version of a shader right before the *rest-of-the-way* compilation.

That final compilation happens when you call `vkCreateComputePipelines()`.

Without Specialization Constants, you would have to commit to a final value before the SPIR-V compile was done, which could have been a long time ago.

Shader Source → SPIR-V Compile → .spv File → Pipeline Shader Stage → Final Compile

Specialization Constants

SIGGRAPH 2019

mjb – July 24, 2020

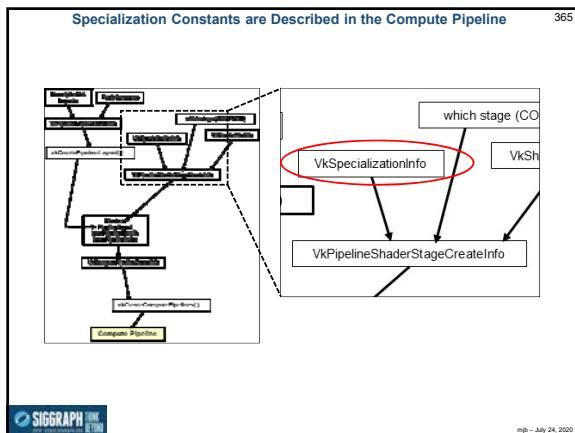
Why Do We Need Specialization Constants?

Specialization Constants could be used for:

- Setting the work-items per work-group in a compute shader
- Setting a Boolean flag and then eliminating the if-test that used it
- Setting an integer constant and then eliminating the switch-statement that looked for it
- Making a decision to unroll a for-loop because the number of passes through it are small enough
- Collapsing arithmetic expressions into a single value
- Collapsing trivial simplifications, such as adding zero or multiplying by 1

SIGGRAPH 2019

mjb – July 24, 2020



Specialization Constant Example -- Setting an Array Size

In the compute shader:

```
layout( constant_id = 7 ) const int ASIZE = 32;
```

In the Vulkan C/C++ program:

```
int asize = 64;
VkSpecializationMapEntry vsme[1];
vsme[0].constantID = 7;
vsme[0].offset = 0;
vsme[0].size = sizeof(asize);
// # bytes into the Specialization Constant
// array this one item is
// size of just this Specialization Constant

VkSpecializationInfo vsi;
vsi.mapEntryCount = 1;
vsi.pMapEntries = &vsme[0];
vsi.dataSize = sizeof(asize);
vsi.pData = &asize;
// size of all the Specialization Constants together
// array of all the Specialization Constants
```

vsme[1]; // one array element for each Specialization Constant

SIGGRAPH 2019

mjb – July 24, 2020

Linking the Specialization Constants into the Compute Pipeline 367

```

int asize = 64;
VkSpecializationMapEntry vsme[1];
vsme[0].constantID = 7;
vsme[0].offset = 0;
vsme[0].size = sizeof(asize);

VkSpecializationInfo vsi;
vsi.mapEntryCount = 1;
vsi.pMapEntries = &vsme[0];
vsi.dataSize = sizeof(asize);
vsi.pData = &asize;

VkPipelineShaderStageCreateInfo vpssci;
vpssci.sType = VK_STRUCTURE_TYPE_PIPELINE_SHADER_STAGE_CREATE_INFO;
vpssci.pNext = NULL;
vpssci.flags = 0;
vpssci.stage = VK_SHADER_STAGE_COMPUTE_BIT;
vpssci.module = computeShader;
vpssci.pName = "main";
vpssci.pSpecializationInfo = &vsi;

VkComputePipelineCreateInfo vcpcl[1];
vcpcl[0].sType = VK_STRUCTURE_TYPE_COMPUTE_PIPELINE_CREATE_INFO;
vcpcl[0].pNext = NULL;
vcpcl[0].flags = 0;
vcpcl[0].basePipeline = vpsc;
vcpcl[0].layout = ComputePipeline.layout;
vcpcl[0].basePipelineHandle = VK_NULL_HANDLE;
vcpcl[0].basePipelineIndex = 0;

result = vkCreateComputePipelines(LogicalDevice, VK_NULL_HANDLE, 1, &vcpcl[0], PALLOCATOR, OUT &ComputePipeline);
    
```

Specialization Constant Example – Setting Multiple Constants 368

In the compute shader:

```

layout( constant_id = 9 ) const int a = 1;
layout( constant_id = 10 ) const int b = 2;
layout( constant_id = 11 ) const float c = 3.14;
    
```

In the C/C++ program:

```

struct abc { int a, int b, float c; } abc;
VkSpecializationMapEntry vsme[3];
vsme[0].constantID = 9;
vsme[0].offset = offsetof(abc, a);
vsme[0].size = sizeof(abc.a);
vsme[1].constantID = 10;
vsme[1].offset = offsetof(abc, b);
vsme[1].size = sizeof(abc.b);
vsme[2].constantID = 11;
vsme[2].offset = offsetof(abc, c);
vsme[2].size = sizeof(abc.c);

VkSpecializationInfo vsi;
vsi.mapEntryCount = 3;
vsi.pMapEntries = &vsme[0];
vsi.dataSize = sizeoff(abc);
vsi.pData = &abc;
    
```

It's important to use `sizeof()` and `offsetof()` instead of hardcoding numbers!

// size of all the Specialization Constants together
// array of all the Specialization Constants

Specialization Constants – Setting the Number of Work-items Per Work-Group in the Compute Shader 369

In the compute shader:

```

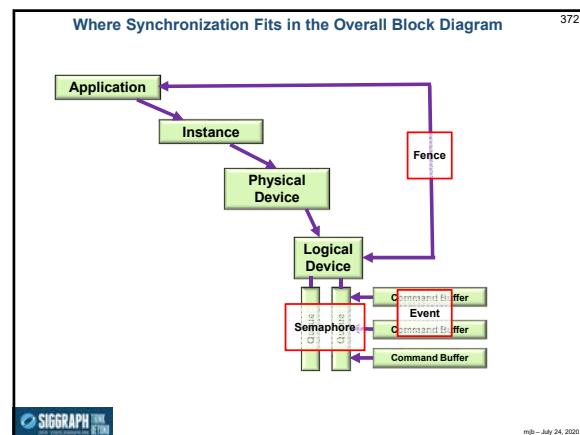
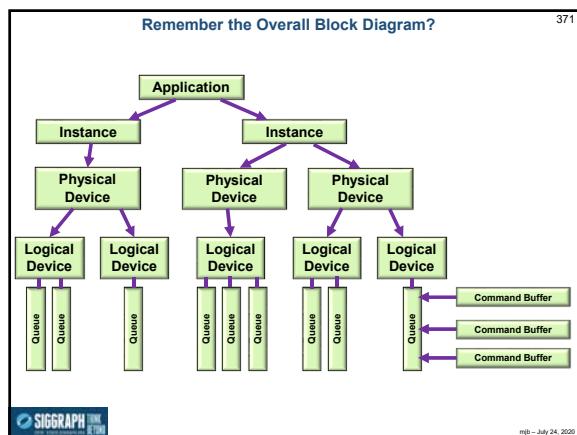
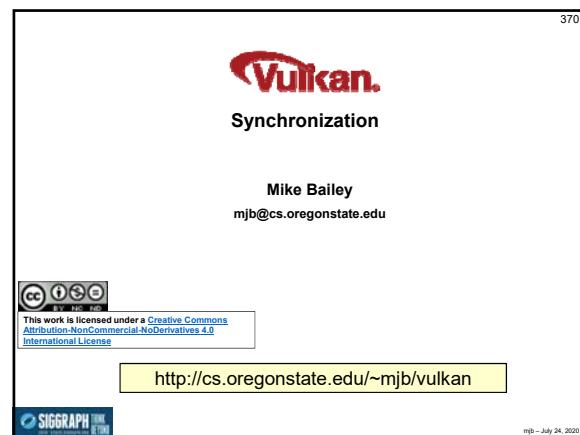
layout( local_size_x_id=12 ) in;
layout( local_size_x = 32, local_size_y = 1, local_size_z = 1 ) in;
    
```

In the C/C++ program:

```

int numXworkItems = 64;
VkSpecializationMapEntry vsme[1];
vsme[0].constantID = 12;
vsme[0].offset = 0;
vsme[0].size = sizeof(int);

VkSpecializationInfo vsi;
vsi.mapEntryCount = 1;
vsi.pMapEntries = &vsme[0];
vsi.dataSize = sizeof(int);
vsi.pData = &numXworkItems;
    
```



Semaphores 373

- Used to synchronize work executing on different queues within the same logical device
- You create them, and give them to a Vulkan function which sets them. Later on, you tell a Vulkan function to wait on this particular semaphore
- You don't end up setting, resetting, or checking the semaphore yourself
- Semaphores must be initialized ("created") before they can be used

mjb - July 24, 2020

Creating a Semaphore 374

```
VkSemaphoreCreateInfo vsci;
vsci.sType = VK_STRUCTURE_TYPE_SEMAPHORE_CREATE_INFO;
vsci.pNext = nullptr;
vsci.flags = 0;

VkSemaphore semaphore;
result = vkCreateSemaphore(LogicalDevice, IN &vsci, PALLOCATOR, OUT &semaphore);
```

This doesn't actually do anything with the semaphore – it just sets it up

mjb - July 24, 2020

Semaphores Example during the Render Loop 375

```
VkSemaphoreCreateInfo vsci;
vsci.sType = VK_STRUCTURE_TYPE_SEMAPHORE_CREATE_INFO;
vsci.pNext = nullptr;
vsci.flags = 0;

result = vkCreateSemaphore(LogicalDevice, IN &vsci, PALLOCATOR, OUT &imageReadySemaphore);

uint32_t nextImageIndex;
vkAcquireNextImageKHR(LogicalDevice, IN SwapChain, IN UINT64_MAX,
                     IN imageReadySemaphore, IN VK_NULL_HANDLE, OUT &nextImageIndex);
Set the semaphore
```

...

```
VkPipelineStageFlags waitAtBottom = VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT;
VkSubmitInfo vsi;
vsi.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vsi.pNext = nullptr;
vsi.waitSemaphoreCount = 1;
vsi.pWaitSemaphores = &imageReadySemaphore;
vsi.pWaitDstStageMask = &waitAtBottom;
Wait on the semaphore
```

You do this to wait for an image to be ready to be rendered into

mjb - July 24, 2020

Fences 376

- Used when the host needs to wait for the device to complete something big
- Used to synchronize the application with commands submitted to a queue
- Announces that queue-submitted work is finished
- Much finer control than semaphores
- You can un-signal, signal, test or block-while-waiting

mjb - July 24, 2020

Fences 377

```
#define VK_FENCE_CREATE_UNSIGNALED_BIT 0

VkFenceCreateInfo vfc;
vfc.sType = VK_STRUCTURE_TYPE_FENCE_CREATE_INFO;
vfc.pNext = nullptr;
vfc.flags = VK_FENCE_CREATE_UNSIGNALED_BIT; // only other option

VkFence fence;
fence = vkCreateFence(LogicalDevice, IN &vfc, PALLOCATOR, OUT &fence);
Set the fence
```

...

```
// returns to the host right away.
result = vkGetFenceStatus(LogicalDevice, IN fence)
// result = VK_SUCCESS means it has signaled
// result = VK_NOT_READY means it has not signaled
```

Wait on the fence(s)

```
// blocks the host from executing:
result = vkWaitForFences(LogicalDevice, IN &fence, waitForAll, timeout);
// waitForAll = VK_TRUE: wait for all fences in the list
// timeout is a uint64_t timeout in nanoseconds (could be 0, which means to return immediately)
// timeout can be up to UINT64_MAX = 0xffffffffffff (= 580+ years)
// result = VK_SUCCESS means it returned because a fence (or all fences) signaled
// result = VK_TIMEOUT means it returned because the timeout was exceeded
```

mjb - July 24, 2020

Fence Example 378

```
VkFence renderFence;
vkCreateFence(LogicalDevice, &vfc, PALLOCATOR, OUT &renderFence);

VkPipelineStageFlags waitAtBottom = VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT;

VkQueue presentQueue;
vkGetDeviceQueue(LogicalDevice, FindQueueFamilyThatDoesGraphics(), 0, OUT &presentQueue);

VkSubmitInfo vsi;
vsi.sType = VK_STRUCTURE_TYPE_SUBMIT_INFO;
vsi.pNext = nullptr;
vsi.waitSemaphoreCount = 1;
vsi.pWaitSemaphores = &imageReadySemaphore;
vsi.pWaitDstStageMask = &waitAtBottom;
vsi.commandBufferCount = 1;
vsi.pCommandBuffers = &CommandBuffers[nextImageIndex];
vsi.signalSemaphoreCount = 0;
vsi.pSignalSemaphores = (VkSemaphore) nullptr;
```

```
result = vkQueueSubmit(presentQueue, 1, IN &vsi, IN renderFence)
...
```

```
result = vkWaitForFences(LogicalDevice, 1, IN &renderFence, VK_TRUE, UINT64_MAX);
...
```

```
result = vkQueuePresentKHR(presentQueue, IN &vpi);
```

mjb - July 24, 2020

Events 379

- Events provide even finer-grained synchronization
- Events are a primitive that can be signaled by the host or the device
- Can even signal at one place in the pipeline and wait for it at another place in the pipeline
- Signaling in the pipeline means "signal me as the last piece of this draw command passes that point in the pipeline".
- You can signal, un-signal, or test from a vk function or from a vkCmd function
- Can wait from a vkCmd function

mjb – July 24, 2020

Controlling Events from the Host 380

```

VkEventCreateInfo
{
    veci.sType = VK_STRUCTURE_TYPE_EVENT_CREATE_INFO;
    veci.pNext = nullptr;
    veci.flags = 0;
}

vkEvent event;
result = vkCreateEvent( LogicalDevice, IN &veci, PALLOCATOR, OUT &event );

result = vkSetEvent( LogicalDevice, IN event );
result = vkResetEvent( LogicalDevice, IN event );
result = vkGetEventStatus( LogicalDevice, IN event );
// result = VK_EVENT_SET: signaled
// result = VK_EVENT_RESET: not signaled

```

Note: the host cannot *block* waiting for an event, but it can test for it

mjb – July 24, 2020

Controlling Events from the Device 381

```

result = vkCmdSetEvent( CommandBuffer, IN event, pipelineStageBits );
result = vkCmdResetEvent( CommandBuffer, IN event, pipelineStageBits );
result = vkCmdWaitEvents( CommandBuffer, 1, &event, // Could be an array of events
                        srcPipelineStageBits, dstPipelineStageBits, // Where signaled, where wait for the signal
                        memoryBarrierCount, pMemoryBarriers,
                        bufferMemoryBarrierCount, pBufferMemoryBarriers,
                        imageMemoryBarrierCount, pImageMemoryBarriers );

```

Memory barriers get executed after events have been signaled

Note: the device cannot *test* for an event, but it can *block*

mjb – July 24, 2020

Vulkan.
Pipeline Barriers

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License

<http://cs.oregonstate.edu/~mjb/vulkan>

mjb – July 24, 2020

From the Command Buffer Notes:
These are the Commands that can be entered into the Command Buffer, I

```

vkCmdBeginQuery( commandBuffer, flags );
vkCmdBeginRenderPass( commandBuffer, const contents );
vkCmdBindDescriptorSets( commandBuffer, pDynamicOffsets );
vkCmdBindIndexBuffer( commandBuffer, indexType );
vkCmdBindVertexBuffers( commandBuffer, firstBinding, bindingCount, const pOffsets );
vkCmdBindImage( commandBuffer, filter );
vkCmdBindImageLayout( commandBuffer, attachmentCount, const pRects );
vkCmdClearColorImage( commandBuffer, pRanges );
vkCmdClearDepthStencilImage( commandBuffer, pRanges );
vkCmdCopyImage( commandBuffer, pRegions );
vkCmdCopyBufferToImage( commandBuffer, pRegions );
vkCmdCopyImage( commandBuffer, pRegions );
vkCmdCopyImage( commandBuffer, pRegions );
vkCmdCopyQueryPoolResult( commandBuffer, flags );
vkCmdDebugMarkerBeginEXT( commandBuffer, pMarkerInfo );
vkCmdDebugMarkerEndEXT( commandBuffer, pMarkerInfo );
vkCmdDebugMarkerInsertEXT( commandBuffer, pMarkerInfo );
vkCmdDispatch( commandBuffer, groupCountX, groupCountY, groupCountZ );
vkCmdDraw( commandBuffer, vertexCount, instanceCount, firstVertex, firstInstance );
vkCmdDrawIndexed( commandBuffer, indexCount, instanceCount, firstIndex, int32_1, vertexOffset, firstInstance );
vkCmdDrawIndexedIndirect( commandBuffer, stride );
vkCmdDrawIndexedIndirect( commandBuffer, stride );
vkCmdDrawIndirect( commandBuffer, stride );
vkCmdDrawIndirect( commandBuffer, stride );
vkCmdDrawQuery( commandBuffer, query );
vkCmdEndRenderPass( commandBuffer );
vkCmdExecuteCommands( commandBuffer, commandBufferCount, const pCommandBuffers );

```

mjb – July 24, 2020

From the Command Buffer Notes:
These are the Commands that can be entered into the Command Buffer, II

```

vkCmdFlush( commandBuffer, dstBuffer, dstOffset, size, data );
vkCmdPipelineBarrier( commandBuffer, srcStageMask, dstStageMask, dependencyFlags, memoryBarrierCount, VkMemoryBarrier* pMemoryBarriers,
                     bufferMemoryBarrierCount, pBufferMemoryBarriers, imageMemoryBarrierCount, pImageMemoryBarriers );
vkCmdPushDescriptorSetKHR( commandBuffer, pPipelineBindPoint, layout, set, descriptorWriteCount, pDescriptorWrites );
vkCmdPushDescriptorSetWithTemplate( commandBuffer, pDescriptorTemplate, layout, set, pData );
vkCmdResolveImageForCommandsNVX( commandBuffer, pImage, pImageSize );
vkCmdResetEvent( commandBuffer, event, stageMask );
vkCmdResetQueryPool( commandBuffer, firstQuery, queryCount );
vkCmdResetQueryPool( commandBuffer, srcImageLayout, dstImageLayout, regionCount, pRegions );
vkCmdResolveImage( commandBuffer, srcImageLayout, dstImageLayout, regionCount, pRegions );
vkCmdBlitImage( commandBuffer, blendConstant[4] );
vkCmdBlitImage( commandBuffer, depthBiasFactor, depthBiasSlopeFactor );
vkCmdDepthBias( commandBuffer, minDepthBounds, maxDepthBounds );
vkCmdSetDeviceMaskKH(X( commandBuffer, deviceMask );
vkCmdDiscardRectanglesEXT( commandBuffer, firstDiscardRectangle, discardRectangleCount, pDiscardRectangles );
vkCmdSetScissor( commandBuffer, firstScissor, scissorCount, pScissors );
vkCmdSetScissor( commandBuffer, firstScissor, scissorCount, pScissors );
vkCmdSetStencilReference( commandBuffer, faceMask, reference );
vkCmdSetStencilWriteMask( commandBuffer, faceMask, writeMask );
vkCmdSetViewport( commandBuffer, firstViewport, viewportCount, pViewports );
vkCmdSetViewportWScalingNV( commandBuffer, firstViewport, viewportCount, pViewportWScalings );
vkCmdUpdateBuffer( commandBuffer, dstBuffer, dstOffset, dataSize, pData );
vkCmdWriteImage( commandBuffer, dstImage, dstImageLayout, dstImageSubresourceRange, dstImageAspectMask, memoryBarrierCount, pMemoryBarriers,
                 bufferMemoryBarrierCount, pBufferMemoryBarriers, imageMemoryBarrierCount, pImageMemoryBarriers );
vkCmdWriteTimestamp( commandBuffer, pipelineStage, queryPool, query );

```

mjb – July 24, 2020

Potential Memory Race Conditions that Pipeline Barriers can Prevent 385

1. Write-then-Read (WtR) – the memory write in one operation starts overwriting the memory that another operation's read needs to use
2. Read-then-Write (RtW) – the memory read in one operation hasn't yet finished before another operation starts overwriting that memory
3. Write-then-Write (WtW) – two operations start overwriting the same memory and the end result is non-deterministic

Note: there is no problem with Read-then-Read (RtR) as no data has been changed

mjb - July 24, 2020

vkCmdPipelineBarrier() Function Call 386

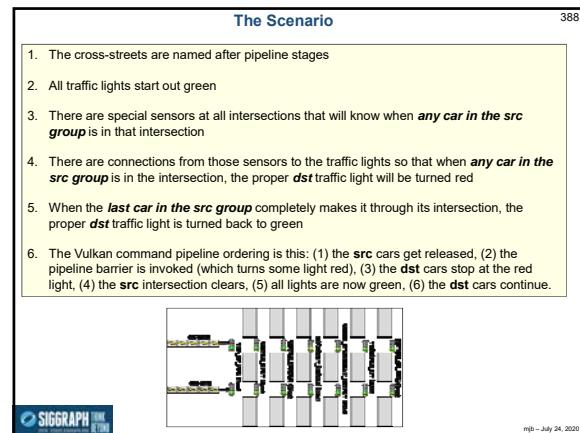
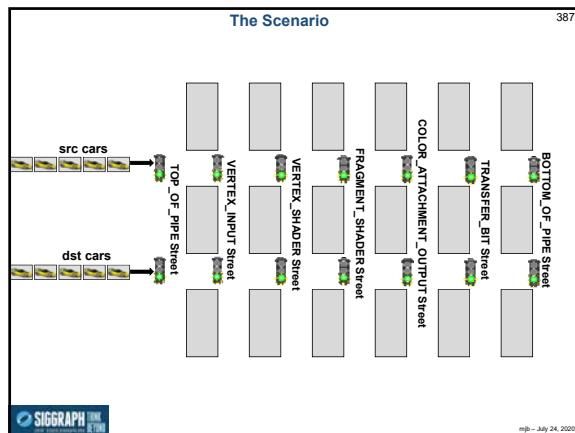
A Pipeline Barrier is a way to establish a memory dependency between commands that were submitted before the barrier and commands that are submitted after the barrier

```

vkCmdPipelineBarrier( commandBuffer,
    srcStageMask,           // Guarantee that this pipeline stage is completely done being used before ...
    dstStageMask,           // ... allowing this pipeline stage to be used
    VK_DEPENDENCY_BY_REGION_BIT,
    memoryBarrierCount,     // pMemoryBarriers,
    bufferMemoryBarrierCount, // pBufferMemoryBarriers,
    imageMemoryBarrierCount, // pImageMemoryBarriers
);
    
```

Defines what data we will be blocking on or un-blocking on

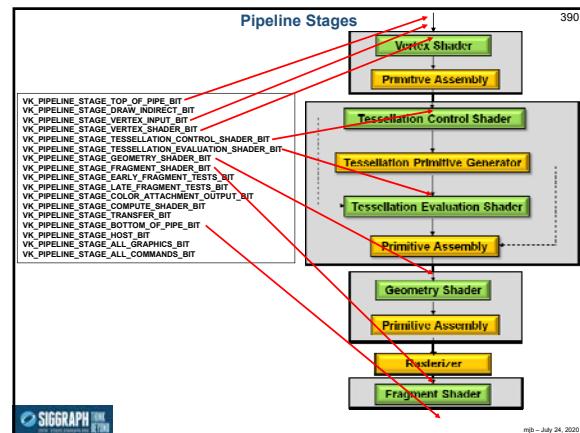
mjb - July 24, 2020



Pipeline Stage Masks – Where in the Pipeline is this Memory Data being Generated or Consumed? 389

VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT
VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT
VK_PIPELINE_STAGE_VERTEX_INPUT_BIT
VK_PIPELINE_STAGE_VERTEX_SHADER_BIT
VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT
VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT
VK_PIPELINE_STAGE_GEOMETRY_SHADER_BIT
VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT
VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT
VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT
VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT
VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT
VK_PIPELINE_STAGE_TRANSFER_BIT
VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT
VK_PIPELINE_STAGE_HOST_BIT
VK_PIPELINE_STAGE_ALL_GRAPHICS_BIT
VK_PIPELINE_STAGE_ALL_COMMANDS_BIT

mjb - July 24, 2020



Access Masks – What are you Interested in Generating or Consuming this Memory for?												
VK_ACCESS_INDIRECT_COMMAND_READ_BIT												
VK_ACCESS_INDEX_READ_BIT	•											
VK_ACCESS_VERTEX_ATTRIBUTE_READ_BIT		•										
VK_ACCESS_UNIFORM_READ_BIT			•									
VK_ACCESS_INPUT_ATTACHMENT_READ_BIT				•								
VK_ACCESS_SHADER_READ_BIT					•							
VK_ACCESS_SHADER_WRITE_BIT						•						
VK_ACCESS_COLOR_ATTACHMENT_READ_BIT							•					
VK_ACCESS_COLOR_ATTACHMENT_WRITE_BIT								•				
VK_ACCESS_DEPTH_STENCIL_ATTACHMENT_READ_BIT									•			
VK_ACCESS_DEPTH_STENCIL_ATTACHMENT_WRITE_BIT										•		
VK_ACCESS_TRANSFER_READ_BIT											•	
VK_ACCESS_TRANSFER_WRITE_BIT												•
VK_ACCESS_HOST_READ_BIT												
VK_ACCESS_HOST_WRITE_BIT												
VK_ACCESS_MEMORY_READ_BIT												
VK_ACCESS_MEMORY_WRITE_BIT												

mjb – July 24, 2020

Pipeline Stages and what Access Operations are Allowed												
VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT												
VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT	•											
VK_PIPELINE_STAGE_VERTEX_INPUT_BIT		•										
VK_PIPELINE_STAGE_VERTEX_SHADER_BIT			•									
VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT				•								
VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT					•							
VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT						•						
VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT							•					
VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT								•				
VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT									•			
VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT										•		
VK_PIPELINE_STAGE_TRANSFER_BIT											•	
VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT												•
VK_PIPELINE_STAGE_HOST_BIT												
VK_PIPELINE_STAGE_ALL_GRAPHICS_BIT												
VK_PIPELINE_STAGE_ALL_COMMANDS_BIT												

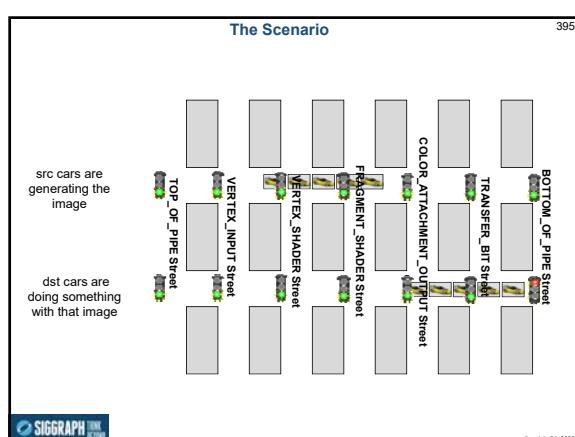
mjb – July 24, 2020

Access Operations and what Pipeline Stages they can be used In												
VK_ACCESS_INDIRECT_COMMAND_READ_BIT	•											
VK_ACCESS_INDEX_READ_BIT		•										
VK_ACCESS_VERTEX_ATTRIBUTE_READ_BIT			•									
VK_ACCESS_UNIFORM_READ_BIT				•								
VK_ACCESS_INPUT_ATTACHMENT_READ_BIT					•							
VK_ACCESS_SHADER_READ_BIT						•						
VK_ACCESS_SHADER_WRITE_BIT							•					
VK_ACCESS_COLOR_ATTACHMENT_READ_BIT								•				
VK_ACCESS_COLOR_ATTACHMENT_WRITE_BIT									•			
VK_ACCESS_DEPTH_STENCIL_ATTACHMENT_READ_BIT										•		
VK_ACCESS_DEPTH_STENCIL_ATTACHMENT_WRITE_BIT											•	
VK_ACCESS_TRANSFER_READ_BIT												•
VK_ACCESS_TRANSFER_WRITE_BIT												
VK_ACCESS_HOST_READ_BIT												
VK_ACCESS_HOST_WRITE_BIT												
VK_ACCESS_MEMORY_READ_BIT												
VK_ACCESS_MEMORY_WRITE_BIT												

mjb – July 24, 2020

Example: Be sure we are done writing an output image before using it for something else												
Stages												
VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT	src											
VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT		src										
VK_PIPELINE_STAGE_VERTEX_INPUT_BIT			src									
VK_PIPELINE_STAGE_VERTEX_SHADER_BIT				src								
VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT					src							
VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT						src						
VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT							src					
VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT								dst				
VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT									dst			
VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT										dst		
VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT											dst	
VK_PIPELINE_STAGE_TRANSFER_BIT												dst
VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT												
VK_PIPELINE_STAGE_HOST_BIT												
VK_PIPELINE_STAGE_ALL_GRAPHICS_BIT												
VK_PIPELINE_STAGE_ALL_COMMANDS_BIT												

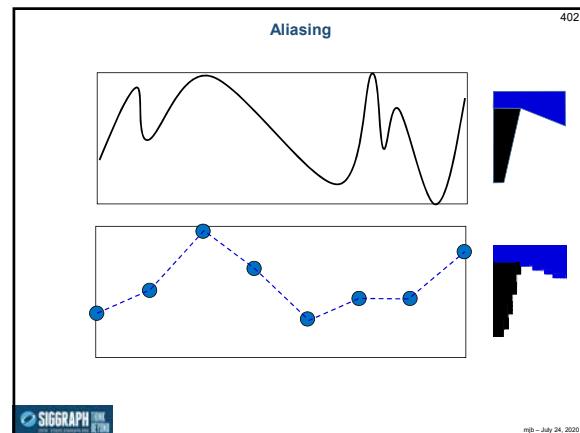
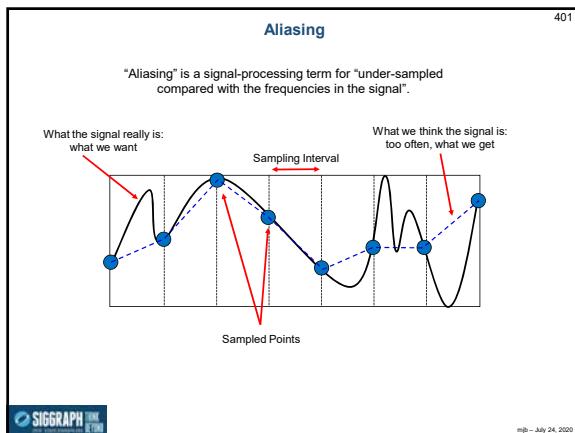
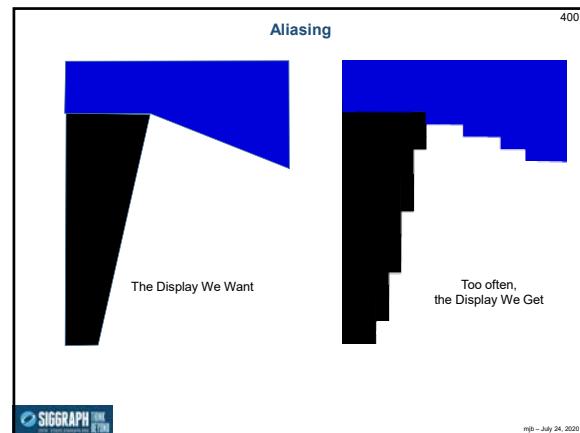
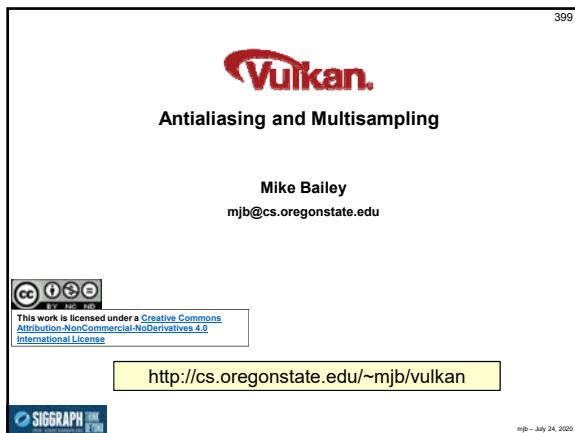
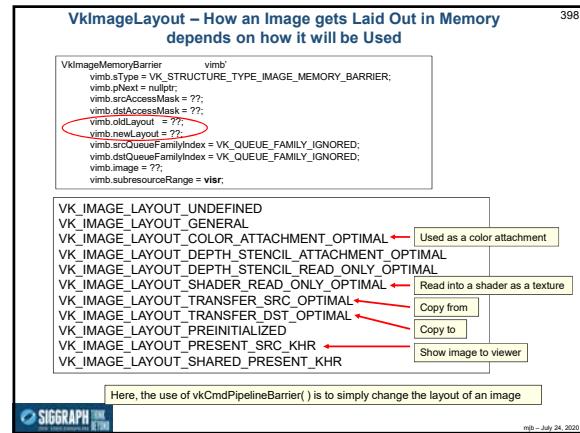
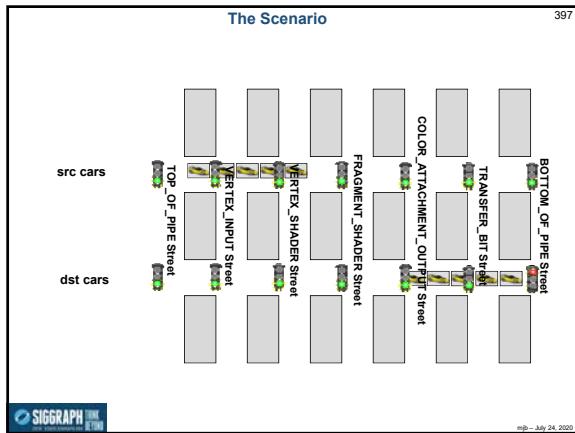
mjb – July 24, 2020



mjb – July 24, 2020

Example: Don't read a buffer back to the host until a shader is done writing it												
Stages												
VK_PIPELINE_STAGE_TOP_OF_PIPE_BIT	src											
VK_PIPELINE_STAGE_DRAW_INDIRECT_BIT		src										
VK_PIPELINE_STAGE_VERTEX_INPUT_BIT			src									
VK_PIPELINE_STAGE_VERTEX_SHADER_BIT				src								
VK_PIPELINE_STAGE_TESSELLATION_CONTROL_SHADER_BIT					src							
VK_PIPELINE_STAGE_TESSELLATION_EVALUATION_SHADER_BIT						src						
VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT							src					
VK_PIPELINE_STAGE_EARLY_FRAGMENT_TESTS_BIT								dst				
VK_PIPELINE_STAGE_LATE_FRAGMENT_TESTS_BIT									dst			
VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT										dst		
VK_PIPELINE_STAGE_COMPUTE_SHADER_BIT											dst	
VK_PIPELINE_STAGE_TRANSFER_BIT												dst
VK_PIPELINE_STAGE_BOTTOM_OF_PIPE_BIT												
VK_PIPELINE_STAGE_HOST_BIT												
VK_PIPELINE_STAGE_ALL_GRAPHICS_BIT												
VK_PIPELINE_STAGE_ALL_COMMANDS_BIT												

mjb – July 24, 2020



The Nyquist Criterion

"The Nyquist [sampling] rate is twice the maximum component frequency of the function [i.e., signal] being sampled." -- Wikipedia

403

SIGGRAPH

mjb – July 24, 2020

MultiSampling

Oversampling is a computer graphics technique to improve the quality of your output image by looking inside every pixel to see what the rendering is doing there.

There are two approaches to this:

- Supersampling:** Pick some number of sub-pixels within that pixel that pass the depth and stencil tests. Render the image at each of these sub-pixels..

One pixel

Sub-pixels

VK_SAMPLE_COUNT_8_BIT

- Multisampling:** Pick some number of sub-pixels within that pixel that pass the depth and stencil tests. If any of them pass, then render a single color for the one pixel and assign that single color to all the sub-pixels that passed the depth and stencil tests.

The final step will be to average those sub-pixels' colors to produce one final color for this whole pixel. This is called **resolving** the pixel.

404

SIGGRAPH

mjb – July 24, 2020

Vulkan Specification Distribution of Sampling Points within a Pixel

VK_SAMPLE_COUNT_1_8_BIT

VK_SAMPLE_COUNT_2_8_BIT

VK_SAMPLE_COUNT_4_8_BIT

VK_SAMPLE_COUNT_8_8_BIT

VK_SAMPLE_COUNT_16_8_BIT

405

SIGGRAPH

mjb – July 24, 2020

Vulkan Specification Distribution of Sampling Points within a Pixel

VK_SAMPLE_COUNT_2_BIT	VK_SAMPLE_COUNT_4_BIT	VK_SAMPLE_COUNT_8_BIT	VK_SAMPLE_COUNT_16_BIT
(0.25, 0.25)	(0.875, 0.375)	(0.3125, 0.1875)	(0.5625, 0.3125)
(0.375, 0.125)	(0.4375, 0.6875)	(0.3125, 0.625)	(0.4375, 0.3125)
(0.75, 0.75)	(0.125, 0.625)	(0.1875, 0.4375)	(0.75, 0.4375)
(0.625, 0.875)	(0.6875, 0.9375)	(0.625, 0.9375)	(0.625, 0.875)
	(0.9375, 0.9375)	(0.9375, 0.625)	(0.9375, 0.3125)
		(0.0, 0.5)	(0.0, 0.0)
		(0.0, 0.3125)	(0.0, 0.0)

406

SIGGRAPH

mjb – July 24, 2020

Consider Two Triangles Who Pass Through the Same Pixel

Let's assume (for now) that the two triangles don't overlap – that is, they look this way because they butt up against each other.

407

SIGGRAPH

mjb – July 24, 2020

Supersampling

VK_SAMPLE_COUNT_8_BIT

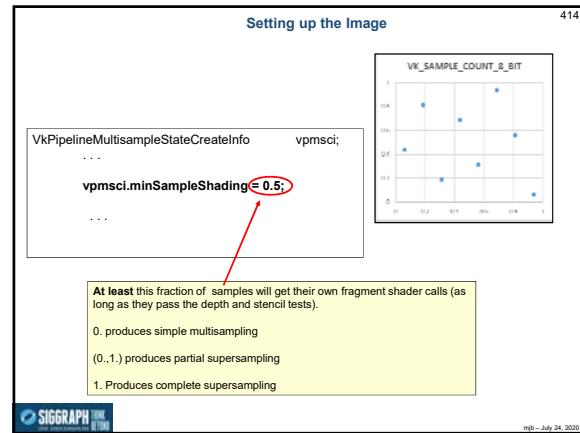
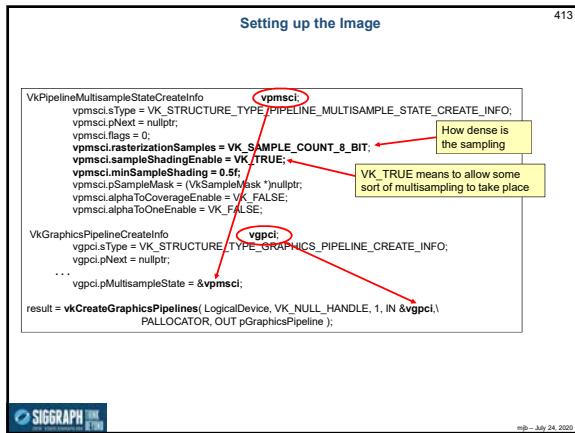
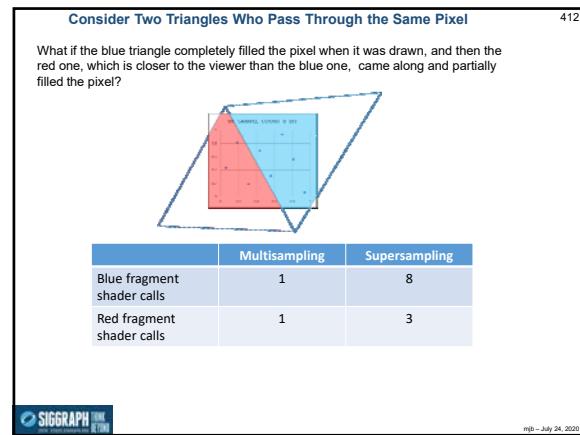
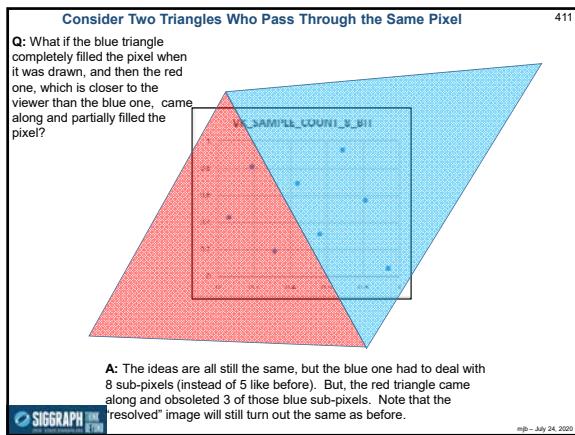
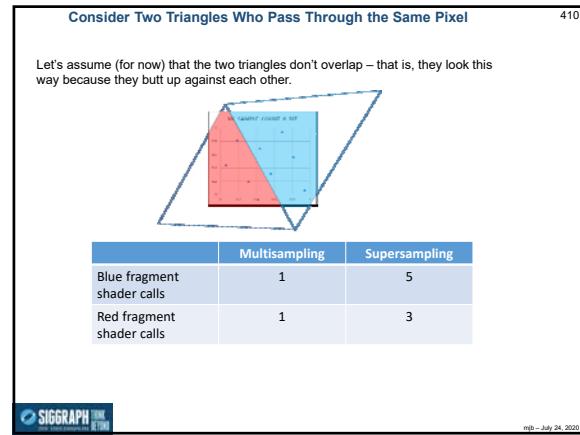
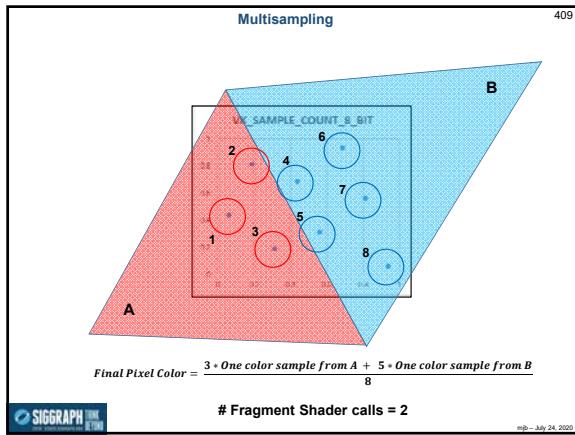
Final Pixel Color = $\frac{\sum_{i=1}^8 \text{Color sample from subpixel}_i}{8}$

Fragment Shader calls = 8

408

SIGGRAPH

mjb – July 24, 2020



Setting up the Image

```

VkAttachmentDescription
    vad[2].format = VK_FORMAT_B8G8R8A8_SRGB;
    vad[0].sampleCount = VK_SAMPLE_COUNT_8_BIT;
    vad[0].loadOp = VK_ATTACHMENT_LOAD_OP_CLEAR;
    vad[0].storeOp = VK_ATTACHMENT_STORE_OP_STORE;
    vad[0].stencilLoadOp = VK_ATTACHMENT_LOAD_OP_DONT_CARE;
    vad[0].stencilStoreOp = VK_ATTACHMENT_STORE_OP_DONT_CARE;
    vad[0].initialLayout = VK_IMAGE_LAYOUT_UNDEFINED;
    vad[0].finalLayout = VK_IMAGE_LAYOUT_PRESENT_SRC_KHR;
    vad[0].flags = 0;

    vad[1].format = VK_FORMAT_D24_UNORM_S8_UINT;
    vad[1].sampleCount = VK_SAMPLE_COUNT_8_BIT;
    vad[1].loadOp = VK_ATTACHMENT_LOAD_OP_CLEAR;
    vad[1].storeOp = VK_ATTACHMENT_STORE_OP_DONT_CARE;
    vad[1].stencilLoadOp = VK_ATTACHMENT_LOAD_OP_DONT_CARE;
    vad[1].stencilStoreOp = VK_ATTACHMENT_STORE_OP_DONT_CARE;
    vad[1].initialLayout = VK_IMAGE_LAYOUT_UNDEFINED;
    vad[1].finalLayout = VK_IMAGE_LAYOUT_DEPTH_STENCIL_ATTACHMENT_OPTIMAL;
    vad[1].flags = 0;

VkAttachmentReference colorReference;
colorReference.attachment = 0;
colorReference.layout = VK_IMAGE_LAYOUT_COLOR_ATTACHMENT_OPTIMAL;

VkAttachmentReference depthReference;
depthReference.attachment = 1;
depthReference.layout = VK_IMAGE_LAYOUT_DEPTH_STENCIL_ATTACHMENT_OPTIMAL;

```

mjb - July 24, 2020

Setting up the Image

```

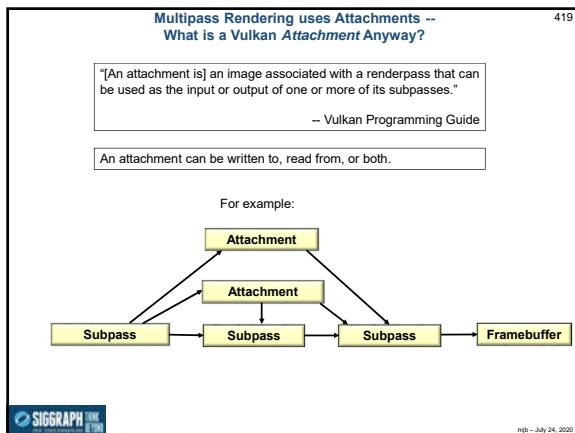
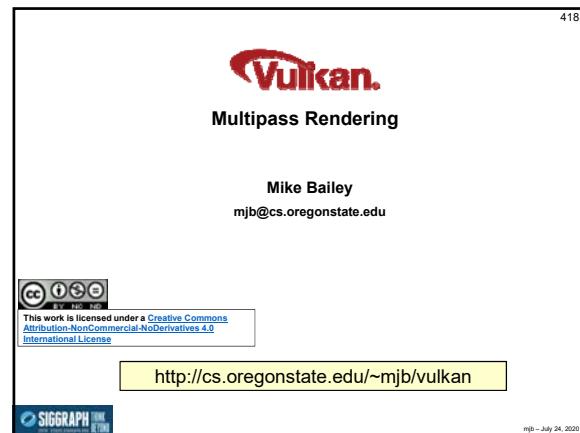
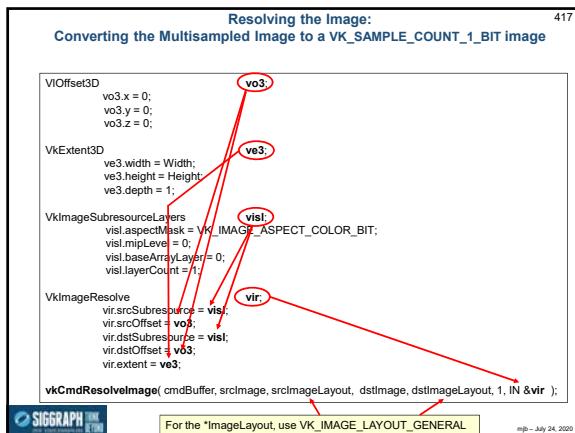
VkSubpassDescription
    vsd.flags = 0;
    vsd.pipelineBindPoint = VK_PIPELINE_BIND_POINT_GRAPHICS;
    vsd.inputAttachmentCount = 0;
    vsd.pColorAttachments = VK_ATTACHMENT_REFERENCE *nullptr;
    vsd.colorAttachmentCount = 0;
    vsd.pResolveAttachments = &colorReference;
    vsd.pDepthStencilAttachment = &depthReference;
    vsd.preserveAttachmentCount = 0;
    vsd.pPreserveAttachments = (uint32_t *)nullptr;

VkRenderPassCreateInfo
    vrpci.sType = VK_STRUCTURE_TYPE_RENDER_PASS_CREATE_INFO;
    vrpci.pNext = nullptr;
    vrpci.flags = 0;
    vrpci.attachmentCount = 2; // color and depth/stencil
    vrpci.pAttachments = vad;
    vrpci.subpassCount = 1;
    vrpci.pSubpasses = &vsd;
    vrpci.dependencyCount = 0;
    vrpci.pDependencies = (VkSubpassDependency *)nullptr;

result = vkCreateRenderPass(LogicalDevice, &vrpci, PALLOCATOR, OUT &RenderPass);

```

mjb - July 24, 2020



420

What is an Example of Wanting to do This?

There is a process in computer graphics called **Deferred Rendering**. The idea is that a game-quality fragment shader takes a long time (relatively) to execute, but, with all the 3D scene detail, a lot of the rendered fragments are going to get z-buffered away anyhow. So, why did we invoke the fragment shaders so many times when we didn't need to?

Here's the trick:

Let's create a grossly simple fragment shader that writes out (into multiple framebuffers) each fragment's:

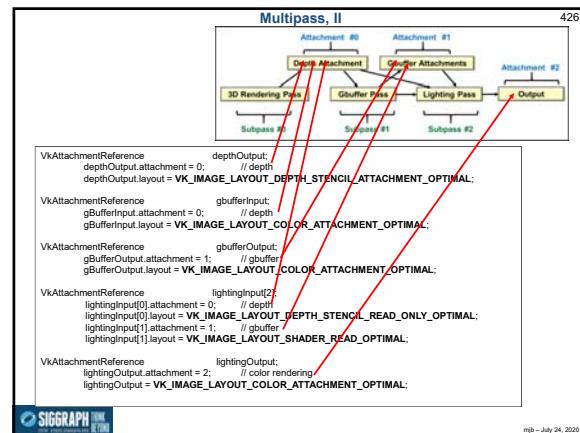
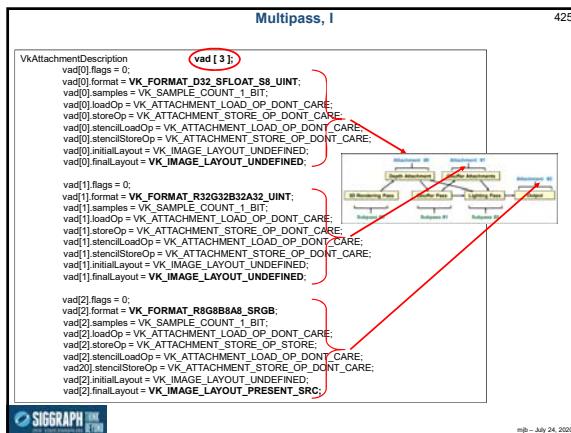
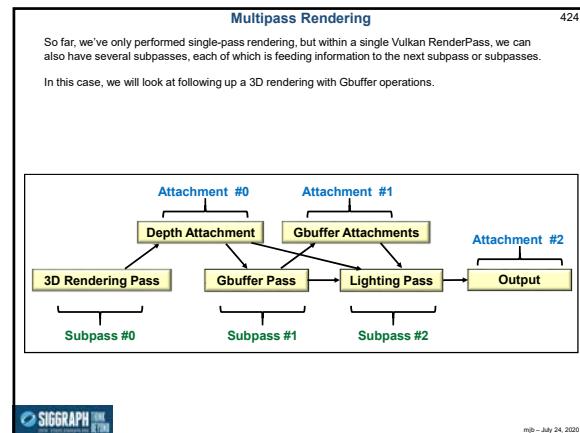
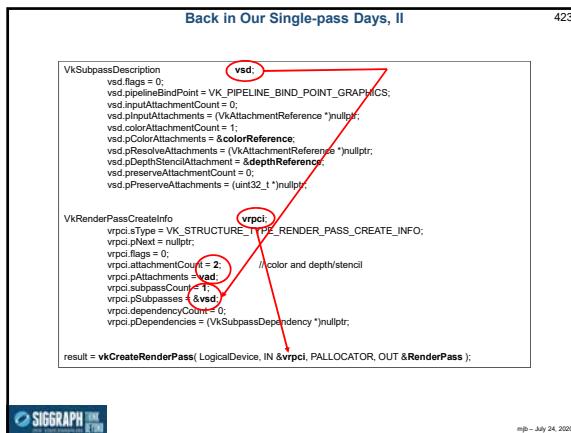
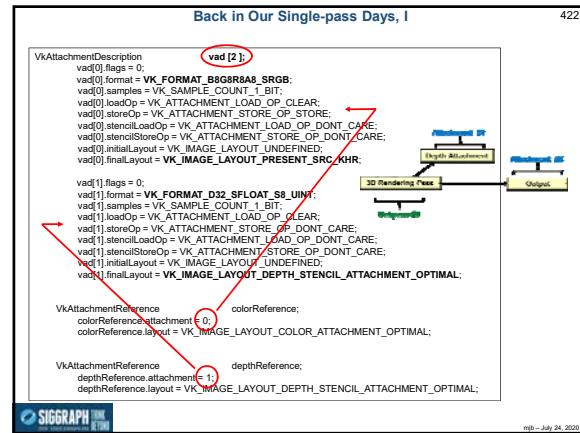
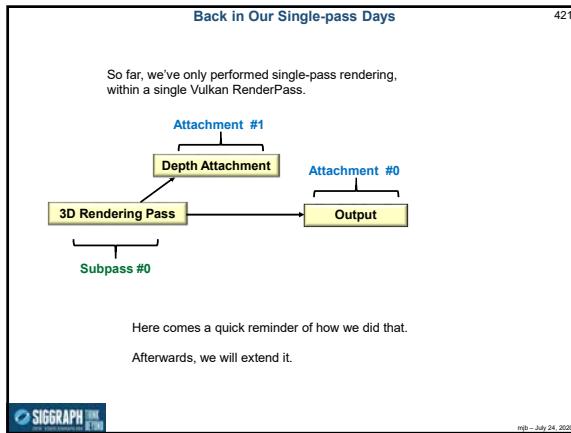
- position (x,y,z)
- normal (nx,ny,nz)
- material color (r,g,b)
- texture coordinates (s,t)

As well as:

- the current light source positions and colors
- the current eye position

When we write these out, the final framebuffers will contain just information for the pixels that *can be seen*. We then make a second pass running the expensive lighting model *just* for those pixels. This known as the **G-buffer Algorithm**.

mjb - July 24, 2020



Multipass, III

```

VkSubpassDescription vsd[3];
vsd[0].flags = 0;
vsd[0].pipelineBindPoint = VK_PIPELINE_BIND_POINT_GRAPHICS;
vsd[0].inputAttachmentCount = 0;
vsd[0].colorAttachmentCount = 0;
vsd[0].pColorAttachments = &VAttachmentReference *nullptr;
vsd[0].pResolveAttachments = &VAttachmentReference *nullptr;
vsd[0].pDepthStencilAttachment = &depthOutput;
vsd[0].preserveAttachmentCount = 0;
vsd[0].pPreserveAttachments = (uint32_t *)nullptr;

vsd[1].flags = 0;
vsd[1].pipelineBindPoint = VK_PIPELINE_BIND_POINT_GRAPHICS;
vsd[1].inputAttachmentCount = 0;
vsd[1].colorAttachmentCount = 1;
vsd[1].pColorAttachments = &gBufferOutput;
vsd[1].pResolveAttachments = &VAttachmentReference *nullptr;
vsd[1].pDepthStencilAttachment = &VAttachmentReference *nullptr;
vsd[1].preserveAttachmentCount = 0;
vsd[1].pPreserveAttachments = (uint32_t *)nullptr;

vsd[2].flags = 0;
vsd[2].pipelineBindPoint = VK_PIPELINE_BIND_POINT_GRAPHICS;
vsd[2].inputAttachmentCount = 0;
vsd[2].colorAttachmentCount = 1;
vsd[2].pColorAttachments = &lightingInput[0];
vsd[2].pResolveAttachments = &VAttachmentReference *nullptr;
vsd[2].pDepthStencilAttachment = &VAttachmentReference *nullptr;
vsd[2].preserveAttachmentCount = 0;
vsd[2].pPreserveAttachments = (uint32_t *)nullptr;

```



mjb - July 24, 2020

Multipass, IV

```

VkSubpassDependency vsdp[2];
vsdp[0].srcSubpass = 0; // depth rendering →
vsdp[0].dstSubpass = 1; // → gbuffer
vsdp[0].srcStageMask = VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT;
vsdp[0].dstStageMask = VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT;
vsdp[0].srcAccessMask = VK_ACCESS_COLOR_ATTACHMENT_WRITE_BIT;
vsdp[0].dstAccessMask = VK_ACCESS_SHADER_READ_BIT;
vsdp[0].dependencyFlags = VK_DEPENDENCY_BY_REGION_BIT;

vsdp[1].srcSubpass = 1; // gbuffer →
vsdp[1].dstSubpass = 2; // → color output
vsdp[1].srcStageMask = VK_PIPELINE_STAGE_COLOR_ATTACHMENT_OUTPUT_BIT;
vsdp[1].dstStageMask = VK_PIPELINE_STAGE_FRAGMENT_SHADER_BIT;
vsdp[1].srcAccessMask = VK_ACCESS_COLOR_ATTACHMENT_WRITE_BIT;
vsdp[1].dstAccessMask = VK_ACCESS_SHADER_READ_BIT;
vsdp[1].dependencyFlags = VK_DEPENDENCY_BY_REGION_BIT;

```

Notice how similar this is to creating a **Directed Acyclic Graph (DAG)**.



mjb - July 24, 2020

Multipass, V

```

vkRenderPassCreateInfo rrpcl;
rrpcl.sType = VK_STRUCTURE_TYPE_RENDER_PASS_CREATE_INFO;
rrpcl.pNext = nullptr;
rrpcl.flags = 0;
rrpcl.attachmentCount = 3; // depth, gbuffer, output
rrpcl.pAttachments = vad;
rrpcl.subpassCount = 3;
rrpcl.pSubpasses = vad;
rrpcl.dependencyCount = 2;
rrpcl.pDependencies = vsdp;

result = vkCreateRenderPass( LogicalDevice, IN &rrpcl, PALLOCATOR, OUT &RenderPass );

```



mjb - July 24, 2020

Multipass, VI

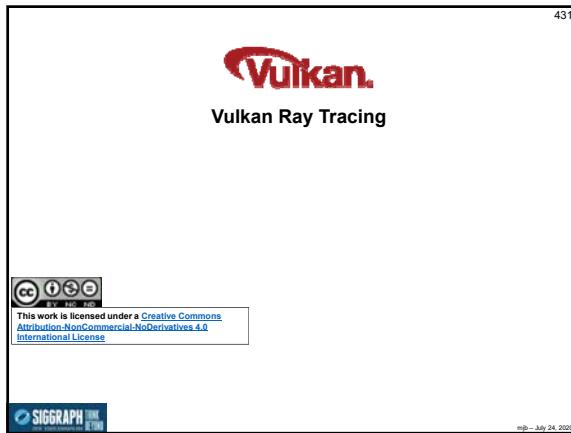
```

vkCmdBeginRenderPass( CommandBuffers[nextImageIndex], IN &vrpb, IN VK_SUBPASS_CONTENTS_INLINE );
// subpass #0 is automatically started here
vkCmdBindPipeline( CommandBuffers[nextImageIndex], VK_PIPELINE_BIND_POINT_GRAPHICS, GraphicsPipeline );
vkCmdBindDescriptorSets( CommandBuffers[nextImageIndex], VK_PIPELINE_BIND_POINT_GRAPHICS,
                        GraphicsPipelineLayout, 0, 4, DescriptorSets, 0, (uint32_t *)nullptr );
vkCmdBindVertexBuffers( CommandBuffers[nextImageIndex], 0, 1, vBuffers, offsets );
vkCmdDraw( CommandBuffers[nextImageIndex], vertexCount, firstVertex, firstInstance );
vkCmdNextSubpass(CommandBuffers[nextImageIndex], VK_SUBPASS_CONTENTS_INLINE );
// subpass #1 is started here
vkCmdNextSubpass(CommandBuffers[nextImageIndex], VK_SUBPASS_CONTENTS_INLINE );
// subpass #2 is started here
vkCmdEndRenderPass( CommandBuffers[nextImageIndex] );

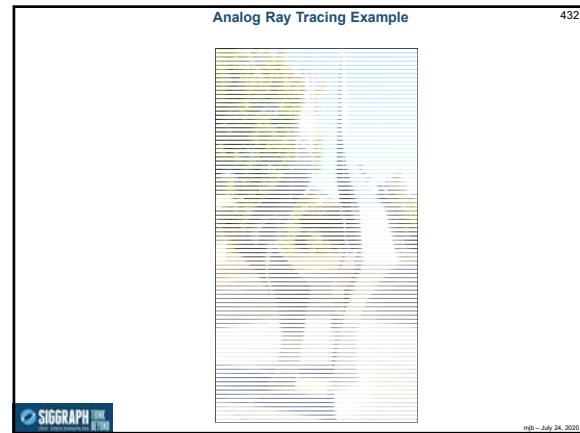
```



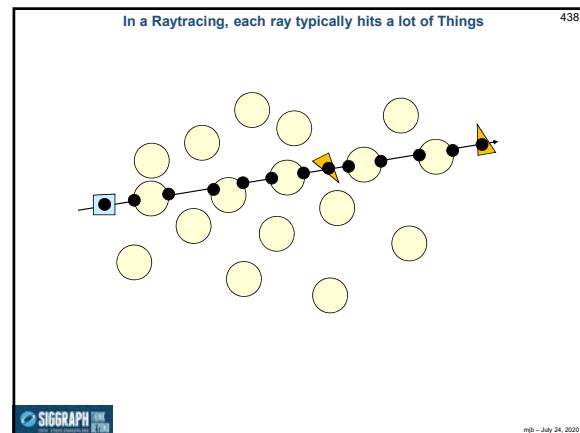
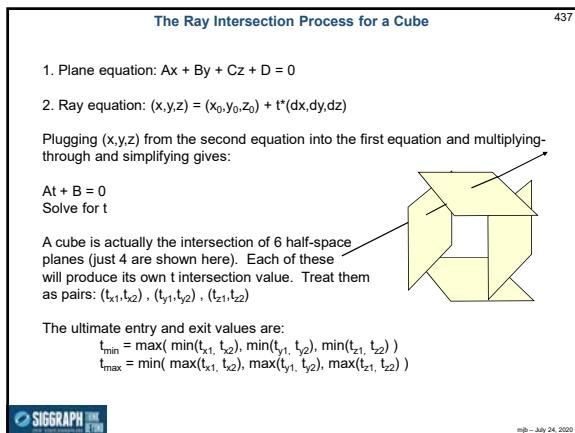
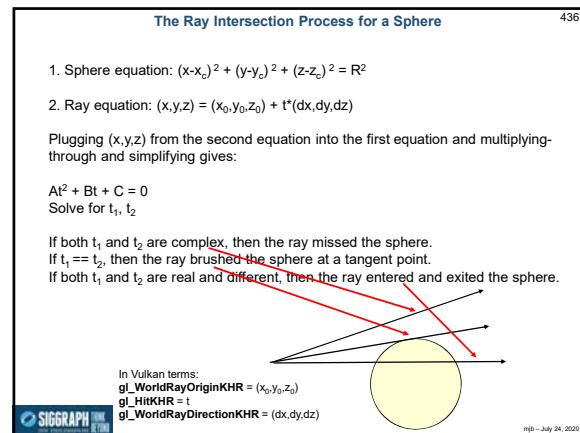
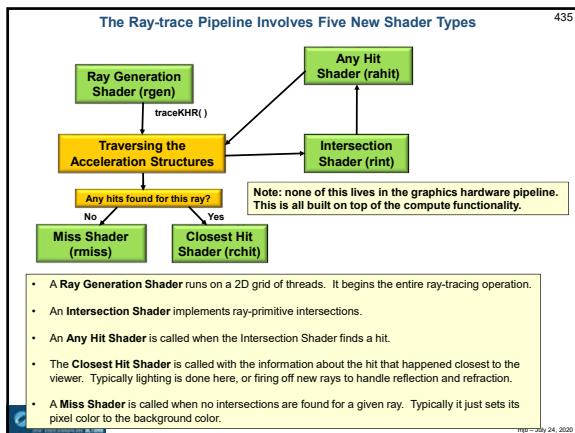
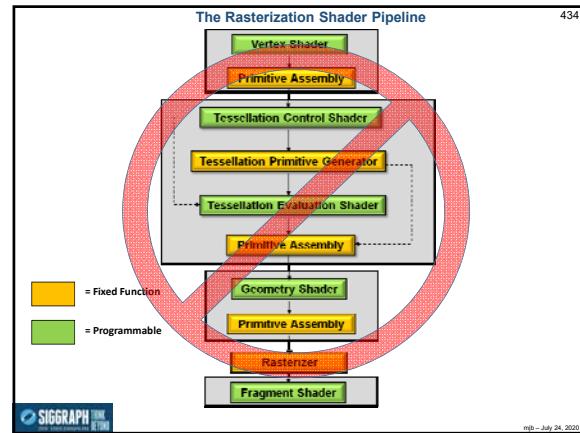
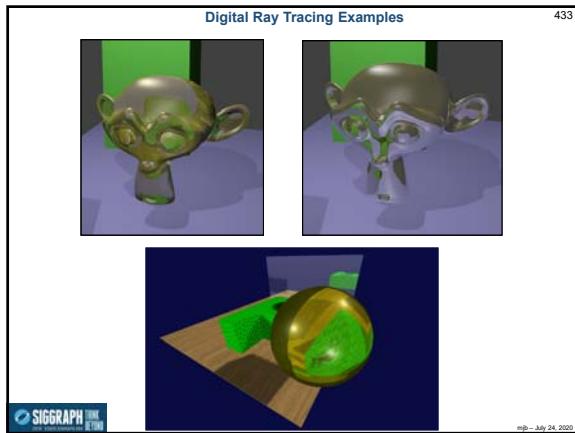
mjb - July 24, 2020



mjb - July 24, 2020



mjb - July 24, 2020



Acceleration Structures 439

- Bottom-level Acceleration Structure (BLAS) holds the vertex data and is built from vertex and index VKBuffers
- The BLAS can also hold transformations, but it looks like usually the BLAS holds vertices in the original Model Coordinates.
- Top-level Acceleration Structure (TLAS) holds a pointer to elements of the BLAS and a transformation.
- The BLAS is used as a Model Coordinate bounding box.
- The TLAS is used as a World Coordinate bounding box.
- A TLAS can instance multiple BLAS's.

Creating Bottom Level Acceleration Structures 440

```
vkCreateAccelerationStructureKHR
    vasi.sType = VK_ACCELERATION_STRUCTURE_TYPE_BOTTOM_LEVEL_KHR;
    vasi.pNext = nullptr;
    vasi.instanceCount = 0;
    vasi.geometryCount = << number of vertex buffers >>;
    vasi.pGeometries = << vertex buffer pointers >>;
    vasci.sType = VK_STRUCTURE_TYPE_ACCELERATION_STRUCTURE_CREATE_INFO_KHR;
    vasci.pNext = nullptr;
    vasci.info = &vasi;
    vasci.compactedSize = 0;
result = vkCreateAccelerationStructureKHR(LogicalDevice, IN &vasi, PALLOCATOR, OUT &BottomLevelAccelerationStructure);
```

Creating Top Level Acceleration Structures 441

```
vkCreateAccelerationStructureKHR
    topLevelAccelerationStructure;
    VkaAccelerationStructureInfoKHR
        vasi.sType = VK_ACCELERATION_STRUCTURE_TYPE_TOP_LEVEL_KHR;
        vasi.flags = 0;
        vasi.pNext = nullptr;
        vasi.instanceCount = << number of bottom level acceleration structure instances >>;
        vasi.geometryCount = 0;
        vasi.pGeometries = VK_NULL_HANDLE;
    VkaAccelerationStructureCreateInfoKHR
        vasci.sType = VK_STRUCTURE_TYPE_ACCELERATION_STRUCTURE_CREATE_INFO_KHR;
        vasci.pNext = nullptr;
        vasci.info = &vasi;
        vasci.compactedSize = 0;
result = vkCreateAccelerationStructureKHR(LogicalDevice, &vasci, PALLOCATOR, &topLevelAccelerationStructure);
```

Ray Generation Shader 442

Gets all of the rays going and writes the final color to the pixel

```
layout( location = 1 ) rayPayloadKHR myPayLoad
{
    vec4 color;
}

void main( )
{
    traceKHR( topLevel, ... );
    imageStore( framebuffer, gl_GlobalInvocationIDKHR.xy, color );
}
```

A "payload" is information that keeps getting passed through the process. Different stages can add to it. It is finally consumed at the very end, in this case by writing color into the pixel being worked on.

A New Built-in Function 443

```
void traceKHR
(
    accelerationStructureKHR topLevel,
    uint rayFlags,
    culMask,
    uint sbtRecordOffset,
    uint sbtRecordStride,
    uint missIndex,
    vec3 origin,
    float tmin,
    float tmax,
    int payload
);
```

In Vulkan terms:
`gl_WorldRayOriginKHR = (x0,y0,z0)`
`gl_HitKHR = t`
`gl_WorldRayDirectionKHR = (dx,dy,dz)`

Intersection Shader 444

Intersect a ray with an arbitrary 3D object.
 Passes data to the Any Hit shader.
 There is a built-in ray-triangle Intersection Shader.

```
hitAttributeKHR vec3 attrs
void main( )
{
    SpherePrimitive sph = spheres[ gl_PrimitiveID ];
    vec3 org = gl_WorldRayOriginKHR;
    vec3 dir = normalize( gl_WorldRayDirectionKHR );
    ...
    float discr = b*b - 4.*a*c;
    if( discr < 0. ) return;
    float tmp = (-b + sqrt(discr)) / (2.*a);
    if( gl_RayMinKHR < tmp && tmp < gl_RayTmaxKHR )
    {
        vec3 p = org + tmp * dir;
        attrs = p;
        reportIntersectionKHR( tmp, 0 );
        return;
    }
    tmp = (-b + sqrt(discr)) / (2.*a);
    if( gl_RayMinKHR < tmp && tmp < gl_RayTmaxKHR )
    {
        vec3 p = org + tmp * dir;
        attrs = p;
        reportIntersectionKHR( tmp, 0 );
        return;
    }
}
```

Miss Shader 445

Handle a ray that doesn't hit any objects.

```
rayPayloadKHR myPayLoad
{
    vec4 color;
};

void main()
{
    color = vec4(0., 0., 0., 1.);
}
```

mjb - July 24, 2020

Any Hit Shader 446

Handle a ray that hits *anything*. Store information on each hit. Can reject a hit.

```
layout( binding = 4, set = 0 ) buffer outputProperties
{
    float outputValues[ ];
} outputData;

layout(location = 0) rayPayloadInKHR uint outputId;
layout(location = 1) rayPayloadInKHR uint hitCounter;
hitAttributeKHR vec 3 attribs;

void main()
{
    outputData.outputValues[ outputId + hitCounter ] = gl_PrimitiveID;
    hitCounter = hitCounter + 1;
}
```

mjb - July 24, 2020

Closest Hit Shader 447

Handle the intersection closest to the viewer. Collects data from the Any Hit shader. Can spawn more rays.

```
rayPayloadKHR myPayLoad
{
    vec4 color;
};

void main()
{
    vec3 stp = gl_WorldRayOriginKHR + gl_HitKHR * gl_WorldRayDirectionKHR;
    color = texture( MaterialUnit, stp ); // material properties lookup
}
```

In Vulkan terms:
 $gl_WorldRayOriginKHR = (x_0, y_0, z_0)$
 $gl_HitKHR = t$
 $gl_WorldRayDirectionKHR = (dx, dy, dz)$

mjb - July 24, 2020

Other New Built-in Functions 448

Loosely equivalent to "discard".

```
void terminateRayKHR();
void ignoreIntersectionKHR();
void reportIntersectionKHR( float hit, uint hitKind );
```

mjb - July 24, 2020

Ray Trace Pipeline Data Structure 449

```
VkPipeline RaytracePipeline;
VkPipelineLayout PipelineLayout;

VkPipelineLayoutCreateInfo vplci;
vplci.sType = VK_STRUCTURE_TYPE_PIPELINE_LAYOUT_CREATE_INFO;
vplci.pNext = nullptr;
vplci.flags = 0;
vplci.setLayoutCount = 1;
vplci.pSetLayouts = &descriptorSetLayout;
vplci.pushConstantRangeCount = 0;
vplci.pushConstantRanges = nullptr;
result = vkCreatePipelineLayout( LogicalDevice, IN &vplci, nullptr, OUT &PipelineLayout );

VkRayTracingPipelineCreateInfoKHR vrtpci;
vrtpci.sType = VK_STRUCTURE_TYPE_RAY_TRACING_PIPELINE_CREATE_INFO_KHR;
vrtpci.pNext = nullptr;
vrtpci.flags = 0;
vrtpci.stageCount = << # of shader stages in the ray-trace pipeline >>;
vrtpci.pStages = << what those shader stages are >>;
vrtpci.maxRecursionDepth = << how many recursion layers deep the ray tracing is allowed to go >>;
vrtpci.layout = PipelineLayout;
vrtpci.basePipelineHandle = VK_NULL_HANDLE;
vrtpci.basePipelineIndex = 0;
result = vkCreateRayTracingPipelinesKHR( LogicalDevice, PALLOCATOR, 1, IN &vrtpci, nullptr, OUT &RaytracePipeline );
```

mjb - July 24, 2020

The Trigger comes from the Command Buffer: vkCmdBindPipeline() and vkCmdTraceRaysKHR() 450

```
vkCmdBindPipeline( CommandBuffer, VK_PIPELINE_BIND_POINT_RAYTRACING_KHR, RaytracePipeline );
vkCmdTraceRaysKHR( CommandBuffer,
    raygenShaderBindingTableBuffer, raygenShaderBindingOffset,
    missShaderBindingTableBuffer, missShaderBindingOffset,
    hitShaderBindingTableBuffer, hitShaderBindingOffset,
    callableShaderBindingTableBuffer, callableShaderBindingOffset,
    callableShaderBindingStride,
    width, height, depth );
```

mjb - July 24, 2020



452

Vulkan.

Introduction to the Vulkan Computer Graphics API

Mike Bailey
mjb@cs.oregonstate.edu

This work is licensed under a [Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International License](#)

<http://cs.oregonstate.edu/~mjb/vulkan>

FULL.pptx

mjb – July 24, 2020

The slide is titled "Vulkan." and "Introduction to the Vulkan Computer Graphics API". It features the SIGGRAPH 2019 logo and the Oregon State University Computer Graphics logo. The author is listed as Mike Bailey with the email mjb@cs.oregonstate.edu. A Creative Commons license notice is present. The URL <http://cs.oregonstate.edu/~mjb/vulkan> is highlighted in a yellow box. The slide is identified as number 452 and was created on July 24, 2020.