A Note From the Editors

DEAR TCAS-II Reader,

We are very pleased to see the coming to fruition of the efforts of compiling the first Special Issue of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—II: EXPRESS BRIEFS (TCAS-II) after the reorganization which took place in January 2004, that is since TCAS-II became a vehicle for the rapid diffusion of research results in this period of rapid technological progress.

Because of the specific scope of the journal, the model and structure of this Special Issue (and its companion which will appear in April 2008) differs with respect to similar initiatives in other TRANSACTIONS sponsored by the Circuits and Systems Society (CAS-S) and, to the best of our knowledge, in all IEEE publications. More specifically:

- contributions are in brief format (5 pages) only and target a readership of experts in the topic. In other words, we aim to bring to our readers the same excitement that they can experience by following an invited session in one of the most prestigious IEEE conferences;
- the targeted publication date is 6–7 months after the deadline for manuscript submission.

We are, therefore, extremely grateful to the Guest Editors of this 2-part Special Issue on “Multifunctional Circuits and Systems for Future Generation of Wireless Communications.” Dr. Aleksandar Tasic (Qualcomm), Dr. Wouter A. Serdijn (TU Delft), and Dr. Lawrence E. Larson (UCSD), for their continuous efforts during the extremely tight time period of the review process. Our gratitude needs to be extended also to the IEEE staff, Mona Mittra and Anuradha Gupte, for their work around the clock to make the March and April Special Issues available on time.

We are also convinced that this new model of the Special Issue will be appreciated by our readership and hope that these will be only the first examples of many. We would like to take this opportunity to solicit new proposals for special issues addressing the latest advancements pertaining to current or expanding fields of interest in the IEEE Circuits and Systems Society (CAS-S).

These can be submitted to the TCAS-II Editor-in-Chief, Prof. Un-Ku Moon, to the address given below.

Happy reading!

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Gianluca Setti (S’89–M’91–SM’02–F’06) received the Dr.Eng. degree (with honors) in electronic engineering and the Ph.D. degree in electronic engineering and computer science from the University of Bologna, Bologna, Italy, in 1992 and in 1997, respectively, for his contribution to the study of neural networks and chaotic systems.

From May 1994 to July 1995, he was with the Laboratory of Nonlinear Systems (LANOS), Swiss Federal Institute of Technology in Lausanne (EPFL), Lausanne, Switzerland, as a Visiting Researcher. Since 1997, he has been with the School of Engineering at the University of Ferrara, Ferrara, Italy, where he is currently an Associate Professor of Circuit Theory and Analog Electronics. His research interests include nonlinear circuits, recurrent neural networks, implementation and application of chaotic circuits and systems, statistical signal processing, electromagnetic compatibility, wireless communications and sensor networks.

Dr. Setti received the 1998 Caianiello prize for the best Italian Ph.D. thesis on Neural Networks and he is co-recipient of the 2004 IEEE Circuits and Systems Society (CAS-S) Darlington Award, as well as of the Best Paper award at ECCSTD2005 and the Best Student Paper Award at EMCZurich2005. He served as an Associate Editor of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: REGULAR PAPERS (1999–2002 and 2002–2004) and of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—II: EXPRESS BRIEFS (2004–2007), the Deputy Editor-in-Chief of the IEEE Circuits and Systems Magazine (2004–2007), and as the Editor-in-Chief of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—II: EXPRESS BRIEFS (2006–2007). Since January 2008, he is serving as the Editor-in-Chief of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: REGULAR PAPERS. He was the 2004 Chair of the Technical Committee on Nonlinear Circuits and Systems of the IEEE Circuits and Systems Society.
Un-Ku Moon (S’92–M’94–SM’99) received the B.S. degree from the University of Washington, Seattle, in 1987, the M.Eng. degree from Cornell University, Ithaca, NY, in 1989, and the Ph.D. degree from the University of Illinois at Urbana-Champaign, in 1994, all in electrical engineering.

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Prof. Moon is a recipient of the National Science Foundation CAREER Award and the Oregon State University’s Excellence in Graduate Mentoring Award. He has served as an Associate Editor of the IEEE Journal of Solid-State Circuits, an Associate Editor of the IEEE Transactions on Circuits and Systems—II: Analog and Digital Signal Processing, and on the Technical Program Committee of the IEEE Custom Integrated Circuits Conference. He currently serves as the Editor-in-Chief of the IEEE Transactions on Circuits and Systems—II: Express Briefs, and on the Technical Program Committee of the IEEE VLSI Circuits Symposium and the Analog Signal Processing Technical Committee of the IEEE Circuits and Systems Society. He also serves on the IEEE Solid-State Circuits Society (SSCS) Administrative Committee (AdCom) and the IEEE Circuits and Systems Society (CASS) Board of Governors (BoG) as the SSCS representative to CASS.

Systems of the of the IEEE CAS-S, a Distinguished Lecturer (2004–2005) and a member of the Board of Governors (since 2005) of the same society. He was also the Technical Program Co-Chair of NDES2000 (Catania, Italy) the Track Chair for Nonlinear Circuits and Systems of ISCAS2004 (Vancouver, BC, Canada), the Special Sessions Co-Chair of ISCAS2005 (Kobe, Japan) and ISCAS2006 (Kos, Greece), the Technical Program Co-Chair of ISCAS2007 (New Orleans, LA) and ISCAS2008 (Seattle), as well as the General Co-Chair of NOLTA2006 (Bologna, Italy). He is Co-editor of the book Chaotic Electronics in Telecommunications (CRC Press, 2000) and one of the Guest Editors of the May 2002 Special Issue of the Proceedings of the IEEE on Applications of Nonlinear Dynamics to Electronic and Information Engineering.